
**Information technology — Fibre
channel —**

**Part 246:
Backbone — 6 (FC-BB-6)**

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Foreword

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Introduction

FC-BB-6 defines mappings for transporting Fibre Channel over different network technologies. FC-BB-6 defines four distinct Fibre Channel mappings:

- a) FC over TCP/IP;
- b) FC over GFPT;
- c) FC over MPLS; and
- d) FC over Ethernet.

The FC over ATM and FC over SONET backbone mappings are not specified in FC-BB-6. As such, FC-BB-6 is not a complete replacement of FC-BB-3 (i.e., see FC-BB-3 for the specification of the FC over ATM and FC over SONET backbone mappings).

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Information technology — Fibre channel —

Part 246: Backbone — 6 (FC-BB-6)

1 Scope

This standard consists of distinct Fibre Channel mappings resulting in the following models:

- FC-BB_IP (FC over TCP/IP backbone network)
- Transparent FC-BB consisting of:
 - FC-BB_GFPT (FC over SONET/SDH/OTN/PDH backbone network using GFPT adaptation)
 - FC-BB_PW (FC over MPLS network using PW adaptation)
- FC-BB_E (FC over Ethernet)

Figure 1, Figure 2, Figure 3, and Figure 4 illustrate the scope and the major components of the FC-BB-6 models and its relationship to the IETF, ITU-T, and IEEE standards. Table 1 shows the organization of this standard. FC-BB_IP, Transparent FC-BB, and FC-BB_E do not interoperate in any manner and are independent models.

Table 1 – FC-BB-6 organization

Model Type	Applicable Clauses and Annexes
FC-BB_IP, FC-BB_GFPT, FC-BB_PW, FC-BB_E	1, 2, 3, 4
FC-BB_IP	5
Transparent FC-BB	
FC-BB_GFPT	6, Annex A
FC-BB_PW	6
FC-BB_E	7, Annex B, Annex C, Annex D, Annex E, Annex Annex F: , Annex G

The scope and components of the FC-BB_IP model is shown in Figure 1.

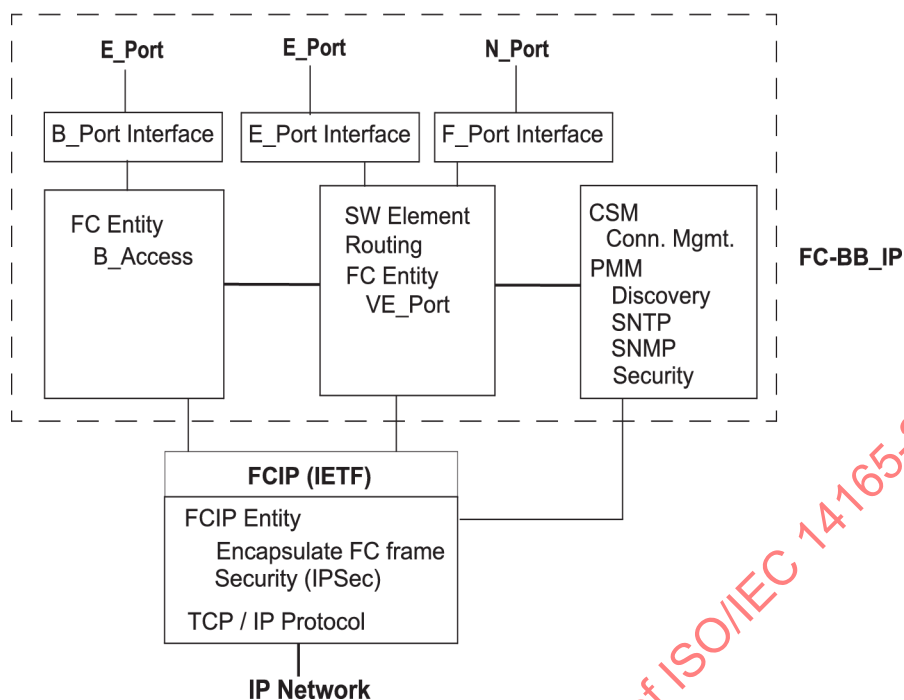


Figure 1 – Scope and components of FC-BB_IP model

The scope and components of the FC-BB_GFPT model is shown in Figure 2.

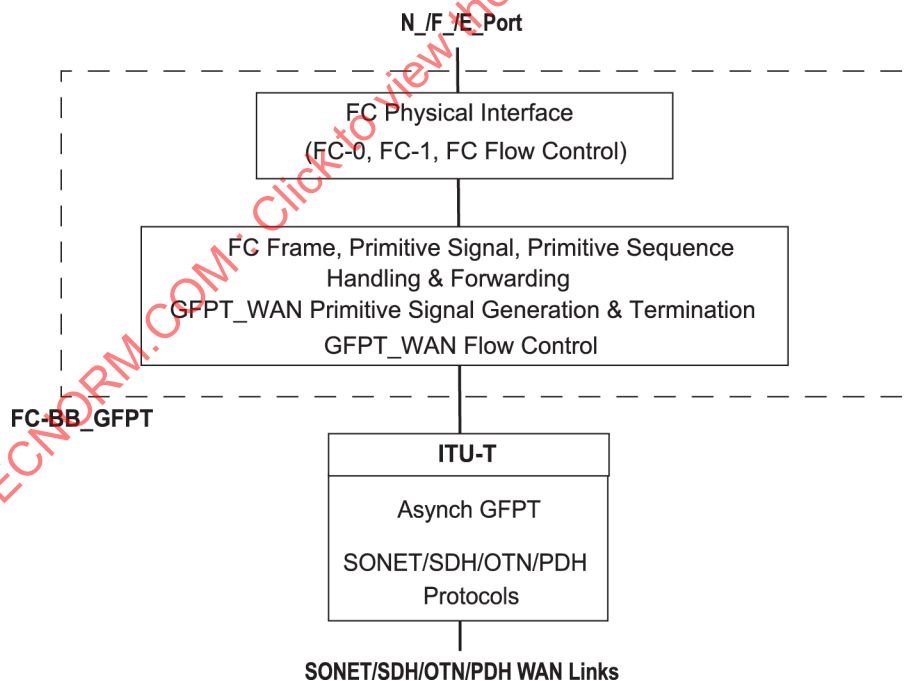


Figure 2 – Scope and components of FC-BB_GFPT model

The scope and components of the FC-BB_PW model is shown in Figure 3.

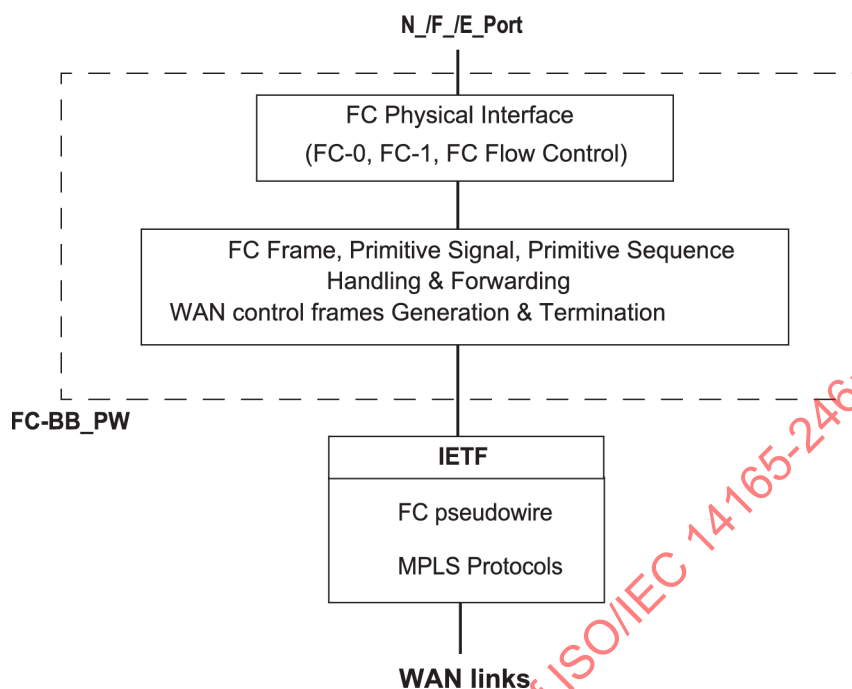


Figure 3 – Scope and components of FC-BB_PW model

The scope and components of the FC-BB_E model is shown in Figure 4.

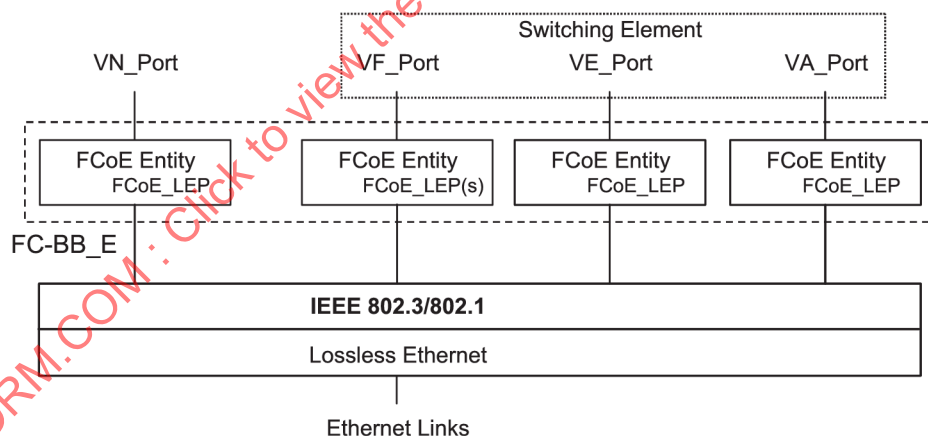


Figure 4 – Scope and components of FC-BB_E model

2 Normative References

2.1 Overview

The following standards contain provisions that, through reference in the text, constitute provisions of this standard. At the time of publication, the editions indicated were valid. All standards are subject to revision, and parties to agreements based on this standard are encouraged to investigate the possibility of applying the most recent editions of the standards listed below.

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Web: <http://www.incits.org>
E-mail: incits@itic.org

Additional availability contact information is provided below as needed.

2.2 Approved references

ANSI T1.105-2001, *Synchronous Optical Network (SONET) - Basic Description Including Multiplex Structures, Rates, and Formats*

ANSI INCITS 496-2012, *Fibre Channel - Security Protocols - 2 (FC-SP-2)*

ANSI INCITS 241-1994 (R1999), *Data Compression Method - Adaptive Coding with Sliding Window for Information Interchange*

2.3 References under development

At the time of publication, the following referenced standards were still under development. For information on the current status of the documents, or regarding availability, contact the relevant standards body or other organization as indicated.

For electronic copies of references under development by INCITS T11, see www.t11.org

T11/Project 2238-D, *Fibre Channel - Framing and Signaling - 4 (FC-FS-4)*

T11/Project 2220-D, *Fibre Channel - Switch Fabric - 6 (FC-SW-6)*

T11/Project 2237-D, *Fibre Channel - Link Services - 3 (FC-LS-3)*

2.4 ITU-T references

Copies of the following approved ITU-T standards may be obtained through the ITU-T Publications department at <http://www.itu.int>.

ITU-T Rec. G.707/Y.1322, (2007), *Network node interface for the synchronous digital hierarchy (SDH)*

ITU-T Rec. G.7041/Y.1303, (2005), *Generic Framing Procedure (GFP)*

ITU-T Rec. G.783, (2006), *Characteristics of synchronous digital hierarchy (SDH) equipment functional blocks*

ITU-T Rec. G.806, (2006), *Characteristics of transport equipment - Description methodology and generic functionality*

ITU-T Rec. G.702, (1988), *Digital Hierarchy Bit Rates*

2.5 IETF references

Copies of the following approved IETF standards may be obtained through the Internet Engineering Task Force (IETF) at www.ietf.org.

RFC 5905, *Network Time Protocol Version 4: Protocol and Algorithms Specification*, June 2010

RFC 3246, *An Expedited Forwarding PHB (Per-Hop Behavior)*, March 2002

RFC 3643, *Fibre Channel (FC) Frame Encapsulation*, December 2004

RFC 3821, *Fibre Channel Over TCP/IP (FCIP)*, July 2004

RFC 3822, *Finding Fibre Channel over TCP/IP (FCIP) Entities Using Service Location Protocol version 2 (SLPv2)*, July 2004

RFC 3031, *Multiprotocol Label Switching (MPLS) Architecture*, January 2001

RFC 3985, *Pseudowire Emulation Edge-to-Edge (PWE3) Architecture*, March 2005

RFC 4385, *Multiprotocol Pseudowire Emulation Edge-to-Edge (PWE3) Control Word for Use over an MPLS PSN*, February 2006

RFC 4447, *Pseudowire Setup and Maintenance Using the Label Distribution Protocol (LDP)*, April 2006

RFC 6307, *Encapsulation Methods for Transport of Fibre Channel frames Over MPLS Networks*, March 2012

2.6 IEEE references

Copies of the following approved IEEE standards may be obtained through the Institute of Electrical and Electronics Engineers (IEEE) at <http://standards.ieee.org>.

IEEE 802.3-2012: *IEEE Standard for Ethernet*

IEEE 802.1Q-2011: *Media Access Control (MAC) Bridges and*

Virtual Bridge Local Area Networks

IEEE 802.1Qbb-2011: *Media Access Control (MAC) Bridges and Virtual Bridged Local Area Network - Amendment 17: Priority-based Flow Control*

IEEE 802.1Qaz-2011: *Media Access Control (MAC) Bridges and Virtual Bridged Local Area Networks - Amendment 18: Enhanced Transmission Selection for Bandwidth Sharing Between Traffic Classes*

IEEE 1588-2008: *Precision Clock Synchronization Protocol for Networked Measurement and Control Systems*

IEEE 802.1AS-2011: *Timing and Synchronization for Time-Sensitive Applications in Bridged Local Area Networks*

3 Definitions and conventions

3.1 Common definitions

3.1.1

A_Port (see FC-SW-6)

combination of one PA_Port (see FC-SW-6) and one VA_Port operating together

3.1.2

B_Port

Bridge Port on a device that implements FC-BB_IP and connects to an E_Port on an FC switch

3.1.3

B_Port_Name (see FC-SW-6)

Name_Identifier (see 3.1.25) that identifies a B_Port (see 3.1.2) for identification purpose

3.1.4

codeword

sequence of bits of a code corresponding to a symbol

3.1.5

E_Port (see FC-SW-6)

Fabric expansion port that attaches to another E_Port to create an Inter-Switch Link

3.1.6

E_Port_Name

Name_Identifier (see 3.1.25) that identifies an E_Port (see 3.1.5)

3.1.7

ELP (see FC-SW-6)

Exchange Link Parameters SW_ILS

3.1.8

F_BSY (see FC-FS-4)

Fabric Busy

3.1.9

F_Port (see FC-SW-6)

combination of one PF_Port and one VF_Port operating together

3.1.10

F_Port_Name

Name_Identifier (see 3.1.25) that identifies an F_Port (see 3.1.9)

3.1.11

Fabric Initialization (see FC-SW-6)
process for configuring and building a Fabric

3.1.12

Fabric_Name
Name_Identifier (see 3.1.25) associated with a Fabric (see FC-SW-6)

3.1.13

FC-BB_E
protocol mapping defined by this standard in order to transport Fibre Channel over a Lossless Ethernet network

3.1.14

FC-BB_GFPT
equipment model defining gateway functionality for the interconnection of two non-Arbitrated Loop FC physical ports using 8B/10B encoding across a GFPT_WAN infrastructure (e.g., SONET, SDH, OTN, PDH)

3.1.15

FC-BB_IP
model defining equipment that interfaces with a Fibre Channel switched network on one side and an IP network on the other side

3.1.16

FC-BB_PW
equipment model defining gateway functionality for the interconnection of two non-Arbitrated Loop FC physical ports using 8B/10B encoding across a PSN (see 3.4.5)

3.1.17

FC_Port (see FC-FS-4)
port capable of transmitting and receiving Fibre Channel frames

3.1.18

Fibre Channel Backbone link
the Transport Trail (see 3.3.18) or equivalent network channel connection used for communications between two FC-BB-6 devices, encompassing FC-BB_IP and GFPT_WAN links, and in some cases made up of more than one physical or logical connection

3.1.19

FLOGI (see FC-LS-3)
Fabric Login ELS

3.1.20

Generic Framing Procedure (GFP) (see ITU-T Rec. G.7041/Y.1303)
procedure for adaptation of data (i.e., PDUs or 8B/10B encoded characters) to octet-synchronous (i.e., SONET, SDH, OTN) and bit-synchronous (i.e., PDH) Wide Area Network transport infrastructures

3.1.21

ISL (see FC-SW-6)
Inter-Switch Link

3.1.22

Keep Alive Timeout Value (K_A_TOV)
timer that is used by the Link Keep Alive (LKA) ELS (see FC-LS-3) as a trigger for issuing LKA

3.1.23

LS_ACC (see FC-LS-3)
Link Service Accept

3.1.24

LS_RJT (see FC-LS-3)
Link Service Reject

3.1.25

Name_Identifier (see FC-FS-4)
64-bit identifier, with a 60-bit value preceded with a 4-bit Network_Address_Authority Identifier, used to identify entities in Fibre Channel (e.g., N_Port, node, F_Port, or Fabric)

3.1.26

Node_Name (see FC-FS-4)
Name_Identifier (see 3.1.25) associated with a node

3.1.27

N_Port
device port that generates/terminates FC-4 traffic

3.1.28

N_Port_ID (see FC-FS-4)
topology unique address identifier of an Nx_Port

3.1.29

N_Port_Name
Name_Identifier (see 3.1.25) that identifies an N_Port (see 3.1.27)

3.1.30

Ordered Set (see FC-FS-4)

3.1.31

PLOGI (see FC-LS-3)
N_Port Login

3.1.32

P_BSY (see FC-FS-4)
N_Port Busy

3.1.33

Switch_Name
Name_Identifier (see 3.1.25) that uniquely identifies a Switch or a Bridge device

3.1.34

SW_ACC (see FC-SW-6)
Switch Fabric Internal Link Service Accept

3.1.35

SW_RJT (see FC-SW-6)
Switch Fabric Internal Link Service Reject

3.1.36

Transparent FC-BB
general model consisting of the FC-BB-GFPT (see 3.1.14) and the FC-BB_PW (see 3.1.16) models

3.1.37

WAN interface
interface that connects to a Wide Area Network, either physical (e.g., SONET) or logical (e.g., GFPT_WAN)

3.2 FC-BB_IP definitions

3.2.1

B_Access

component of the FC Entity (see 3.2.8) that interfaces with the FCIP_LEP (see 3.2.21) component of the FCIP Entity (see 3.2.15) on one side and the B_Port on the other side

3.2.2

B_Access_Name

Name_Identifier (see 3.1.25) of the B_Access portal

3.2.3

B_Access Virtual ISL

Virtual ISL (see 3.2.26) that connects two B_Access portals

3.2.4

Control and Service Module (CSM)

control component of the FC-BB_IP interface that mainly handles connection management and interfaces with the PMM (see 3.2.23)

3.2.5

encapsulated FC frame

SOF/EOF delimited FC frame prefixed with a 28-byte FC frame Encapsulation Header (see RFC 3643)

3.2.6

Encapsulated Frame Receiver Portal

TCP access point through which an encapsulated FC frame (see 3.2.5) is received from the IP network by an FCIP_DE (see 3.2.14)

3.2.7

Encapsulated Frame Transmitter Portal

TCP access point through which an encapsulated FC frame (see 3.2.5) is transmitted to the IP network by the FCIP_DE (see 3.2.14)

3.2.8

FC Entity

data forwarding component of the FC-BB_IP interface consisting of VE_Port(s) (see 3.2.24) and/or B_Access (see 3.2.1) portals

3.2.9

FC Entity Protocol Layer

protocol layer that lies between the Fibre Channel level FC-2 and the FCIP Entity Protocol Layer (see 3.2.16)

3.2.10

FC Receiver Portal

access point through which an FC frame and timestamp enters an FCIP_DE (see 3.2.14) from the VE_Port/B_Access (see 3.2.24 and 3.2.1)

3.2.11

FC Transmitter Portal

access point through which an FC frame and timestamp leaves an FCIP_DE (see 3.2.14) to the VE_Port/B_Access (see 3.2.24 and 3.2.1)

3.2.12

FC-BB_IP device

device defined by the FC-BB_IP model

3.2.13

FC-BB_IP interface

point that has interfaces to the FC switched network on one side and the IP network on the other side. It consists of a Switching Element, FC/FCIP Entity pair(s), the CSM, and the PMM

3.2.14

FCIP Data Engine (FCIP_DE)

data forwarding component of the FCIP Entity's (see 3.2.15) FCIP_LEP (see 3.2.21) that handles FC frame encapsulation, decapsulation, and transmission of encapsulated frames through a single TCP connection

3.2.15

FCIP Entity

data forwarding component of the FC-BB_IP interface consisting of the FCIP_LEP (see 3.2.21)

3.2.16

FCIP Entity Protocol Layer

protocol layer that lies between the FC Entity (see 3.2.8) layer and the TCP layer

3.2.17

FCIP frame

encapsulated FC frame (see 3.2.5)

3.2.18

FCIP Link

Virtual Link that connects an FCIP_LEP (see 3.2.21) in one FC-BB_IP device (see 3.2.12) with another

3.2.19

FCIP Link Acceptor

FC-BB_IP FCIP_LEP (see 3.2.21) that accepts an FCIP Link

3.2.20

FCIP Link Originator

FC-BB_IP FCIP_LEP (see 3.2.21) that originates an FCIP Link

3.2.21

FCIP Link Endpoint (FCIP_LEP)

component of an FCIP Entity (see 3.2.15) that contains one or more FCIP_DEs (see 3.2.14)

3.2.22

FCIP Transit Time (FTT)

total transit time of an encapsulated Fibre Channel frame in the IP network

3.2.23

Platform Management Module (PMM)

management component of the FC-BB_IP interface that handles time synchronization, discovery, and security, and interfaces with the CSM (see 3.2.4)

3.2.24

Virtual E_Port (VE_Port) (see FC-SW-6)

instance of the FC-2V sublevel of Fibre Channel that communicates with another VE_Port

3.2.25

VE_Port Virtual ISL

Virtual ISL (see 3.2.26) that connects two VE_Ports (see 3.2.24)

3.2.26

Virtual ISL

ISL that connects two VE_Ports (see 3.2.24) or two B_Access portals (see 3.2.1) across a non-FC link

3.3 FC-BB_GFPT definitions

3.3.1

ASFC_PAUSE

GFPT_WAN Primitive Signal used to pause flow on a GFPT_WAN link (see 6.4.4) and never transmitted to, or expected from, FC_Ports

3.3.2

ASFC_RESUME:

GFPT_WAN Primitive Signal used to resume flow on a GFPT_WAN link (see 6.4.4) and never transmitted to, or expected from, FC_Ports

3.3.3

GFP Server

Generic Framing Procedure (see 3.1.20) adaptation/de-adaptation engine

3.3.4

GFPT

(Asynchronous) Transparent Generic Framing Procedure (see 3.1.20)

3.3.5

GFPT_WAN interface

transport network-side interface, on an FC-BB_GFPT device, corresponding to one GFPT_WAN facility (see 3.3.7), and to one Transport Trail (see 3.3.18)

3.3.6

GFPT_WAN link:

Transport Trail (see 3.3.18) assigned to one GFPT_WAN facility (see 3.3.7)

3.3.7

GFPT_WAN facility

Transport Trail (see 3.3.18), GFP Server (see 3.3.3), FC-BB_GFPT devices, and their respective GFPT_WAN interfaces (see 3.3.5), corresponding to one interconnected FC_Port pair

3.3.8

inbound

sent from the FC-BB_GFPT device to the attached FC_Port

3.3.9

LEM (see 6.4.3)

Login Exchange Monitor

3.3.10

OTN (see ITU-T G.709/Y.1331)

term that refers to the rates and formats for Optical Transport Network

3.3.11

outbound

sent from the attached FC_Port to the FC-BB_GFPT device

3.3.12

PDH (see ITU-T G.702)

term that refers to the rates and formats for Plesiochronous Digital Hierarchy

3.3.13

PING

GFPT_WAN Primitive Signal used to initiate latency measurement on a GFPT_WAN link and never transmitted to, or expected from, FC_Ports

3.3.14

PING_ACK

GFPT_WAN Primitive Signal used to reply to a PING and complete round-trip latency measurement on a GFPT_WAN link and never transmitted to, or expected from, FC_Ports

3.3.15

RPSC ELS (see FC-LS-3)

Report Port Speed Capabilities ELS

3.3.16

SONET (see ANSI T1.105)

term that refers to the rates and formats for Synchronous Optical NETWORK

3.3.17

Synchronous Digital Hierarchy (SDH)

term that refers to the rates and formats specified in ITU-T G.707/Y.1322

3.3.18

Transport Trail

contiguously or virtually-concatenated signal group (see T1.105-2001) made up of one or more standardized SONET/SDH/OTN/PDH synchronous transport signals

3.3.19

WAN Primitive Signal

ASFC_PAUSE (see 3.3.1), ASFC_RESUME (see 3.3.2), PING (see 3.3.13), or PING_ACK (see 3.3.14) Primitive Signal

3.3.20

WAN_HOLDOFF_TOV

time-out value, specific to FC-BB_GFPT devices, which defines the period that elapses, following detection/indication of a GFPT_WAN link failure, before a GFPT_WAN Down condition is declared for the purposes of the state machine described in 6.4.2

3.4 FC-BB_PW definitions

3.4.1

Customer Edge (CE) (see RFC3985)

device where one end of a service originates and/or terminates

3.4.2

Multiprotocol Label Switching (MPLS)

data-carrying mechanism that belongs to the family of packet switched networks (see 3.4.5)

3.4.3

Provider Edge (PE)

device that provides PWE3 (see 3.4.4) to a CE (see 3.4.1)

3.4.4

Pseudowire Emulation Edge-to-Edge (PWE3)

mechanism that emulates the essential elements of an emulated service from one PE (see 3.4.3) to one or more PEs over a PSN (see 3.4.5)

3.4.5

Packet Switched Network (PSN)

network using IP or MPLS as the mechanism for packet forwarding

3.5 FC-BB_E definitions

3.5.1

Controlling FCF

Controlling Switch (see FC-SW-6) that supports Lossless Ethernet MACs (see 7.6)

3.5.2

Distributed FCF

set of FDFs associated with at least one Controlling FCF, that controls the operations of the set of FDFs

3.5.3

ENode (FCoE Node)

Fiber Channel node (see FC-FS-4) that is able to transmit FCoE frames using one or more ENode MACs

3.5.4

ENode MAC

Lossless Ethernet MAC coupled with an FCoE Controller in an ENode

3.5.5

ENode MAC address

assigned MAC address of an ENode MAC, used by the FCoE Controller of the ENode MAC for the FCoE Initialization Protocol (FIP)

3.5.6

FPMA (Fabric Provided MAC Address)

MAC address that is assigned by an FCF to a single ENode MAC and associated with a single VN_Port at that ENode MAC

3.5.7

FC-MAP (Mapped Address Prefix)

required value for the upper 24 bits of a MAC address assigned to a VN_Port in a Fabric Provided MAC Address

3.5.8

FCF (FCoE Forwarder)

Fibre Channel Switching Element (see FC-SW-6) that is able to forward FCoE frames across one or more FCF-MACs, and that optionally includes one or more Lossless Ethernet bridging elements and/or a Fibre Channel Fabric interface

3.5.9

FCF-MAC

Lossless Ethernet MAC coupled with an FCoE Controller in an FCF

3.5.10

FCF-MAC address

MAC address of an FCF-MAC

3.5.11

FCoE Controller

functional entity, coupled with a Lossless Ethernet MAC, instantiating and de-instantiating VE_Ports, VF_Ports, VN_Ports, VA_Ports, and/or FCoE_LEPs using the FCoE Initialization Protocol (FIP)

3.5.12

FCoE Entity

interface, containing one or more FCoE_LEPs, between a VN_Port, a VF_Port, or a VE_Port, and a Lossless Ethernet MAC

3.5.13

FCoE frame

Ethernet frame (see IEEE 802.3-2012) that contains an FCoE PDU (see 3.5.15)

3.5.14

FCoE_LEP (FCoE Link End-Point)

data forwarding component of an FCoE Entity that handles FC frame encapsulation/decapsulation, and transmission/reception of encapsulated frames through a single Virtual Link

3.5.15

FCoE PDU

PDU identified by the FCoE Ethernet Type that encapsulates a byte-encoded FC frame (see 7.10)

3.5.16

FDF (FCoE Data-Plane Forwarder)

FCDF (see FC-SW-6) that supports Lossless Ethernet MACs (see 7.7)

3.5.17

FDF-MAC

lossless Ethernet MAC coupled with an FCoE Controller in an FDF

3.5.18

FDF_Set

Switch_Names of the FDFs that are part of a Distributed FCF

3.5.19

FIP frame

Ethernet frame (see IEEE 802.3-2012) containing a FIP PDU

3.5.20

FIP PDU

PDU identified by the FIP Ethernet Type that encapsulates one or more FIP operations (see 7.11.7)

3.5.21

Lossless Ethernet bridging element

Ethernet bridging function operating across Lossless Ethernet MACs

3.5.22

Lossless Ethernet MAC

full duplex Ethernet MAC implementing extensions to avoid Ethernet frame loss due to congestion (e.g., the PAUSE mechanism (see IEEE 802.3-2012) or the Priority-based Flow Control mechanism (see IEEE 802.1Qbb))

3.5.23

Lossless Ethernet network

Ethernet network composed only of full duplex links, Lossless Ethernet MACs, and Lossless Ethernet bridging elements (see 4.4.4)

3.5.24

multi-node

VN_Port to VN_Port configuration supporting two or more VN2VN ENodes and in which the Locally Unique N_Port_ID protocol performs probing and claiming of an N_Port_ID

3.5.25

Multicast MAC address

MAC address associated with a group of logically related Ethernet stations on an Ethernet network and called a Multicast-Group Address in IEEE 802.3-2012

3.5.26**N_Port_ID P2P Beacon**

FIP N_Port_ID Beacon with the REC/P2P bit set to one

3.5.27**N_Port_ID P2P Claim Notification**

FIP N_Port_ID Claim Notification with the REC/P2P bit set to 1

3.5.28**N_Port_ID P2P Claim Response**

FIP N_Port_ID Claim Response with the REC/P2P bit set to 1

3.5.29**PE_Port (Physical E_Port)**

LCF (see FC-FS-4) within the Fabric that attaches to another PE_Port through a native FC link (see FC-SW-6)

3.5.30**PF_Port (Physical F_Port)**

LCF (see FC-FS-4) within the Fabric that attaches to a PN_Port through a native FC link (see FC-SW-6)

3.5.31**PN_Port (Physical N_Port)**

LCF (see FC-FS-4) that supports only VN_Ports (see FC-FS-4)

3.5.32**point-to-point**

VN_Port to VN_Port configuration supporting two VN2VN ENodes and in which the Locally Unique N_Port_ID protocol performs only the claiming of an N_Port_ID

3.5.33**Unicast MAC address**

MAC address associated with a particular Ethernet station on an Ethernet network and called an Individual Address in IEEE 802.3-2012

3.5.34**VA_Port (Virtual A_Port)**

instance of the FC-2V sublevel of Fibre Channel that communicates with another VA_Port

3.5.35**VA_Port/FCoE_LEP pair**

VA_Port and its associated FCoE_LEP

3.5.36**Vendor ID**

8-byte ASCII string, the value of which shall be assigned by INCITS Technical Committee T10 (see <http://www.t10.org/lists/2vid.htm>), used to uniquely identify an organizational entity

3.5.37**VE_Port (Virtual E_Port)**

instance of the FC-2V sublevel of Fibre Channel that communicates with another VE_Port (see FC-SW-6)

3.5.38**VE_Port/FCoE_LEP pair**

VE_Port and its associated FCoE_LEP

3.5.39**VF_Port (Virtual F_Port)**

instance of the FC-2V sublevel of Fibre Channel that communicates with one or more VN_Ports (see FC-SW-6)

3.5.40

VF_Port/FCoE_LEP pair

VF_Port and one of its associated FCoE_LEPs

3.5.41

VN_Port/FCoE_LEP pair

VN_Port and its associated FCoE_LEP

3.5.42

Virtual Link

logical link connecting two FCoE_LEPs (see 7.8)

3.5.43

VN_Port (Virtual N_Port)

instance of the FC-2V sublevel of Fibre Channel that operates as an N_Port (see FC-FS-4)

3.5.44

VN2VN ENode

ENode supporting one or more VN2VN_Ports

3.5.45

VN2VN ENode MAC

ENode MAC that supports the Locally Unique N_Port_IDs protocol

3.5.46

VN2VN_Port

VN_Port dedicated to the instantiation of VN_Port to VN_Port Virtual Links

3.5.47

VN_Port MAC address

MAC address used by an ENode for a particular VN_Port

3.6 Editorial conventions

In FC-BB-6, a number of conditions, mechanisms, sequences, parameters, events, states, or similar terms are printed with the first letter of each word in uppercase and the rest lowercase (e.g., Exchange, Sequence). Any lowercase uses of these words have the normal technical English meanings.

Lists sequenced by letters (e.g., a-red, b-blue, c-green) show no ordering relationship between the listed items. Numbered lists (e.g., 1-red, 2-blue, 3-green) show an ordering relationship between the listed items.

In case of any conflict between figure, table, and text, the text, then tables, and finally figures take precedence. Exceptions to this convention are indicated in the appropriate clauses.

In all of the figures, tables, and text of this document, the most significant bit of a binary quantity is shown on the left side. Exceptions to this convention are indicated in the appropriate clauses.

Data structures in this standard are displayed in Fibre Channel format (i.e., “big-endian”), while specifications originating in IEEE and IETF may display data structures in Ethernet format (i.e., “little-endian”).

If the value of the bit or field is not relevant, then x or xx appears in place of a specific value. If a field or a control bit in a frame is specified as not meaningful, then the entity that receives the frame shall not check that field or control bit.

Numbers that are not immediately followed by lower-case b or h are decimal values.

Numbers immediately followed by lower-case b (xxb) are binary values.

Numbers or upper case letters immediately followed by lower-case h (xxh) are hexadecimal values.

In figures, dashed components or bracketed components are optional.

3.7 List of commonly used acronyms and abbreviations

Abbreviations and acronyms applicable to this standard are listed. Definitions of several of these items are included in clause 3.

3.7.1 General

BB	Backbone
EBP	Exchange B_Access Parameters
ELP	Exchange Link Parameters
EOF	End of Frame
EOFni	End of Frame Invalid
ESC	Exchange Switch Capabilities
F_BSY	Fabric Busy
FCS	Frame Check Sequence
FC-FS-4	Fibre Channel - Framing and Signaling - 4
FC-LS-3	Fibre Channel - Link Services - 3
FC-SP-2	Fibre Channel - Security Protocols - 2
FC-SW-6	Fibre Channel - Switched Fabric - 6
FLOGI	Fabric Login
ISL	Inter-switch Link
ITU-T	International Telecomm. Union - Telecommunication Standardization Section
K_A_TOV	Keep Alive Timeout value
LKA	Link Keep Alive
LS_ACC	Link Service Accept reply frame
LS_RJT	Link Service Reject reply frame
LSB	least significant byte
MSB	most significant byte
P_BSY	N_Port Busy
PDU	Protocol Data Unit
PLOGI	N_Port Login
SOF	Start of Frame
SW_ACC	Switch Fabric Internal Link Service Accept
SW_ILS	Switch Fabric Internal Link Services
SW_RJT	Switch Fabric Internal Link Service Reject
VL	Virtual Link
WAN	Wide Area Network

3.7.2 FC-BB_IP

B_Access	B_Access Portals
CSM	Control and Service Module
FCIP	FC over TCP/IP
FCIP_DE	FCIP Data Engine
FCIP_LEP	FCIP Link Endpoint
IETF	IETF Internet Engineering Task Force (www.ietf.org)
PMM	Platform Management Module
RFC	Request For Comment
VE_Port	Virtual E_Port

3.7.3 FC-BB_GFPT

ASFC	Alternate Simple Flow Control
GFP	Generic Framing Procedure
GFPT	(Asynchronous) Transparent Generic Framing Procedure
GFPT_WAN	GFPT Wide Area Network
LEM	Login Exchange Monitor
OTN	Optical Transport Network
PDH	Plesiochronous Digital Hierarchy
SDH	Synchronous Digital Hierarchy
SONET	Synchronous Optical NETwork

3.7.4 FC-BB_PW

EF PHB	Expedited Forwarding Per-Hop Behavior
PE	Provider Edge
PW	pseudowire
MPLS/PW	Multiprotocol Label Switching pseudowire

3.7.5 FC-BB_E

ACE	Access Control Entry
ACL	Access Control List
D_A_TOV	Discovery Advertisement Timeout Value
ENode	FCoE Node
FC-MAP	FCoE Mapped Address Prefix
FCF	FCoE Forwarder
FCF-MAC	FCoE Forwarder Media Access Control
FCoE_LEP	FCoE Link Endpoint
FIP	FCoE Initialization Protocol
FPMA	Fabric Provided MAC Address
LAN	Local Area Network
MAC	Media Access Control
PFC	Priority-based Flow Control
PHY	Physical Layer
PTP	Precision Time Protocol
TLV	Type, Length, Value
VA_Port	Virtual A_Port
VE_Port	Virtual E_Port
VF_Port	Virtual F_Port
VLAN	Virtual Local Area Network
VN_Port	Virtual N_Port

3.8 Symbols

Unless indicated otherwise, the following symbol has the listed meaning.

!= not equal

3.9 Keywords

3.9.1 ignored: A keyword used to describe an unused bit, byte, word, field or code value. The contents or value of an ignored bit, byte, word, field or code value shall not be examined by the receiving device and may be set to any value by the transmitting device.

3.9.2 invalid: A keyword used to describe an illegal or unsupported bit, byte, word, field or code value. Receipt of an invalid bit, byte, word, field or code value shall be reported as an error.

3.9.3 mandatory: A keyword indicating an item that is required to be implemented as defined in this standard.

3.9.4 may: A keyword that indicates flexibility of choice with no implied preference (equivalent to “may or may not”).

3.9.5 may not: A keyword that indicates flexibility of choice with no implied preference (equivalent to “may or may not”).

3.9.6 optional: A keyword that describes features that are not required to be implemented by this standard. However, if any optional feature defined by this standards is implemented, then it shall be implemented as defined in this standard.

3.9.7 reserved: A keyword referring to bits, bytes, words, fields and code values that are set aside for future standardization. A reserved bit, byte, word or field shall be set to zero, or in accordance with a future extension to this standard. Recipients should not check reserved bits, bytes, words or fields for zero values. Receipt of reserved code values in defined fields shall be reported as an error.

3.9.8 shall: A keyword indicating a mandatory requirement. Designers are required to implement all such mandatory requirements to ensure interoperability with other products that conform to this standard.

3.9.9 should: A keyword indicating flexibility of choice with a strongly preferred alternative; equivalent to the phrase “it is strongly recommended”.

3.9.10 x or xx: The value of the bit or field is not relevant.

4 FC-BB-6 Structure and Concepts

4.1 FC-BB-6 backbone mappings

FC-BB-6 models (i.e., FC-BB_IP, FC-BB_GFPT, FC-BB_PW, and FC-BB_E), specified in this standard, define mappings for transporting Fibre Channel over different network technologies.

The FC-BB_IP model uses TCP connections over IP networks. The FC-BB_GFPT model makes use of the Asynchronous Transparent Generic Framing Procedure (GFPT) (see ITU-T Rec. G.7041/Y.1303). GFPT may be used for adaptation to different transport facilities including SONET, SDH, OTN and PDH. Details regarding the mapping of GFPT-adapted traffic to such transport facilities are elaborated in various ITU-T standards (see 2.4). The FC-BB_PW model uses PW connections over MPLS networks. The FC-BB_E model uses Lossless Ethernet links (i.e., full duplex Ethernet links extended to become lossless, see 4.4.4).

A second important distinction among the mappings discussed in this standard relates to supported architectures (i.e., network and/or link topologies) and the place of the defined devices within them. FC-BB_IP defines an FC Entity that consists of:

- a) one or more Virtual E_Port (VE_Port) virtual ISL(s) that interconnect the E_Ports of external FC switches and N_Ports of external end devices; or
- b) one or more B_Access virtual ISL(s) that interconnect the E_Ports of external FC switches.

The B_Access virtual ISL(s) are part Fabric bridge device with resident switch-facing interfaces called B_Ports. B_Ports have selected Fabric functions (see FC-SW-6). B_Ports are Fabric ports, and FC-BB_IP devices are components of an FC Fabric. FC-BB_IP also defines support for FC bridge devices with Fabric-facing B_Ports. However, FC-BB_IP also supports functional integration within an FC Switch. Thus FC-BB_IP devices may also have E_Ports and F_Ports.

FC-BB_GFPT and FC-BB_PW define a device that is not a component of a Fabric, and supports no Fabric functionality. Instead, it interconnects two Fibre Channel physical ports (i.e., attached FC_Ports), appearing architecturally as a wire to those ports.

FC-BB_E defines end devices (i.e., ENodes) and Fabric devices (i.e., FCFs and FDFs). ENodes are Fibre Channel nodes (see FC-FS-4) that are able to transport Fibre Channel frames over Lossless Ethernet. FCFs and FDFs are Fibre Channel Switching Elements (see FC-SW-6) that are able to transport Fibre Channel frames over Lossless Ethernet.

4.2 FC-BB-6 reference models

4.2.1 FC-BB-6 reference models overview

FC-BB-6 defines reference models corresponding to the FC-BB_IP, FC-BB_GFPT, FC-BB_PW, and FC-BB_E reference models.

The FC-BB_IP reference model supports the attachment of FC switches (i.e., E_Ports) via one or more B_Ports or E_Ports and the attachment of N_Ports via one or more F_Ports. The FC-BB_GFPT and FC-BB_PW reference models support the attachment of N_Ports, F_Ports, and E_Ports, and the following Fibre Channel port interconnections:

- a) N_Port to N_Port;
- b) N_Port to F_Port; and
- c) E_Port to E_Port.

The FC-BB_E reference model supports the operation of:

- a) VN_Ports (see FC-FS-4) in ENodes;
- b) VF_Ports and VE_Ports (see FC-SW-6) in FCFs;
- c) VF_Ports, VE_Ports, and VA_Ports (see FC-SW-6) in Controlling FCFs; and
- d) VF_Ports and VA_Ports (see FC-SW-6) in FDFs.

Table 2 summarizes the resident FC_Port types for the different FC-BB-6 reference models.

Table 2 – Reference models and resident FC_Port types

	Reference Model			
	FC-BB_IP	FC-BB_GFPT	FC-BB_PW	FC-BB_E ^a
Resident FC_Port Type(s)	B_Port, E_Port, F_Port	None (FC Physical Interface)	None (FC Physical Interface)	N_Port, E_Port, F_Port, A_Port
a) Resident FC_Ports are optional for FC-BB_E.				

4.2.2 FC-BB_IP reference model

In Figure 5, frames destined for a remote FC network enter a B_Port, an E_Port, or an F_Port, and are forwarded on the IP network to their destination.

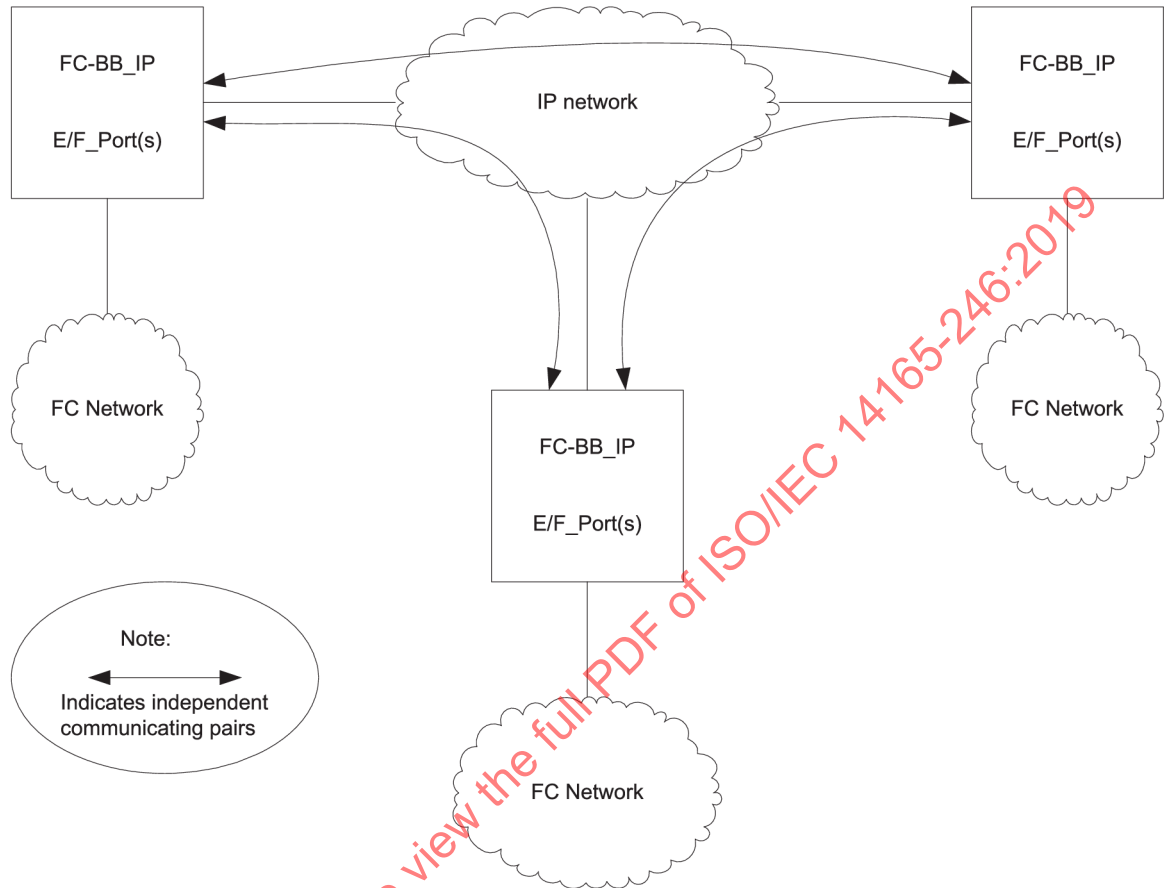


Figure 5 - FC-BB_IP reference model

4.2.3 FC-BB_GFPT reference model

In Figure 6, FC physical signals (i.e., relevant 8B/10B codewords) enter an FC physical port on an FC-BB_GFPT device, and are forwarded on the SONET/SDH/OTN/PDH network to their destination.

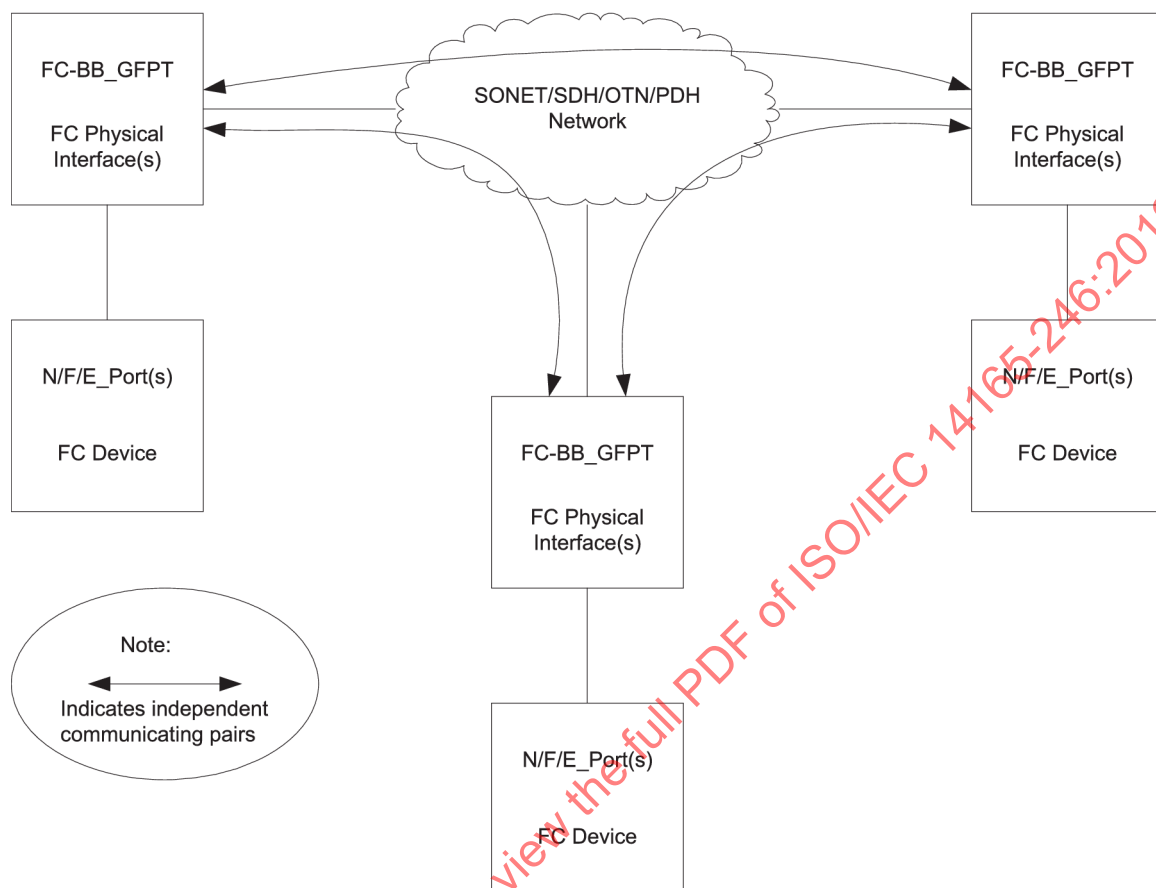


Figure 6 – FC-BB_GFPT reference model

4.2.4 FC-BB_PW reference model

In Figure 7, FC physical signals enter an FC physical port on an FC-BB_PW device, and are forwarded on the PSN network to their destination.

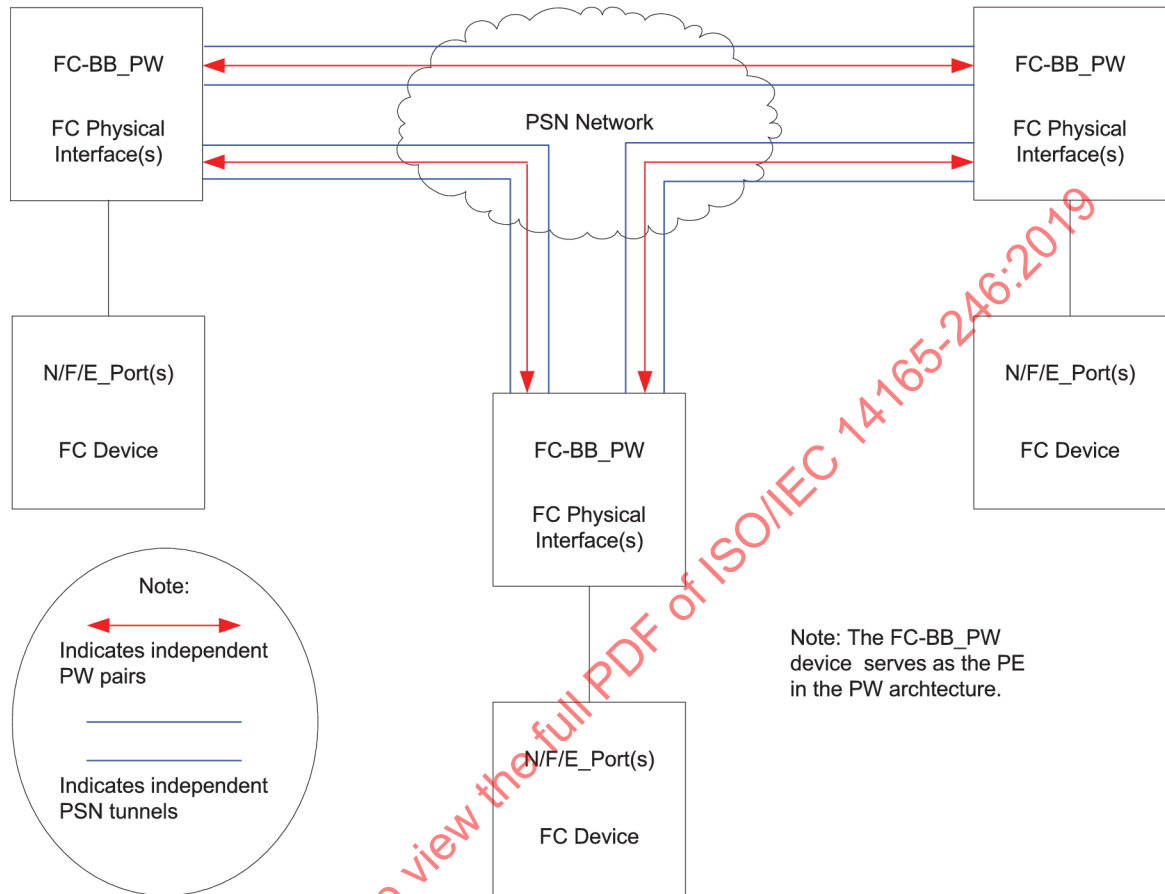


Figure 7 – FC-BB_PW reference model

4.2.5 FC-BB_E reference models

4.2.5.1 FC-BB_E VN_Port to VF_Port reference model

Figure 8 shows the VN_Port to VF_Port reference model. FCoE frames originated by a VN_Port are transported over the Lossless Ethernet network to the VF_Port the VN_Port is logged in with.

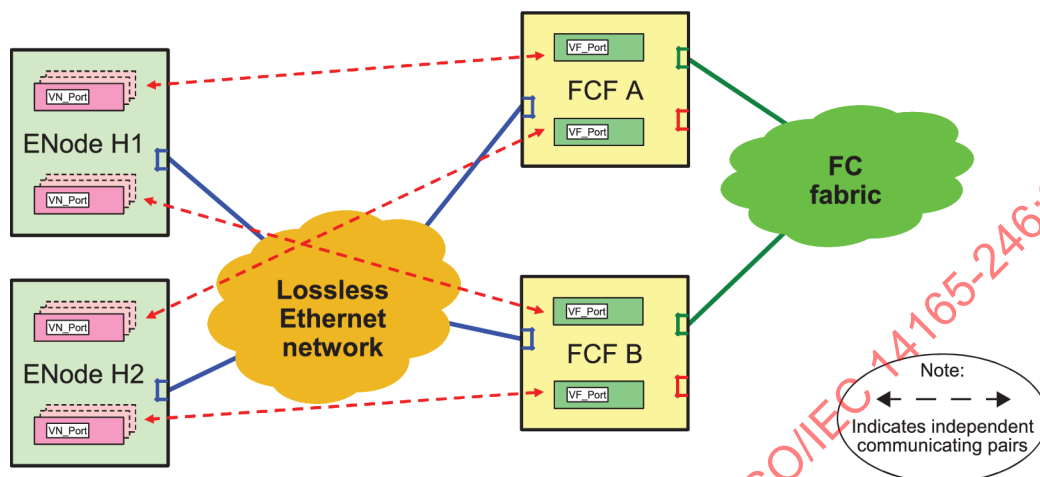


Figure 8 – FC-BB_E VN_Port to VF_Port reference model

4.2.5.2 FC-BB_E VE_Port to VE_Port reference model

Figure 9 shows the VE_Port to VE_Port reference model. FCoE frames originated by a VE_Port are transported over the Lossless Ethernet network to the VE_Port that the VE_Port is logically connected to.

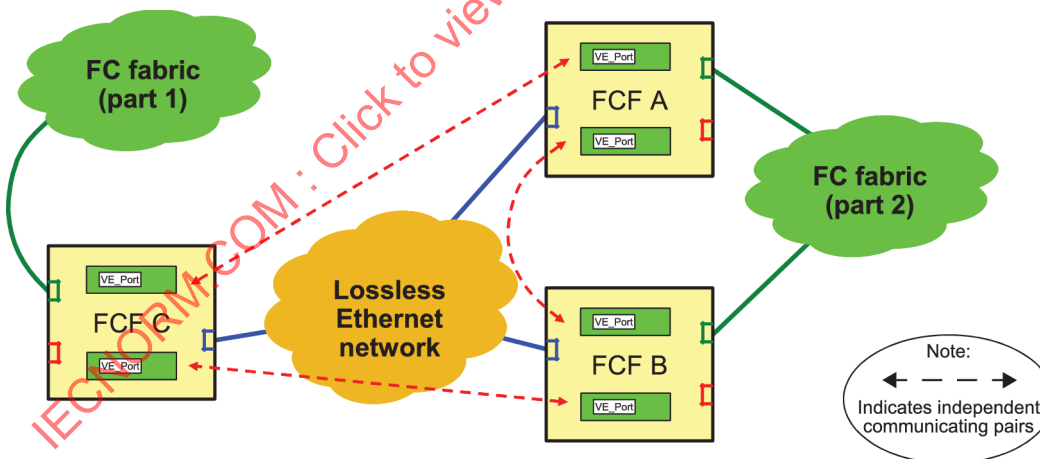


Figure 9 – FC-BB_E VE_Port to VE_Port reference model

4.2.5.3 FC-BB_E VN_Port to VN_Port reference model

Figure 10 shows the VN_Port to VN_Port reference model. FCoE frames originated by a VN_Port are transported over the Lossless Ethernet network to the VN_Port(s) with which the originating VN_Port is logged in with.

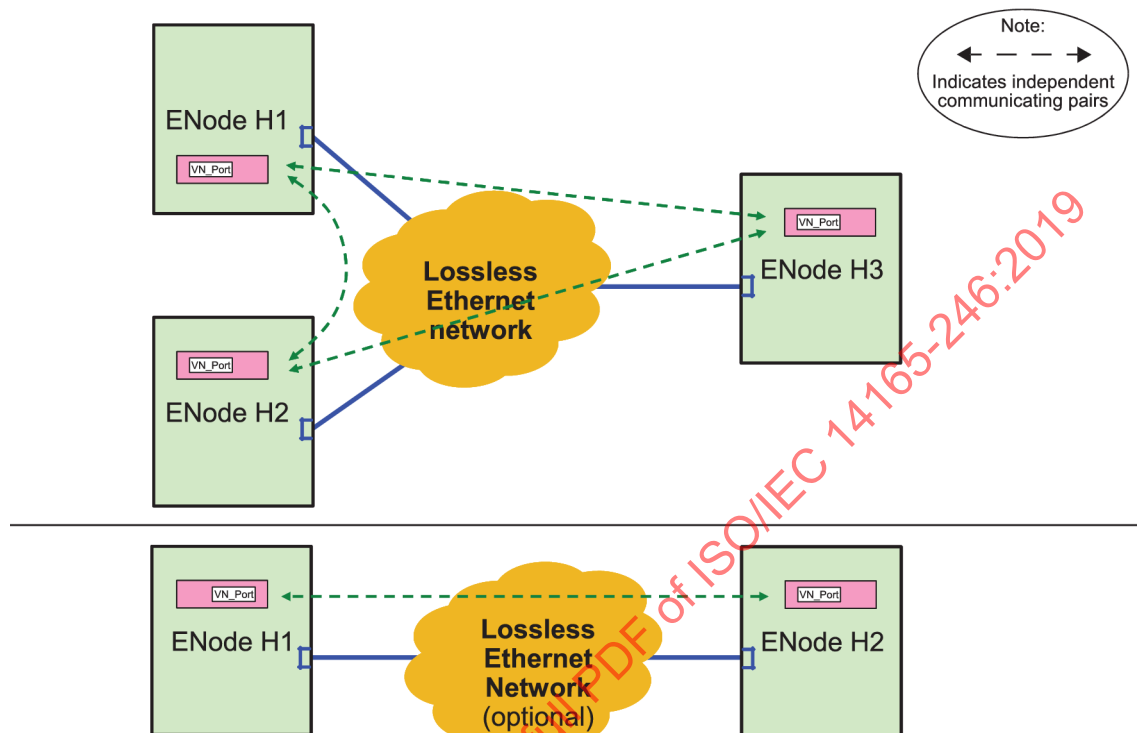


Figure 10 – FC-BB_E VN_Port to VN_Port reference model

4.2.5.4 FC-BB_E VA_Port to VA_Port reference model

Figure 11 shows the VA_Port to VA_Port reference model. FCoE frames originated by a VA_Port are transported over the Lossless Ethernet network to the VA_Port that the VA_Port is logically connected to.

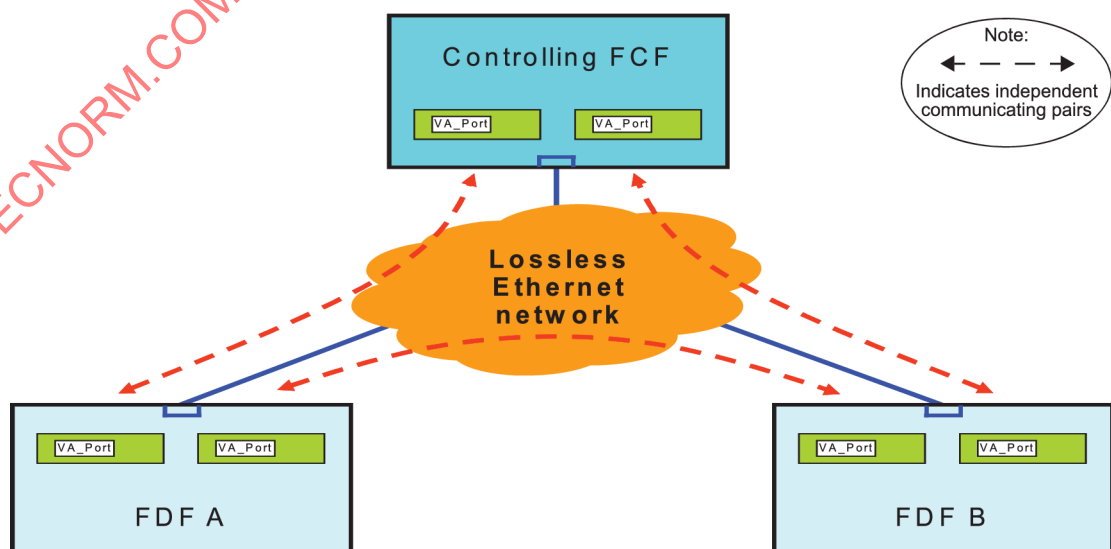


Figure 11 – FC-BB_E VA_Port to VA_Port reference model

4.3 FC-BB-6 models overview

4.3.1 FC-BB_IP

The FC-BB_IP model defines the means by which Fibre Channel networks interface with and connect across an IP network. FC-BB_IP makes use of the FCIP standard (see RFC 3821) to define the mapping and control required by the TCP/IP protocol and the FC frame encapsulation standard (see RFC 3643) to define the encapsulation. FC-BB_IP also defines the connection management, addressing, time synchronization, discovery, security, switching, routing, and error recovery required to support Fibre Channel over TCP/IP. FC-BB_IP is independent of the underlying physical technology that exists beneath the IP layer. In this sense, the IP network could use SONET, Gigabit Ethernet, or any other link-level technology below it.

FC-BB_IP encapsulates byte-encoded Class 2, 3, or F Fibre Channel frames into a suitable format (i.e., encapsulated FC frames) for carriage over the IP network. Subclause 5.6 describes encapsulated FC frames in detail. The TCP/IP protocol suite provides a reliable transport of frames over the IP network. TCP provides flow control and error recovery.

The FC-BB_IP protocol provides mechanisms to create VE_Port or B_Access connectivity over the IP network (see 5).

The FC-BB_IP device interfaces to attached FC_Ports are FC physical interfaces operating at standard rates. The FC-BB_IP devices may support automatic WAN link speed negotiation with the attached IP networks.

4.3.2 FC-BB_GFPT

The FC-BB_GFPT model defines the means by which FC physical links that carry 8B/10B encoded information may be extended over any WAN transport infrastructure for which GFPT mapping is defined. The FC-BB_GFPT model is not applicable to FC physical links that use other encodings (e.g., 16GFC physical variants use 64B/66B encoding, and are not supported). With that restriction, FC-BB_GFPT supports the interconnection of arbitrary, legal, non-Arbitrated Loop FC_Port combinations, imposing no requirements, and making no suppositions regarding the topology, or even the presence of an FC Fabric. FC-BB_GFPT supports efficient transport of FC data over transport facilities of arbitrary bandwidths and potentially large distances. FC-BB_GFPT supports Class 2, 3, and F traffic.

FC-BB_GFPT supports the interconnection of arbitrary, legal, non-Arbitrated Loop FC_Port combinations, imposing no requirements, and making no suppositions regarding the topology, or even the presence of an FC Fabric. FC-BB_GFPT supports efficient transport of FC data over transport facilities of arbitrary bandwidths and potentially large distances. FC-BB_GFPT supports Class 2, 3, and F traffic.

FC-BB_GFPT devices do not generate FC frames and do not directly participate in port initialization or other Exchanges. FC-BB_GFPT devices are exempt from any requirements regarding FC_Port authentication (see FC-SP-2), and they do not impede or interfere with any such processes that may occur between the attached and interconnected FC_Ports. FC-BB_GFPT devices have no FC identity or visibility, and administratively they may be kept separate and distinct from FC Fabrics and ports.

FC_Ports are interconnected pair-wise over SONET/SDH/OTN/PDH networks, via FC-BB_GFPT devices, in a point-to-point fashion. Although multiple FC_Ports may interface with a single FC-BB_GFPT device, each opposing FC_Port pair is connected via a dedicated Transport Trail (e.g., a contiguously or virtually-concatenated group). Since trail and access section configurations may differ, FC-BB_GFPT devices have both physical interfaces to the transport network, and individual FC-BB_GFPT devices may have more than one such physical interface, as well as logical interfaces associated with individual circuits. Logical interfaces are referred to as GFPT_WAN interfaces. A GFPT_WAN interface corresponds to a specific Transport Trail, and always to a single attached FC_Port pair. Governance of the relationship of GFPT_WAN interfaces to physical SONET/SDH/OTN/PDH interfaces, and of any changes of such relationships (e.g., as may occur during network protection events), is specified in the appropriate ITU-T and ANSI-T1 standards (see clause 2), and is therefore outside the scope of this standard. Multiple GFPT_WAN links originating on one FC-BB_GFPT device may be terminated on different, and geographically disparate, FC-BB_GFPT devices. The routing and provisioning of network facilities underlying GFPT_WAN links is outside the scope of this standard.

The FC-BB_GFPT device interfaces to attached FC_Ports are FC physical interfaces operating at standard rates. The FC physical interfaces on FC-BB_GFPT devices may support link speed negotiation with the attached FC_Ports.

4.3.3 FC-BB_PW

The FC-BB_PW model defines the means by which FC physical links that carry 8B/10B encoded may be extended over a wide area MPLS network. The FC-BB_PW model is not applicable to FC physical links that use other encodings (e.g., 16GFC physical variants use 64B/66B encoding, and are not supported). FC-BB_PW is specified in conjunction with RFC 6307 to define the mapping and control required by the MPLS/PW protocol. According to this model the FC-BB_PW device serves as a PE network element in the PW architecture. FC-BB_PW is independent of the physical-level and link-level technologies that exist beneath the MPLS layer.

FC-BB_PW supports the interconnection of FC_Ports transporting Class 2, 3, and F traffic. FC-BB_PW encapsulates 8B/10B-encoded FC frames and a selected set of Primitive Signals and Primitive Sequences into PW PDUs for transport over the MPLS network. FC-BB_PW utilizes reliable transport of FC traffic over the MPLS network provided by the PW termination layer as specified in RFC 6307.

The FC-BB_PW entity does not generate FC frames and does not directly participate in port initialization or other Exchanges. The FC-BB_PW entity is exempt from any requirement regarding FC_Port authentication (see FC-SP-2), and it does not impede or interfere with any such processes that may occur between the attached and interconnected FC_Ports. The FC-BB_PW entity has no FC identity or visibility, and administratively it may be kept separate and distinct from FC Fabrics and ports.

4.3.4 FC-BB_E

The FC-BB_E model defines the means by which Fibre Channel frames are transported over a Lossless Ethernet network (see 4.4.4). Although a generic Ethernet network may lose frames due to congestion, a proper implementation of appropriate Ethernet extensions (e.g., the PAUSE mechanism defined in IEEE 802.3-2012) allows a full duplex Ethernet link to provide a lossless behavior similar to the one provided by the buffer-to-buffer mechanism (see FC-FS-4). The protocol mapping defined by FC-BB_E is referred to as Fibre Channel over Ethernet (FCoE) and requires the underlying Ethernet layer to be full duplex and lossless (i.e., to be composed only of full duplex links and to provide a lossless behavior when transporting FCoE frames).

FC-BB_E encapsulates byte-encoded Class 2, 3, or F Fibre Channel frames into a suitable format (i.e., FCoE frames) for transport over the Lossless Ethernet network. See 7.10.

The FC-BB_E protocol provides mechanisms to create VN_Port to VF_Port Virtual Links, VE_Port to VE_Port Virtual Links, VN_Port to VN_Port Virtual Links, and VA_Port to VA_Port Virtual Links.

4.4 FC-BB-6 requirements

4.4.1 Fibre Channel Class support

Class F shall be supported between FC-BB_IP, FC-BB_GFPT, and FC-BB_PW devices.

Class 2 and Class 3 may be supported between FC-BB_IP, FC-BB_GFPT, and FC-BB_PW devices.

Class 3 shall be supported between FC-BB_E devices. Class 2 and Class F may be supported between FC-BB_E devices.

NOTE 1 – A Lossless Ethernet network configuration that includes Lossless Ethernet bridges may not provide sufficient functionality to support all aspects of Class 2 service. FCFs and ENodes may indicate support for Class 2 service in the presence of Lossless Ethernet bridges, but such a configuration should be managed accordingly.

4.4.2 Payload transparency

4.4.2.1 FC-BB_IP

Arriving Class 2, 3, and F Fibre Channel frames from an FC network and destined to a remote FC network shall be encapsulated using the FC-BB_IP defined mechanisms and transmitted to the appropriate FC-BB_IP device.

Arriving encapsulated frames received from remote FC-BB_IP device shall be decapsulated and sent to an FC network.

Primitive Signals and Primitive Sequences shall not be transported between FC-BB_IP devices.

4.4.2.2 Transparent FC-BB (FC-BB_GFPT and FC-BB_PW)

FC frames inbound from one attached FC_Port shall be delivered to the remote FC_Port in native form (i.e., without further encapsulation) across the transport network according to the adaptation processes described in 6.4.8.1 for FC-BB_GFPT devices and 6.4.8.2 for FC-BB_PW devices. Frames received from remote Transparent FC-BB devices shall be forwarded to the attached FC_Port. Selected ELP, SW_ACC, FLOGI, PLOGI, and LS_ACC frames may be subject to inspection and/or minor modifications, in transiting one or the other Transparent FC-BB device, as described in 6.4.4.

Primitive Signals transmitted by an attached FC_Port are forwarded across the transport network for delivery to the remote FC_Port according to the rules described in clause 6. Primitive Signals are forwarded by an FC-BB_GFPT device in native form (i.e., without further encapsulation) according to the adaptation processes described in 6.4.8.1. Primitive Signals are forwarded by an FC-BB_PW device encapsulated within control frames as specified in RFC 6307.

Primitive Sequences transmitted by an attached FC_Port are forwarded across the transport network for delivery to the remote FC_Port according to the rules described in clause 6. Primitive Sequences are forwarded by an FC-BB_GFPT device in native form (i.e., without further encapsulation) according to the adaptation (including rate adaptation) processes described in 6.4.8.1. Primitive Sequences are forwarded by an FC-BB_PW device encapsulated within control frames as specified in RFC 6307.

4.4.2.3 FC-BB_E

Class 2, 3, and F Fibre Channel frames arriving from a VN_Port, a VF_Port, a VE_Port, or a VA_Port shall be encapsulated in FCoE frames and transmitted to the appropriate FC-BB_E device.

FCoE frames received from a remote FC-BB_E device shall be decapsulated and sent to the appropriate VN_Port, VF_Port, VE_Port, or VA_Port.

Primitive Signals and Primitive Sequences shall not be forwarded between FC-BB_E devices.

4.4.3 Latency delay and timeout value

FC-BB_IP shall ensure that the incoming encapsulated FC frames whose FCIP Transit Time (FTT) exceeds $1/2 E_D_TOV$ shall be discarded and not admitted into the FC network. Fibre Channel timeout values shall be administratively set to accommodate the FTT.

FC-BB_IP shall allow Class F encapsulated FC frames to be transmitted with a zero timestamp value.

Transparent FC-BB requires that the latency between two Transparent FC-BB devices be:

- a) no more than one-half of the E_D_TOV value of the attached devices for frames;
- b) no more than one-half of the R_T_TOV value of the attached devices for Primitive Sequences; and

- c) within the R_A_TOV values of the attached Fabric(s), if any.

The Lossless Ethernet bridges used to build a Lossless Ethernet network suitable for FC-BB_E usage should enforce a maximum bridge transit delay of 500 ms (see IEEE 802.1Q-2011).

4.4.4 QoS and bandwidth

FC-BB_IP recommends that some form of preferential QoS be used for the FCIP traffic in the IP network to minimize latency and packet drops although no particular form of QoS is recommended. See RFC 3821.

FC-BB_GFPT has no specific transport service requirements.

FC-BB_PW recommends that Primitive Sequences are carried with low latency and no loss over the MPLS network. In addition to these properties, FC data traffic should be provided with assurance of some amount of bandwidth, however no specific recommendation is made in this standard. The Differentiated Services EF PHB (see RFC 3246) is an example of a mechanism that may be used for FC-BB_PW traffic management.

FC-BB_E is intended to operate over an Ethernet network that does not discard frames in the presence of congestion. Such an Ethernet network is called Lossless Ethernet in this standard.

Lossless Ethernet may be implemented through the use of some Ethernet extensions. Suitable extensions include the PAUSE mechanism defined in IEEE 802.3-2012, or the Priority-based Flow Control (PFC) mechanism defined in IEEE 802.1Qbb, where FCoE frames shall use a lossless priority (see IEEE 802.1Qbb). The Precision Time Protocol (PTP) may be used to determine link latency (see IEEE 1588-2008 or IEEE 802.1AS).

4.4.5 In-order delivery

FC-BB_IP devices shall provide in-order delivery of frames within the scope of any TCP connection.

FC-BB_GFPT devices shall provide in-order delivery within each provisioned Transport Trail for all transmitted data (i.e., frames, Primitive Signals and Primitive Sequences), as discussed and with the exceptions detailed in clause 6.

FC-BB_PW devices shall provide in-order delivery within each provisioned PW for all transmitted data (i.e., frames, Primitive Signals and Primitive Sequences), as discussed and with the exceptions detailed in clause 6.

FC-BB_E devices shall be able to provide in-order delivery of FCoE frames on at least a per-Exchange basis within the Lossless Ethernet network.

4.4.6 Flow control

FC-BB_IP devices shall ensure that TCP flow control and error recovery acts in proper concert with the Fibre Channel BB_Credit flow control mechanism.

Flow control at E_Ports, F_Ports, VE_Ports, and B_Ports shall operate as defined in FC-SW-6.

The Alternate Simple Flow Control (ASFC) mechanism (see 6.4.4) shall be used between FC-BB_GFPT devices and between FC-BB_PW devices. Flow control on FC_Port-facing links is specified in clause 6.

Flow control at FC-BB_GFPT and FC-BB_PW physical interfaces to attached FC devices shall operate as defined in FC-SW-6 or FC-FS-4, as appropriate.

FC-BB_E devices rely on proper implementation of Ethernet extensions for flow control of FCoE frames. Ethernet extensions achieve the same level of service provided by the Fibre Channel buffer-to-buffer flow control (see FC-FS-4). Suitable extensions include the PAUSE mechanism defined in IEEE 802.3-2012, or the Priority-based Flow Control (PFC) mechanism defined in IEEE 802.1Qbb, where FCoE frames shall use a lossless priority (see IEEE 802.1Qbb). The Precision Time Protocol (PTP) may be used to determine link latency (see IEEE 1588-2008 or IEEE 802.1AS).

4.5 FC-BB-6 SW_ILS codes

Table 3 shows the SW_ILS codes allocated for FC-BB-6 use.

Table 3 – FC-BB-6 SW_ILS codes

Encoded Value (hex)	Description	Abbr.	Reference
28 03 00 00	Authentication Special Frame	ASF	5.7.2.3.2
28 01 00 00	Exchange B_Access Parameter	EBP	5.4.3.3.1

Table 4 shows the ELS codes allocated for FC-BB-6 use.

Table 4 – FC-BB-6 ELS codes

Encoded Value (hex)	Description	Abbr.	Reference
80 00 00 00	Link Keep Alive	LKA	FC-LS-3

5 FC-BB_IP Structure and Concepts

5.1 Applicability

Clause 4 specifies the FC-BB_IP reference model. This clause specifies the FC-BB_IP functional model.

5.2 FC-BB_IP overview

Figure 12 shows a network configuration consisting of three FC-BB_IP devices. FC-BB_IP is a Fibre Channel backbone transport protocol that tunnels encapsulated FC frames across the IP network. An FC-BB_IP device has interfaces to both the IP and the FC network. The FC network interface supports multiple E_Ports/F_Ports (see Figure 13) or multiple B_Ports (see Figure 17).

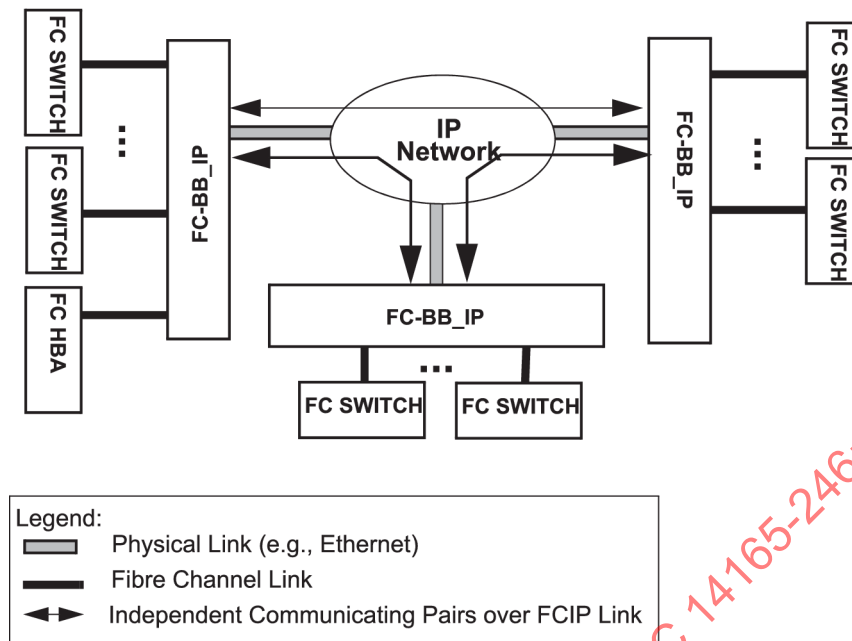


Figure 12 – FC-BB_IP network configuration

Only FC-BB_IP devices that support E_Ports or F_Ports require FC switching.

The FC-BB_IP protocol provides mechanisms to create VE_Port or B_Access connectivity over the IP network. The FC-BB_IP protocol communication occurs between pairs of FC-BB_IP devices over virtual constructs (i.e., FCIP Links) that are described in 5.3.4.2.4. Although the communication occurs between pairs of FC-BB_IP devices, a single FC-BB_IP device may communicate with more than one device at the same time (see Figure 20).

NOTE 2 – Although the current scheme allows an FC-BB_IP device to independently connect to more than one FC-BB_IP device, it does not specify a point-to-multipoint connection.

The FC-BB_IP protocol uses encapsulated FC frames created by prefixing a 28-byte FC Encapsulation Header to the incoming SOF/EOF delimited FC frame (see RFC 3643). FC-BB_IP devices are not required to interpret the data content of the FC frames other than capturing and recording their SOF/EOF identities in the encapsulated FC frame. As such, FC Sequences and Exchanges are not visible to the FC-BB_IP protocol. All encapsulated FC frames are transparently transported over the IP network.

FC-BB_IP devices also exchange SW_ILS control information using Class F FC frames (see Figure 15 and Figure 18). These FC frames are encapsulated and tunneled in the same way as the incoming FC frames.

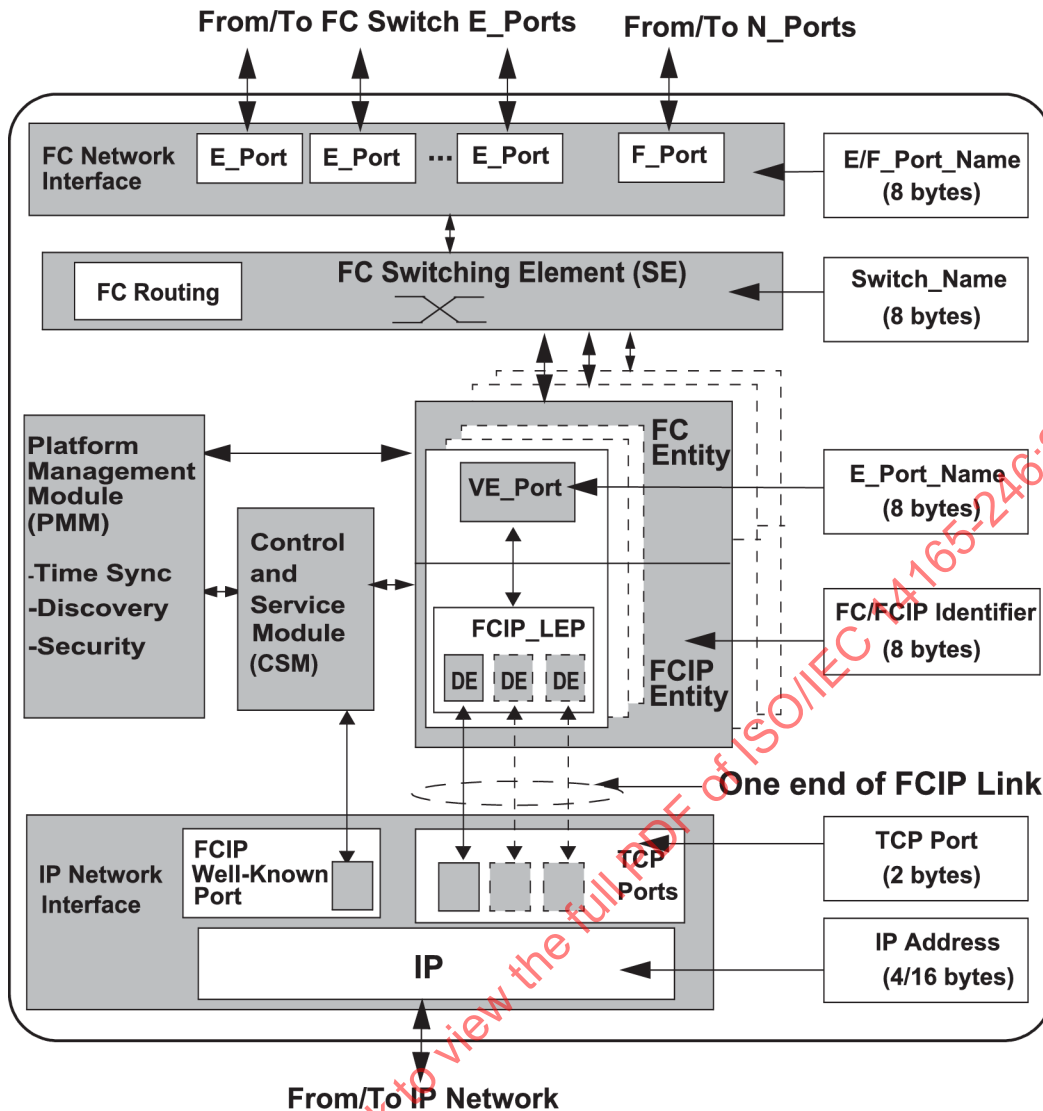
Encapsulated FC frames join the TCP byte stream in order (see Figure 21). TCP segments are created from TCP byte streams without any visibility or regard to encapsulated FC frame boundaries.

TCP flow control between two FC-BB_IP devices provides a reliable transport of encapsulated FC frames across the IP network. The only delivery order guarantee provided by TCP with respect to the FCIP protocol is the correctly ordered delivery of encapsulated FC frames within a single TCP connection. The FC Entity is expected to specify and handle all other FC frame delivery ordering requirements.

5.3 VE_Port functional model

5.3.1 FC-BB_IP interface protocol layers

Figure 13 shows the VE_Port functional model of an FC-BB_IP device that consists of the E_Port/F_Port FC interface, the FC-BB_IP interface, and the IP network interface.



Note: DE within each FCIP_LEP represents an FCIP_DE.

Figure 13 – FC-BB_IP VE_Port functional model

The protocol layers at these interfaces are:

- an E_Port/F_Port FC interface: FC-0, FC-1, and FC-2 levels;
- an FC Switching Element (SE) with FC routing;
- an FC-BB_IP protocol interface: FC Entity and FCIP Entity protocol layers; and
- an IP network interface: TCP and IP layers.

Figure 14 illustrates the protocol layers across these interfaces.

5.3.2 E_Port/F_Port FC interface

The FC-BB_IP FC interface supports one or more E_Ports or F_Ports thus requiring the support of the FC-0, FC-1, and FC-2 levels. The E_Ports in general connect to different external FC switches, but connectivity to the same FC switch is also allowed. The data emerging from the FC-2 level is fed into an FC Switching Element.

The initialization of any generic E_Port or F_Port is described in FC-SW-6. An E_Port indicates its support for the ELP/ESC parameters using the ELP/ESC SW_ILS that is capable of parameter negotiation. Since FC-BB-6 does not support Class 1, the Class 1 Port Parameter VAL bit in the ELP shall be set to 0 (i.e., invalid). An ELP received at an E_Port may be rejected using SW_RJT for many reasons (see FC-SW-6).

An E_Port/F_Port is uniquely identified by an 8-byte E_Port_Name/F_Port_Name.

5.3.3 FC Switching Element (SE) with FC routing

The FC Switching Element (SE) switches and routes the incoming FC frames from the E_Port or F_Port to the proper VE_Port (see FC-SW-6). Routing is accomplished with the support of the FSPF routing protocol. Similarly, the FC SE switches and routes the data arriving from a VE_Port to the proper E_Port or F_Port.

The SE is uniquely identified by an 8-byte Switch_Name.

5.3.4 FC-BB_IP protocol interface

5.3.4.1 Major components

The FC-BB_IP protocol has interfaces to the FC network on one side and the IP network on the other. In addition to the two network interfaces, it consists of the following major components:

- a) FC and FCIP Entities;
- b) Control and Service Module (CSM); and
- c) Platform Management Module (PMM).

FC routing occurs at a higher level than IP routing. FC/FCIP Entities themselves do not actively participate in FC frame routing. FC routing uses the FSPF protocol described in FC-SW-6. FSPF routes are mapped onto the FCIP Links interconnecting FC-BB_IP devices. An FC frame's FSPF route decides the selection of the VE_Port/FCIP_LEP pair within a selected FC/FCIP Entity pair if multiple pairs are in use. If multiple DEs within an FCIP_LEP are in use, then the selection of which FCIP_DE to use is described in 5.7.3.5.

5.3.4.2 FC and FCIP Entities

5.3.4.2.1 Function

The FC Entity is the principal interface point to the FC network on one side and, in combination with the FCIP Entity, to the IP network on the other side. The primary functions of the FC Entity are to support one or more VE_Ports and to communicate with the FCIP Entity. The FC Entity layer lies between the FC-2 FC level and the FCIP Entity layer as shown in Figure 14.

The FCIP Entity is the principal interface point to the IP network on one side, and in combination with the FC Entity, to the FC network on the other. The primary function of the FCIP Entity is formatting, encapsulating, and forwarding encapsulated FC frames across the IP network interface.

The FC/FCIP Entity pair interfaces with the CSM and the PMM through a vendor-specific mechanism.

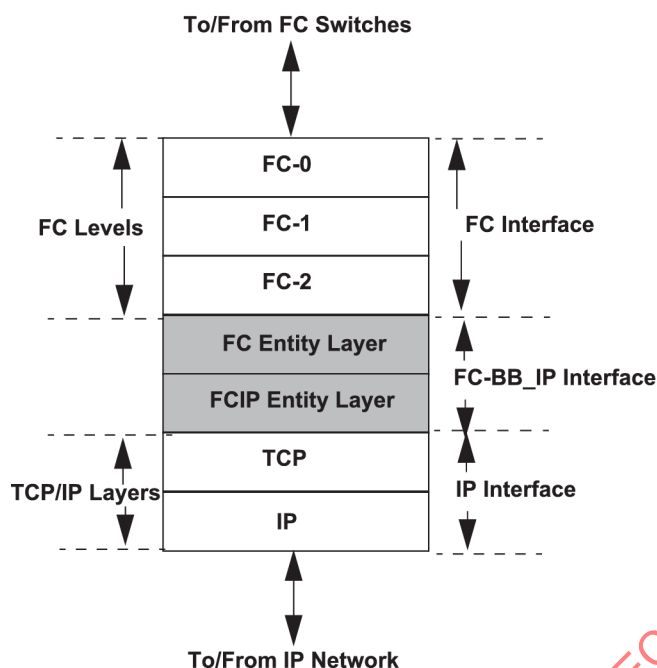


Figure 14 – FC-BB_IP Protocol Layers

5.3.4.2.2 FC Entity

The FC-BB_IP interface may support multiple instances of the FC/FCIP Entity pair. Each instance of the FC/FCIP Entity pair consists of one or more VE_Port/FCIP_LEP pairs. A VE_Port interfaces with the FCIP_LEP component of the FCIP Entity.

The VE_Port receives FC frames from the FC side and sends them to the FCIP_LEP for encapsulation and transmission on the IP network. The VE_Port may also exchange Class F control frames with the remote VE_Port via the FCIP_LEPs. There is a one-to-one relationship between a VE_Port and an FCIP_LEP. VE_Ports communicate via VE_Port Virtual ISLs (see 5.3.4.2.4).

NOTE 3 – The term Virtual ISL if used unqualified refers to both a VE_Port Virtual ISL and a B_Access Virtual ISL.

A VE_Port is uniquely identified by an 8-byte E_Port_Name.

Within an FC-BB_IP device, each FC/FCIP Entity pair instance is uniquely identified by an 8-byte identifier called the FC/FCIP identifier. The FC/FCIP identifier uses the Name_Identifier format.

Initialization at the FC-BB-6 protocol interface occurs between VE_Ports in a manner identical to standard E_Ports and is described in 5.3.4.3.

5.3.4.2.3 FCIP Entity

The FCIP_LEP is a component of the FCIP Entity that formats, encapsulates, and forwards encapsulated FC frames. Encapsulated FC frames are sent as TCP segments over the IP network.

The FCIP_LEP receives byte-encoded SOF/EOF delimited FC frames and a timestamp (see 5.3.4.5.2.2) from its VE_Port. The FCIP Data Engine (FCIP_DE) is the data forwarding component of the FCIP_LEP. The FCIP_DE handles all encapsulation and decapsulation, and transmission and reception of the encapsulated FC frames on the FCIP Link. The FCIP_LEP contains one or more FCIP_DEs, each corresponding to a TCP connection.

The FCIP_DE has four interface points (see RFC 3821):

- a) **FC Receiver Portal:** the access point through which a byte-encoded SOF/EOF delimited FC frame and timestamp enters an FCIP_DE from the VE_Port;
- b) **FC Transmitter Portal:** the access point through which a reconstituted byte-encoded SOF/EOF delimited FC frame and timestamp leaves an FCIP_DE to the VE_Port;
- c) **Encapsulated Frame Receiver Portal:** the TCP access point through which an encapsulated FC frame is received from the IP network by the FCIP_DE; and
- d) **Encapsulated Frame Transmitter Portal:** the TCP access point through which an encapsulated FC frame is transmitted to the IP network by the FCIP_DE.

5.3.4.2.4 VE_Port Virtual ISL and FCIP Link

The FC/FCIP Entity pair provides a data forwarding path between itself and a remote FC/FCIP Entity pair via virtual constructs. Two types of virtual constructs are defined:

- a) a VE_Port Virtual ISL is a logical construct that is created between two FC Entity VE_Ports for the explicit purpose of sending and receiving byte-encoded SOF/EOF delimited FC frames via the FCIP Entity. Conceptually, communication between two VE_Ports is similar to communication between E_Ports; and
- b) an FCIP Link is a logical construct that is created between two FCIP Entity LEPs for the explicit purpose of sending and receiving encapsulated FC frames and encapsulated FCIP control information. Conceptually, communication between two LEPs is similar to the communication between two instances of a TCP application.

There is a one-to-one mapping between a VE_Port Virtual ISL and an FCIP Link. Each FCIP Link consists of one or more TCP connections, all between the same two FC-BB_IP devices. Although more than one FCIP Link may be formed between a pair of FC-BB_IP devices, a typical configuration may only consist of a single FCIP Link. See Figure 20 for some examples of allowed network topologies.

The FCIP_LEP that originates an FCIP Link is defined as the FCIP Link Originator. The corresponding FCIP_LEP that accepts this link is defined as the FCIP Link Acceptor. An FCIP Link is fully characterized by its FCIP Link Originator and FCIP Link Acceptor identities. An FCIP Link Originator or FCIP Link Acceptor is fully identified by all of the following:

- a) an 8-byte Switch_Name;
- b) an 8-byte E_Port_Name; and
- c) an 8-byte FC/FCIP Entity identifier.

To uniquely identify an FCIP Link, all of the following are required:

- a) the 8-byte Switch_Name of the FCIP Link Originator;
- b) the 8-byte E_Port_Name of the FCIP Link Originator;
- c) the 8-byte FC/FCIP Entity identifier of the FCIP Link Originator; and
- d) the 8-byte Switch_Name of the FCIP Link Acceptor.

The FCIP Link Acceptor's 8-byte FC/FCIP Entity identifier and its E_Port_Name provide additional information about an FCIP Link but are not required to uniquely identify it.

5.3.4.3 VE_Port Virtual ISL exchanges

5.3.4.3.1 SW_ILS exchanges

VE_Ports exchange SW_ILSs on the VE_Port Virtual ISL. The SW_ILSs that occur on the VE_Port Virtual ISL are the standard E_Port SW_ILSs (e.g., ELP, ESC, EFP, etc.), and in addition the LKA ELS (see FC-LS-3). Figure 15 shows the scope of the VE_Port Virtual ISLs.

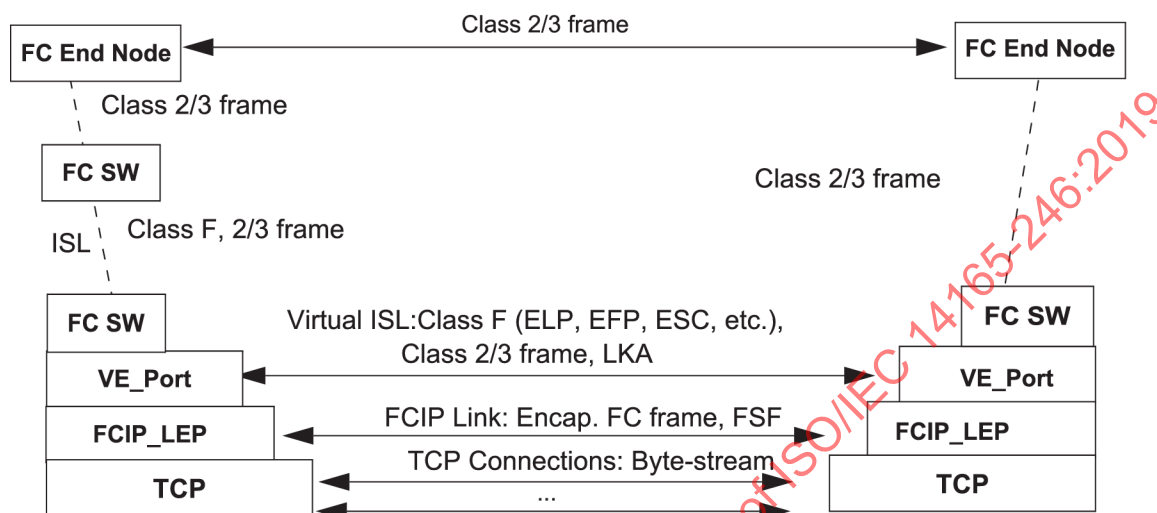


Figure 15 – Scope of VE_Port Virtual ISL

5.3.4.4 Control and Service Module (CSM)

The CSM is a control component of the FC-BB_IP interface that mainly deals with connection management. The CSM creates the FC/FCIP Entity pair during the Virtual ISL/FCIP Link setup. The CSM processes all requests for a link setup via the FCIP-registered TCP Port 3225 or optionally another TCP Port. The CSM processes requests to add additional TCP connections over the same FCIP Link. The CSM is also responsible for tearing down existing FCIP Links and TCP connections and deleting the FC/FCIP Entity pair.

NOTE 4 – Some aspects of the CSM functions are discussed only in RFC 3821.

5.3.4.5 Platform Management Module (PMM)

5.3.4.5.1 Function

The PMM is a management component of the FC-BB_IP interface that handles time synchronization, discovery, and security. The PMM is also the intended component for any miscellaneous housekeeping functions such as maintenance of event logs (see 5.8.3.5).

5.3.4.5.2 Time synchronization

5.3.4.5.2.1 FCIP Transit Time (FTT)

FCIP Transit Time (FTT) is defined as the total transit time of an encapsulated Fibre Channel frame in the IP network. The FCIP Transit Time is calculated by subtracting the timestamp value in the arriving encapsulated FC frame from the synchronized time in the FCIP Entity.

5.3.4.5.2.2 Building outgoing FC frame encapsulation headers

The FC Entity shall establish and maintain a synchronized time value in Simple Network Time Protocol (SNTP) Version 4 format (see RFC 2030) for use in computing the IP network transit times. The FC Entity shall use suitable internal clocks and one of the following mechanisms to establish and maintain the synchronized time value:

- a) Fibre Channel time services; or
- b) IP network SNTP server(s).

Each byte-encoded SOF/EOF delimited FC frame that the FC Entity delivers to the FCIP_DE through the FC receiver portal shall be accompanied by a timestamp value obtained from the synchronized time service. The FCIP_DE places the timestamp in the encapsulation header part of the encapsulated FC frame that carries the FC frame (see RFC 3643). If no synchronized timestamp value is available to accompany an entering Class 2 or Class 3 FC frame, then the frame should not be delivered to the FCIP_DE. However, FC-BB_IP shall allow any class F encapsulated FC frames to be transmitted with a zero timestamp value.

5.3.4.5.2.3 Checking IP network transit times in incoming FC frame encapsulation headers

Each byte-encoded SOF/EOF delimited FC frame delivered to the FC Entity through the FCIP_DE FC transmitter portal is to be accompanied by the timestamp value taken from the encapsulation header of the encapsulated FC frame. As noted in 5.3.4.5.2.2, the timestamp may be zero indicating that no valid timestamp was supplied by the sending FC Entity. Any frame other than a Class F frame whose timestamp is zero shall be discarded. A Class F frame whose timestamp is zero shall be processed as if it met all Fibre Channel timeout requirements.

If the timestamp is non-zero, then the FTT of the arriving encapsulated Fibre Channel frame shall be compared to $1/2 E_D_TOV$. If the FTT exceeds $1/2 E_D_TOV$, then the frame shall be discarded. Otherwise the frame shall be processed normally. Fibre Channel timeout values shall be administratively set to accommodate the FTT.

5.3.4.5.3 Discovery

Discovery of FC-BB_IP devices is handled in accordance with the procedures outlined in 5.7.2.2 and in RFC 3821 and RFC 3822.

5.3.4.5.4 Security

Security in FC-BB_IP is defined at two levels, FC and FCIP. The FC level is secured through FC-SP-2 mechanisms that are extended by FC-BB_IP. The FCIP level is secured through IPSec mechanisms (see RFC 3821). Figure 16 illustrates the scope of the two security mechanisms.

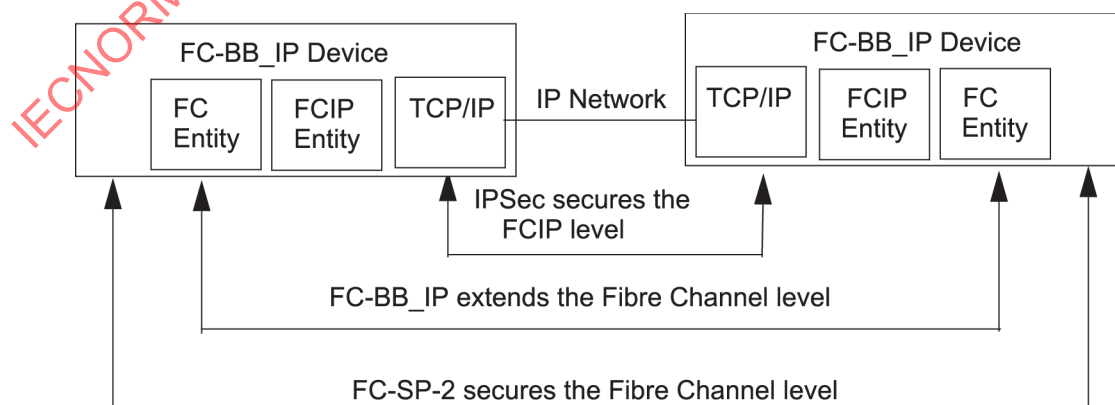


Figure 16 – Security layers

In most cases, the security requirements of an FC/FCIP Entity pair are satisfied outside the scope of this standard as follows:

- a) security for the FC Fabric is provided by the FC-SP-2 capabilities (e.g., switch-to-switch authentication, frame authentication and confidentiality); and
- b) security for the TCP connections used to transit the IP network is provided by the security features described in FCIP (e.g., IPsec packet authentication and confidentiality).

Depending on the security requirements of a given configuration, any or all of the security capabilities described in other standards may be enabled or disabled. However, it is important to note that the public IP network is subject to a large variety of security attacks, meaning that serious consideration should be given to enabling the full suite of security features described in RFC 3821 whenever the public IP network is to be used to transmit FCIP frames.

An FC/FCIP entity pair has a potential security vulnerability where interactions may not be fully secured by either the FC-SP-2 or FCIP security features. This vulnerability occurs if two or more TCP connections are aggregated in a single FCIP Link. The first TCP connection in an FCIP Link and its associated Virtual ISL may be authenticated using the FC-SP-2 mechanisms. However, no such authentication is defined for subsequent TCP connections, since to FC they all appear to be part of an already authenticated Virtual ISL.

To prevent attacking entities in the IP network from forging additional invalid TCP connections, the FC-BB_IP mechanism described in 5.7.2.3 extends the protection of FC-SP-2 authentication to subsequently-added TCP connections. The extension to FC-SP-2 authentication described in 5.7.2.3.2 is based on the exchange of Class F requests and responses between FC Entities. This mechanism works in concert with the FC-SP-2 Virtual ISL authentication mechanism, handling the Class F requests and responses over a previously authenticated TCP connection. In some configurations, this overhead may be unnecessary. However, in cases where Fabric entities are capable of being authenticated without having their behavior fully trusted, the extension to FC-SP-2 authentication should be used in combination with other FC-SP-2 and FCIP security mechanisms to assure trustworthy formation of FCIP Links and Virtual ISLs.

5.3.5 IP network interface

The FC-BB_IP VE_Port reference model supports one logical IP interface and allows sharing a 4-byte IPv4 or 16-byte IPv6 address in the following ways:

- a) a single IP address per FC-BB_IP device (i.e., a single IP address shared by all FC/FCIP Entity pairs);
- b) multiple IP addresses per FC-BB_IP device (i.e., a single IP address per FC/FCIP Entity pair);
- c) multiple IP addresses per FC/FCIP Entity pair (i.e., single IP address per VE_Port/FCIP_LEP pair); and
- d) multiple IP Addresses per FCIP Link (i.e., a single IP address per TCP Port).

Use of different IP address schemes at the two ends of an FCIP Link is not expected to cause inter-operability problems.

As shown in Figure 14, the IP network interface consists of the TCP and IP layers. The encapsulated FC frame emerging from the FCIP_DE interfaces with the TCP layer. The IP layer interfaces with the TCP layer above it and the IP network below it. The TCP layer supports multiple TCP connections, each corresponding to an FCIP_DE. Each TCP connection within an FCIP Link is assigned a unique local TCP Port Number. Either the FCIP well-known TCP Port 3225 or optionally another TCP Port is used for accepting connection requests. These ports interface with the CSM through a vendor-specific mechanism.

IP routing occurs inside the IP network. Within the IP network, the route taken by an encapsulated FC frame is determined by the normal routing procedures of the IP network.

5.4 B_Access functional model

5.4.1 FC-BB_IP interface protocol layers

Figure 17 shows the functional model of an FC-BB_IP device that consists of the B_Port FC interface, the FC-BB_IP protocol interface, and the IP network interface. Figure 14 shows the details of the protocol layers across these interfaces.

NOTE 5 – Because of the similarity between the E_Port and B_Port functional models this subclause only describes any unique definitions for the B_Access. Other definitions and descriptions from 5.3 apply equally well and remain unchanged.

5.4.2 B_Port FC interface

The FC-BB_IP FC network interface supports one or more B_Ports thus requiring the support of the FC-0, FC-1, and FC-2 levels. B_Ports in general connect to different external FC switches, but connectivity to the same FC switch is allowed.

B_Ports are uniquely identified by an 8-byte B_Port_Name.

5.4.3 FC-BB_IP protocol interface

5.4.3.1 Major components

The B_Port FC-BB_IP interface consists of all the components of the VE_Port functional model (see 5.3.4.1) except the FC Switching Element with FC routing.

5.4.3.2 FC and FCIP Entities

5.4.3.2.1 Function

The primary function of the FC Entity is to support one or more B_Access portals and to communicate with the FCIP Entity.

The function of the FCIP Entity is identical to its function in the VE_Port functional model described in 5.3.4.2.3.

The FC/FCIP Entity pair interfaces with the CSM and the PMM through a vendor-specific mechanism.

5.4.3.2.2 FC Entity

The FC-BB_IP interface may support multiple instances of the FC/FCIP Entity pairs. Each instance of the FC/FCIP Entity pair consists of one or more B_Access/FCIP_LEP pairs. A B_Access portal is a component of the FC Entity that interfaces with the FCIP_LEP component of the FCIP Entity. The B_Access portal receives FC frames from the B_Port and sends them to the FCIP_LEP for encapsulation and transmission on the IP network. The B_Access portal may also exchange Class F control frames with the remote B_Access portal via the LEPs. There is a one-to-one relationship between a B_Access portal and an FCIP_LEP. B_Access portals communicate via B_Access Virtual ISLs (see 5.4.3.2.4).

There is no switching and routing required in the case of the B_Port functional model. However, the forwarding of FC frames across the B_Access/FCIP_LEP pair is still required. If multiple DEs within an FCIP_LEP are in use, then the selection of which FCIP_DE to use is described in 5.7.3.5.

Initialization at the FC-BB-6 protocol interface occurs with EBP SW_ILS exchanges between B_Access portals in a manner identical to standard E_Ports and is described in 5.4.3.2.4. The B_Access initialization state machine is described in 5.4.3.3.1.1.

5.4.3.2.3 FCIP Entity

The FCIP_LEP receives byte-encoded SOF/EOF delimited FC frames and a timestamp from its B_Access portals. All other functions are identical to the functions of the FCIP Entity in the VE_Port functional model (see 5.3.4.2.3).

5.4.3.2.4 B_Access Virtual ISL and FCIP Links

A B_Access Virtual ISL is a logical construct that is created between two FC Entity B_Access portals for the explicit purpose of sending and receiving byte-encoded SOF/EOF delimited FC frames via the FCIP Entity. Conceptually, communication between two B_Access portals is similar to communication between two VE_Ports.

There is a one-to-one mapping between a B_Access Virtual ISL and an FCIP Link.

An FCIP Link Originator or FCIP Link Acceptor is fully identified by all of the following:

- a) an 8-byte Fabric_Name;
- b) an 8-byte B_Access_Name; and
- c) an 8-byte FC/FCIP Entity identifier.

To uniquely identify an FCIP Link, the following items are required:

- a) the 8-byte Fabric_Name of the FCIP Link Originator;
- b) the 8-byte B_Access_Name of the FCIP Link Originator;
- c) the 8-byte FC/FCIP Entity identifier of the FCIP Link Originator; and
- d) the 8-byte Fabric_Name of the FCIP Link Acceptor.

The FCIP Link Acceptor's 8-byte FC/FCIP Entity identifier and its B_Access_Name provide additional information about an FCIP Link but are not required to uniquely identify it.

The FC-BB_IP B_Access functional model is shown in Figure 17.

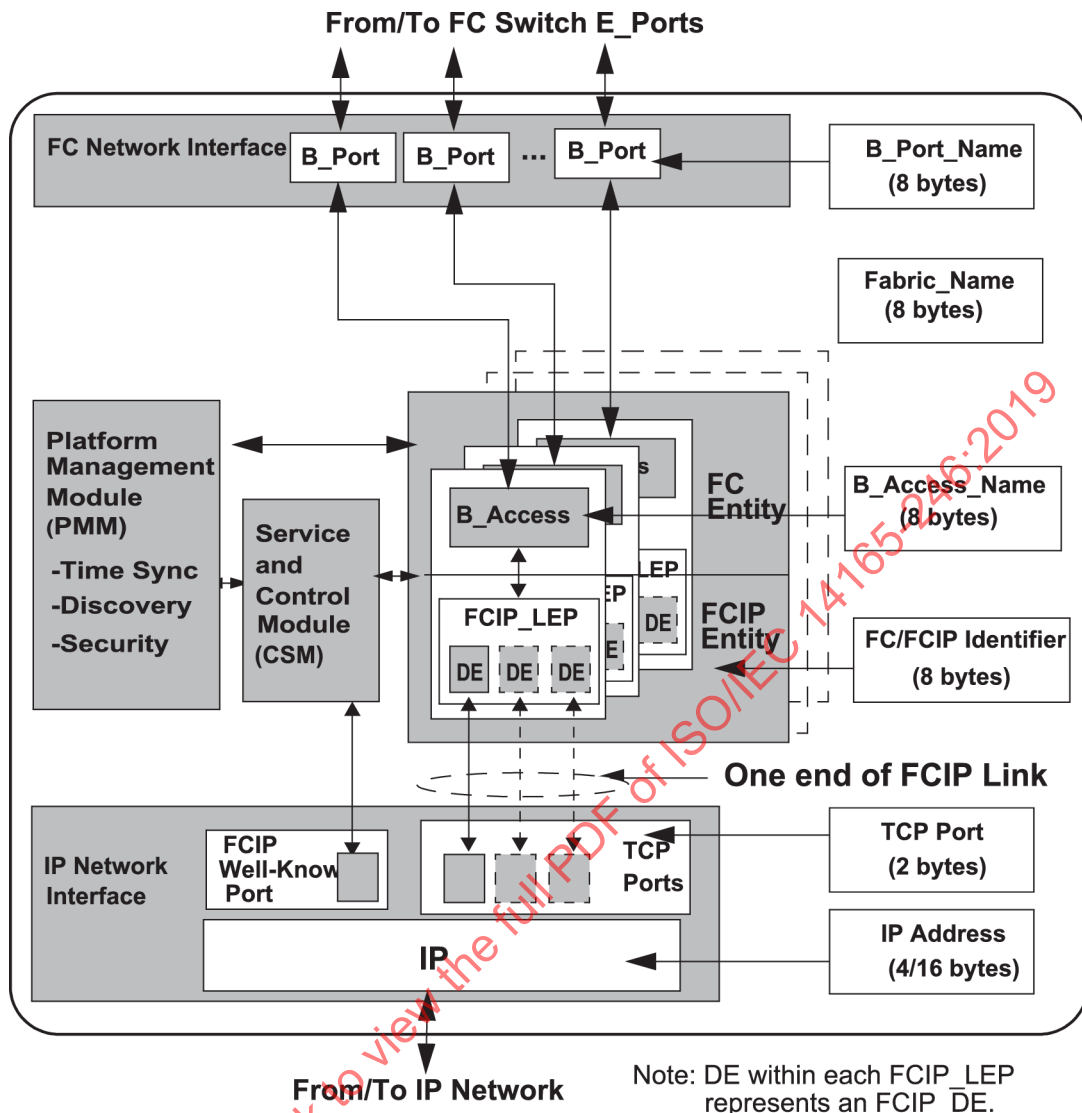


Figure 17 – FC-BB_IP B_Access functional model

5.4.3.3 B_Access Virtual ISL exchanges

5.4.3.3.1 Exchange B_Access Parameters (EBP) SW_ILS

B_Access portals exchange SW_ILSs on the B_Access Virtual ISL. The SW_ILSs that occur on the B_Access Virtual ISL are the EBP SW_ILS and the LKA ELS (see FC-LS-3). Figure 18 shows the scope of the B_Access Virtual ISLs.

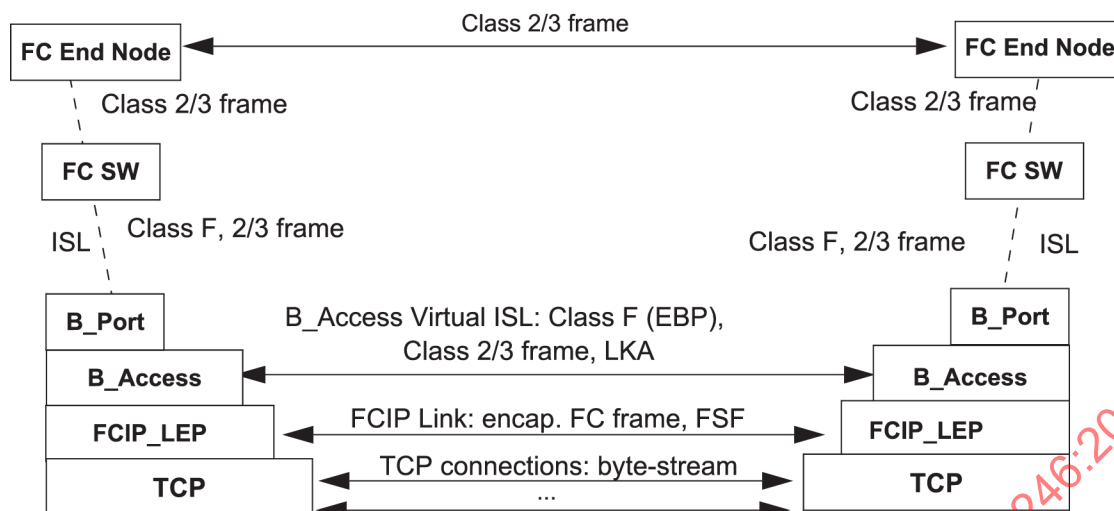


Figure 18 – Scope of B_Access Virtual ISL

The Exchange B_Access Parameters (EBP) Switch Fabric Internal Link Service (SW_ILS) is sent by a B_Access portal to a remote B_Access portal in order to establish operating link parameters and port capabilities for the B_Access Virtual ISL formed by the two B_Access portal peers. Successful acceptance of EBP SW_ILS shall be completed before the B_Ports begin switch port mode initialization.

Protocol:

- Exchange B_Access Parameters (EBP) request Sequence; and
- Reply Switch Fabric Internal Link Service Sequence.

Addressing: For use in switch port configuration, the S_ID field shall be set to FFFFFDh, indicating the Fabric Controller of the originating B_Access portal. The D_ID field shall be set to FFFFFDh, indicating the Fabric Controller of the destination B_Access portal.

Payload: The format of the EBP request payload is shown in Table 5.

Table 5 – EBP request payload

Item	Size Bytes	Remarks
28 01 00 00h	4	
R_A_TOV	4	Value in milliseconds
E_D_TOV	4	Value in milliseconds
K_A_TOV	4	Value in milliseconds
Requester B_Access_Name	8	
Class F Service Parameters	16	

Requester B_Access_Name: This field shall contain the B_Access_Name of the device that originated the EBP request.

R_A_TOV: This field shall be set to the value, in milliseconds, of R_A_TOV required by the FC-BB_IP device.

E_D_TOV: This field shall be set to the value, in milliseconds, of E_D_TOV required by the FC-BB_IP device.

K_A_TOV: This field shall be set to the value, in milliseconds, of K_A_TOV required by the FC-BB_IP device.

Class F Service Parameters: This field shall contain the B_Access Class F Service Parameters and its format is identical with its use in the ELP SW_ILS (see FC-SW-6).

Reply Switch Fabric Internal Link Service Sequence:

Service Reject (SW_RJT)

Accept (SW_ACC)

Accept payload

Payload: The format of the EBP Accept payload is shown in Table 6.

Table 6 – EBP accept payload

Item	Size Bytes	Remarks
02 00 00 00h	4	
R_A_TOV	4	Value in milliseconds
E_D_TOV	4	Value in milliseconds
K_A_TOV	4	Value in milliseconds
Responder B_Access_Name	8	
Class F Service Parameters	16	

The fields in Table 6 are the same as defined for Table 5 except for the Responder B_Access_Name field.

Responder B_Access_Name: This field shall contain the B_Access_Name of the remote device that responds to the EBP request.

The SW_RJT reply payload format is given in FC-SW-6. The EBP reject reason code explanation is shown in Table 7.

Table 7 – EBP reject reason code explanation

Encoded Value (Bits 23-16)	Description
0000 0000	No additional explanation
0000 0001	Class F Service Parameter error
0000 0010	Invalid B_Access_Name
0000 0011	K_A_TOV mismatch
0000 0100	E_D_TOV mismatch
0000 0101	R_A_TOV mismatch
others	Reserved

5.4.3.3.1.1 B_Access initialization state machine

The B_Access initialization state machine is shown in Figure 19.

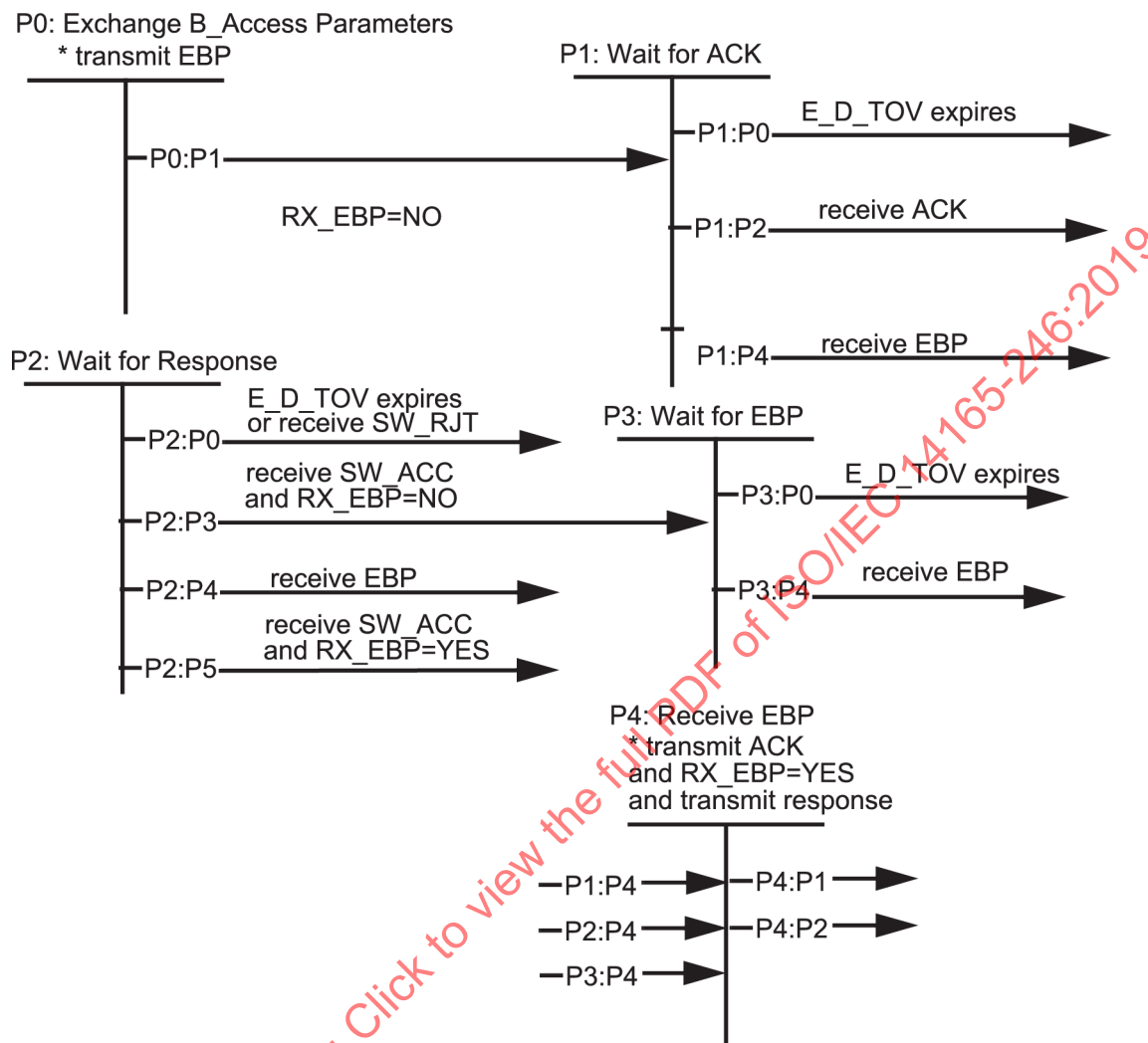


Figure 19 – B_Access initialization state machine

State P0: Exchange B_Access Parameters. This state marks the beginning of the B_Access initialization. Activity other than that described within the state machine is suspended until initialization is complete.

Transition P0:P1. The B_Access resets the RX_EBP flag.

State P1: Wait for ACK. In this state the B_Access waits until an ACK for the B_Access's transmitted EBP is received.

Transition P1:P0. This transition occurs if the B_Access has not received an ACK within E_D_TOV after the transmission of an EBP.

Transition P1:P2. This transition occurs if the B_Access receives an ACK before E_D_TOV expires.

Transition P1:P4. This transition occurs if the B_Access receives an EBP while waiting for an ACK.

State P2: Wait for Response. In this state the B_Access has received an ACK for its EBP and is waiting for a response.

Transition P2:P0. This transition occurs if the B_Access has not received a response within E_D_TOV after the transmission of an EBP or receives an SW_RJT.

Transition P2:P3. This transition occurs if the B_Access receives an SW_ACC and has not received an EBP.

Transition P2:P4. This transition occurs if the B_Access receives an EBP while waiting for a response.

Transition P2:P5. This transition occurs if the B_Access receives an SW_ACC and has received an EBP.

State P3: Wait for EBP. In this state the B_Access has received an ACK for its EBP and is waiting for an EBP.

Transition P3:P0. This transition occurs if the B_Access has not received an EBP within E_D_TOV of the transmission of an EBP.

Transition P3:P4. This transition occurs if a B_Access receives an EBP while waiting for a response.

State P4: Receive EBP. In this state the B_Access has received an EBP. The B_Access responds with an ACK and transmits an SW_ACC or SW_RJT depending upon whether or not the received configuration parameters contained within the EBP are acceptable. The B_Access sets RX_EBP to indicate an EBP has been received and is accepted.

Transition P4:P1. This transition occurs if a B_Access receives an EBP from its peer yet hasn't received an ACK for a previously transmitted EBP.

Transition P4:P2. This transition occurs if a B_Access receives an EBP from its peer yet hasn't received a response for a previously transmitted EBP.

5.4.3.4 B_Port Control and Service Module (CSM)

The B_Port CSM is identical to the E_Port CSM described in 5.3.4.4.

5.4.3.5 B_Port Platform Management Module (PMM)

The B_Port PMM is identical to the E_Port PMM described in 5.3.4.5.

5.4.4 IP Network Interface

The B_Port IP network interface is identical to the E_Port IP network interface described in 5.3.5 with a change in item (c), where a single IP address is per B_Access/FCIP_LEP pair.

5.5 FC-BB_IP network topologies

Figure 20 shows some example FC-BB_IP network topologies that exists between three FC-BB_IP sites:

- a) FCIP Link 1 connects Sites 1 and 2 and consists of three TCP connections;
- b) FCIP Link 2 connects Sites 1 and 2 and consists of two TCP connections. FCIP Link 2 however is distinct from Link 1 although it exists between the same two FC/FCIP Entity pairs (i.e., FC/FCIP_Entity_1 and FC/FCIP_Entity_2);
- c) FCIP Link 3 connects Sites 1 and 3 and consists of two TCP connections. FCIP Link 3 exists between FC/FCIP_Entity_3 and FC/FCIP_Entity_5; and
- d) FCIP Link 4 connects Sites 2 and 3 and consists of one TCP connection. FCIP Link 4 exists between FC/FCIP_Entity_4 and FC/FCIP_Entity_6.

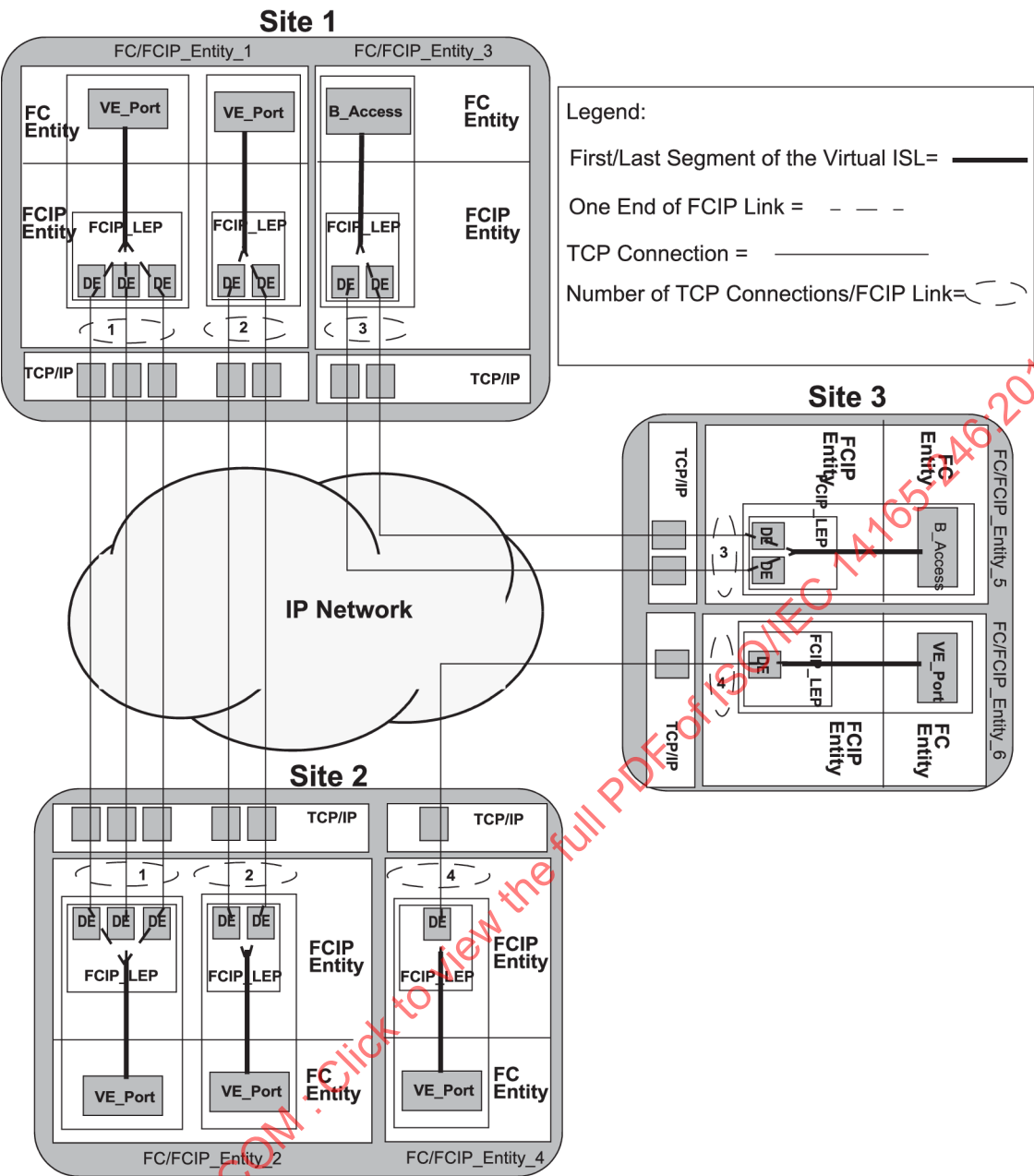


Figure 20 – FC-BB_IP network topologies

5.6 Mapping and message encapsulation using TCP/IP

5.6.1 Encapsulated frame structures

5.6.1.1 FC frame encapsulation structure

An encapsulated FC frame is carried in one or more TCP segments as shown in Figure 21. Each segment's format is shown in Table 8. The structure of each encapsulated FC frame is shown in Table 9 and consists of a FC Encapsulation Header and a byte-encoded SOF/EOF delimited Class 2, 3, or F FC frame as described in RFC 3643.

Table 8 – TCP/IP Segment structure carrying encapsulated FC frame

Field	Sub-field	Size (Bytes)
IP Header		Min:20 Max:40
TCP Header		Min:20 Max:40
TCP payload	one or more portions of encapsulated FC frames	Min:64 Max:2176

Table 9 – Encapsulated FC frame structure

Field	Size (Bytes)
FC Encapsulation Header	28
SOF	4
FC-Header	24
FC frame payload (includes optional header)	Min: 0 Max:2112
CRC	4
EOF	4

FC frame encapsulation (see RFC 3643) describes the structures of the 4-byte SOF/EOF values fields and the FC Encapsulation Header. The FC Encapsulation Header consists of several fields: Protocol#, Version, pFlags, Flags, Frame Length, Timestamp, and CRC.

Protocol#: indicates the FCIP protocol.

Version: indicates the version number.

pFlags: defines flag bits FSF and Ch that distinguish encapsulated FC frames from FCIP originated or echoed control frames.

Flags: the CRCV bit value indicates if the contents of the CRC field are valid or invalid. For FC-BB_IP protocol the CRCV bit shall be zero (i.e., invalid).

Frame Length: contains the length of the entire FC encapsulated frame including the FC Encapsulation Header and the FC frame, including SOF and EOF words.

Timestamp: contains the time at which the FC encapsulated frame was sent as known to the sender. The format of integer and fraction timestamp word values is specified in Simple Network Time Protocol (SNTP) Version 4 (see RFC 2030). The contents of the timestamp integer and timestamp fraction words shall be set as described in 5.3.4.5.2.

CRC: for FC-BB_IP protocol the CRC field shall be set to zero.

5.6.1.2 Encapsulated FCIP Special Frame (FSF) structure

An encapsulated FCIP Special Frame (FSF) is carried as a TCP segment as shown in Table 10. The structure of an encapsulated FSF is shown in Table 11 and consists of an FC Encapsulation Header and an FCIP Special Frame.

Table 10 – TCP/IP Segment structure carrying encapsulated FSF

Field	Sub-field	Size (Bytes)
IP Header		Min:20 Max:40
TCP Header		Min:20 Max:40
TCP payload	encapsulated FSF	76

Table 11 – Encapsulated FSF structure

Field	Size (Bytes)
FC Encapsulation Header	28
FCIP Special Frame (FSF)	48

See 5.6.1.1 for a description of the FC Encapsulation Header structure and format.

The FSF structure is defined in FCIP (see RFC 3821) and consists of several fields: Source FC Fabric_Name, Source FC/FCIP Entity identifier, Connection Nonce, Connection Usage Flags, Connection Usage Code, Destination FC Fabric_Name, and K_A_TOV.

Source FC Fabric_Name: the identifier for the FC Fabric associated with the FC/FCIP Entity pair that generates the FCIP Special Frame. If the FC Fabric is an FC switch, then the field contains the Switch_Name.

Source FC/FCIP Entity identifier: a unique identifier for the FC/FCIP Entity pair that generates the FSF. The value is assigned by the FC Fabric whose name appears in the Source FC Fabric_Name field.

Connection Nonce: contains a 64-bit random number generated to uniquely identify a single TCP connect request in order to provide sufficient security for the nonce, the randomness recommendations described in RFC 3821 should be followed.

Connection Usage Flags: identifies the types of SOF values to be carried on the connection. All or none of the bits corresponding to Class F, 2, or 3 may be set to one. If all of the bits are zero, then the types of FC frames intended to be carried on the connection has no specific relationship to SOF code.

Connection Usage Code: contains Fibre Channel defined information regarding the intended usage of the connection. The FCIP Entity uses the contents of the Connection Usage Flags and the Connection Usage Code fields to locate appropriate QoS settings in the shared database of TCP connection information and apply those settings to a newly formed connection. All values for this field are reserved.

Destination FC Fabric_Name: may contain the Fibre Channel identifier for the FC Fabric associated with the FC/FCIP Entity pair that echoes, as opposed to generates, the FSF.

K_A_TOV: contains the FC Keep Alive Timeout value to be applied to the new TCP connection.

5.6.2 TCP/IP encapsulation

Figure 21 illustrates the TCP/IP encapsulation of an encapsulated FC frame. The TCP/IP encapsulation of an encapsulated FSF is similar.

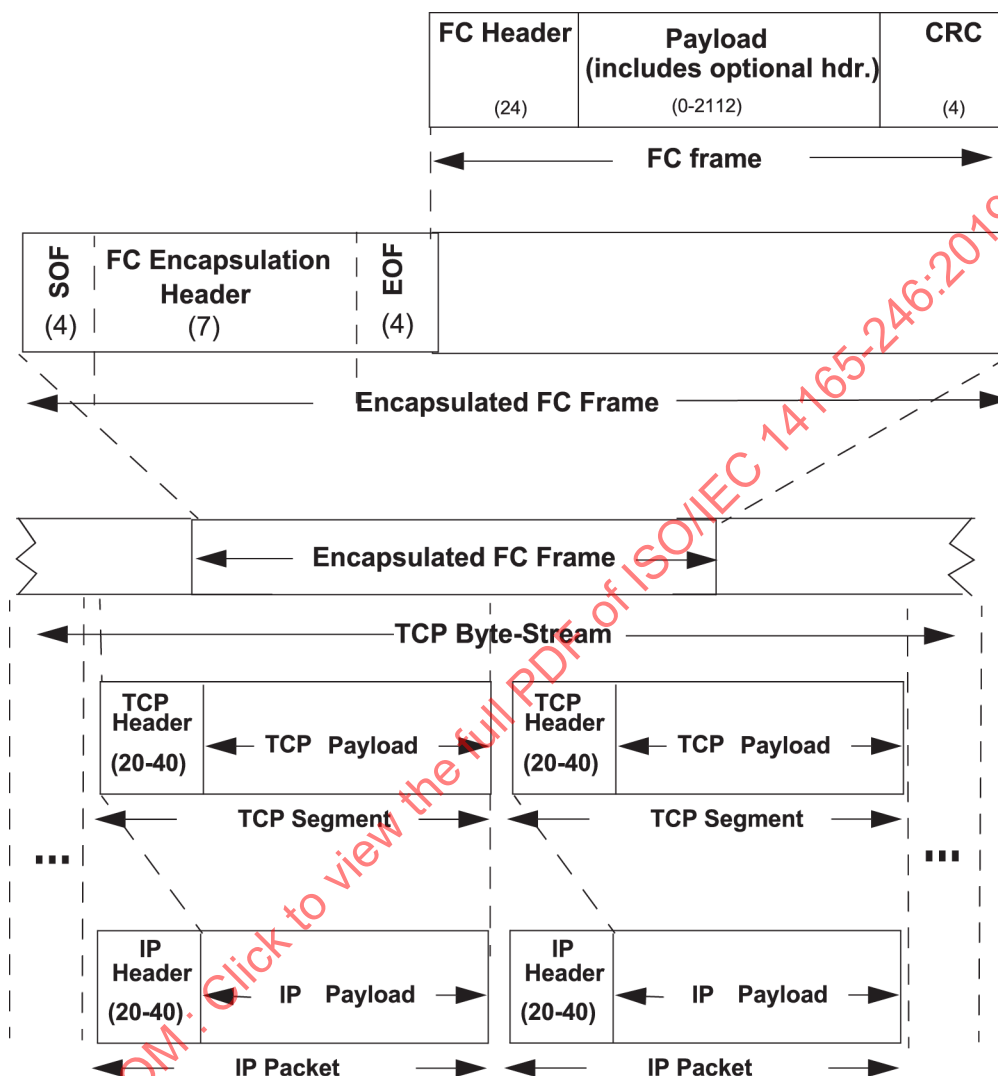


Figure 21 – TCP/IP encapsulation of an encapsulated FC frame

5.7 FC-BB_IP protocol procedures

5.7.1 Overview

This subclause describes the FC-BB_IP protocol procedures for platform management (see 5.7.2), connection management (see 5.7.3), and error detection and recovery (see 5.7.4). There are no specific procedures defined for housekeeping functions such as maintenance of error or event logs.

5.7.2 Procedures for platform management

5.7.2.1 Function

Platform management has three main functions, discovery, security, and time synchronization.

5.7.2.2 Procedures for discovery

Device discovery is one of the functions of the Platform Management Module (PMM). Each FC-BB_IP device is statically or dynamically configured with a list of IP addresses and other identifiers (e.g., N_Port_Names) corresponding to participating FC/FCIP Entities. If dynamic discovery of participating FC-BB_IP devices is supported, then the function is performed using Service Location Protocol version 2 (see RFC 3822).

FC/FCIP Entities themselves do not actively participate in the discovery of FC source and destination identifiers. Discovery of FC addresses accessible via the FC/FCIP Entity is provided by techniques and protocols within the FC architecture as described in FC-FS-4 and FC-SW-6.

5.7.2.3 Procedures for extending FC-SP-2 security

5.7.2.3.1 Authentication mechanisms

The Platform Management Module (PMM) is responsible for extending security at the FC level.

Entity authentication occurs at the FCIP and FC levels as illustrated in Figure 16. Authentication mechanisms at the FCIP level are defined in FCIP (see RFC 3821). Authentication mechanisms at the FC level are defined in FC-SP-2.

During initialization of a Virtual ISL, each switch may authenticate the other switch with FC-SP-2 authentication mechanisms. FC-BB_IP provides for extending the protection of FC-SP-2 authentication to subsequently added TCP connections via either the ASF SW_ILS described in 5.7.2.3.2 or vendor-specific configuration information.

NOTE 6 – The unqualified use of the term Virtual ISL refers to both VE_Port Virtual ISL and B_Access Virtual ISL.

If an FCIP Entity receives a TCP connect request for an additional TCP connection to an existing FCIP Link to which FC-SP-2 authentication has been applied, then the FCIP Entity generates a request to the FC Entity to authenticate the additional TCP connection including at least the following information:

- a) Connection Nonce;
- b) Destination FC Fabric_Name;
- c) Connection Usage Flags; and
- d) Connection Usage Code.

If FC-SP-2 authentication procedures are not being applied to the Virtual ISL, then the FC Entity shall respond to the FCIP Entity indicating that the new TCP is authentic.

NOTE 7 – If the first TCP connection in a Virtual ISL is not authenticated using the applicable FC-SP-2 procedures, then no security is gained by authenticating other TCP connections.

NOTE 8 – The preferred security mechanism for the Public Internet IP network is the success or failure of an ASF SW_ILS.

5.7.2.3.2 Authenticate Special Frame (ASF)

The Authenticate Special Frame (ASF) Switch Fabric Internal Link Service (SW_ILS) is used by an FC Entity to authenticate additional TCP connections on existing FCIP Links. To authenticate a new TCP connection using the ASF SW_ILS, the FC Entity shall use the information provided by the FCIP Entity to transmit an ASF request on the Virtual ISL to which the new TCP connection is being added using a TCP connection in the Virtual ISL that has already been authenticated.

The FC Entity shall use the information from the new FSF request to populate the fields in the ASF request. The fields are the same as defined for FSF (see 5.6.1.2). The format of the ASF request payload is specified in Table 12.

The FC Entity shall transmit the ASF over the previously authenticated TCP connection. This piggybacking technique authenticates additional TCP connections by riding on top of previously authenticated TCP connections.

An FC Entity that receives an ASF SW_ILS shall verify that the information in the request payload identifies a TCP connection initiated by that FC/FCIP Entity pair. If it verifies that this information is sound the FC Entity shall respond with an SW_ACC (see Table 13), otherwise it shall respond with an SW_RJT with a reason code of "Unable to perform command request" and a reason code explanation of "Class F Service Parameter error".

Protocol:

- a) Authenticate Special Frame (ASF) request Sequence; and
- b) Reply Switch Fabric Internal Link Service Sequence.

Addressing: The S_ID field shall be set to FFFFFFFDh, indicating the Fabric Controller of the originating FC Entity. The D_ID field shall be set to FFFFFFFDh, indicating the Fabric Controller of the receiving FC Entity.

Table 12 – ASF request payload

Item	Size Bytes
28 03 00 00h	4
Destination FC Fabric_Name	8
Connection Nonce	8
Connection Usage Flags	1
Reserved	1
Connection Usage Code	2
Reserved	4

Payload: The format of the ASF request payload is specified in Table 12.

Destination FC Fabric_Name: This field is the Fabric_Name of the destination switch and is the Source FC Fabric_Name from the FSF frame.

Connection Nonce: This field is the Connection Nonce from the FSF request.

Connection Usage Flags: This field is the Connection Usage Flags from the FSF request and signifies the acceptance of these flags.

Connection Usage Code: This field is the Connection Usage Code from the FSF request and signifies the acceptance of these codes.

Reply Switch Fabric Internal Link Service Sequence:

- a) Service Reject (SW_RJT); or
- b) Accept (SW_ACC).

Accept payload.

Table 13 – ASF accept response payload

Item	Size Bytes
02 00 00 00h	4

Payload: The format of the ASF accept payload is specified in Table 13.

5.7.3 Procedures for connection management

5.7.3.1 Function

The primary function of the Control and Services Module (CSM) is managing connections.

5.7.3.2 Procedures for link setup

In order to realize a Virtual ISL/FCIP Link between two FC-BB_IP endpoints, an FC-BB_IP device establishes TCP connection(s) with its peer FC-BB_IP device.

NOTE 9 – A Virtual ISL exists between two VE_Ports or two B_Access portals and an FCIP Link exists between two FCIP_LEPs. Conceptually, the procedures for establishing these two are identical.

It may also be useful to assign a pool of connections for transmission of high priority and control frames (e.g., Class F) on connections so they do not encounter head-of-line blocking behind Class 2, or Class 3 traffic. The use of multiple connections and policies for distributing frames on these connections is described in 5.7.3.5.

A Virtual ISL/FCIP Link and the two FC-BB_IP device endpoints that are involved become operational only after the first TCP connection is established. The sequence of operations performed in order to establish a Virtual ISL/FCIP Link is as follows:

- 1) the FC-BB_IP device initializes its local resources to enable it to listen to TCP connection requests;
- 2) the FC-BB_IP device discovers the FC-BB_IP device endpoints to which it is able to establish a Virtual ISL/FCIP Link. The result of the discovery shall be, at the minimum, the IP address and the TCP port of the peer endpoint. The discovery process may rely on administrative configuration or on services such as SLPv2 as described in 5.7.2.2;
- 3) the processes defined by FCIP are used to establish TCP connections. FC level authentication of the first TCP connection is accomplished using the mechanisms and management controls described in FC-SP-2. To extend FC-SP-2 authentication to additional TCP connections the mechanisms described in 5.7.2.3 shall be followed;
- 4) at this point, both endpoints have their respective VE_Port/FCIP_LEP pairs or B_Access/FCIP_LEP pairs established;
- 5) after connection establishment, the FC-BB_IP device constructs the encapsulated FC frames according to the methods described in 5.3.4.5.2.2;
- 6) at this point the Virtual ISL endpoints shall exchange FC virtual port initialization frames to enable and identify port operation. The E_Port port mode initialization state machine is described in FC-SW-6 and the B_Access portal initialization state machine is described in 5.4.3.3.1.1. Switch-to-switch authentication shall use FC-SP-2 authentication mechanisms;
- 7) an FC-BB_IP device operates in E_Port or B_Port mode. If operating in E_Port mode, normal FSPF messages are exchanged and the switch port becomes operational. If operating in B_Port mode, it is expected that the external E_Ports may exchange FSPF messages over the Virtual ISL which result in the link becoming operational;

- 8) link costs are implementation-defined;
- 9) in certain deployments, a single FC-BB_IP device may establish Virtual ISLs/FCIP Links with multiple FC-BB_IP device endpoints. In this situation, the FC-BB_IP device shall manage TCP operational parameters independently for each Virtual ISL or FCIP Link. Also, the FC-BB_IP device VE_Port may perform the E_Port initialization independently, for each Virtual ISL/FCIP Link. The B_Access also may perform initialization independently, for each Virtual ISL/FCIP Link; and
- 10) the FC Entity may participate in determining allowed TCP connections, TCP connection parameters, quality of service usage, and security usage by modifying interactions with the FCIP Entity that are modeled as a shared database. See RFC 3821.

5.7.3.3 Procedures for data transfer

The procedures for data transfer are as follows:

- a) the sending FC Entity shall deliver FC frames to the correct FCIP_LEP/FCIP_DE in the correct FCIP Entity;
- b) each FC frame delivered to the FCIP_DE shall be accompanied by a time value synchronized with the clock maintained by the FC Entity at the other end of the FCIP Link (see 5.3.4.5.2.2); and
- c) when FC frames exit FCIP_DE(s) via the FC Transmitter Portal(s), the FC Entity should forward them to the FC Fabric. However, before forwarding the FC frame the FC Entity shall verify the end-to-end transit time as described in 5.3.4.5.2.3.

5.7.3.4 Procedures for FCIP Link disconnection

The FC Entity may require the FCIP Entity to perform TCP close requests (e.g., to perform a controlled shutdown of a link or to respond to high link error rates). If the FC Entity requests the closure of all TCP connections in an FCIP Link, the FCIP Link is disconnected.

If the FCIP Link is disconnected, notification of the disconnection shall be accomplished according to the procedures in 5.8.3.5.

5.7.3.5 Procedures for multiple connection management

A pair of FC-BB_IP device endpoints may establish a number of TCP connections between them. Since a Virtual ISL potentially maps a fairly large number of FC flows, where a flow is defined as a pair of Fibre Channel S_ID/D_ID addresses, it may not be practical to establish a separate TCP connection for each FC flow. However, once an FC flow is assigned to an FCIP_DE within the Virtual ISL, all FC frames of that flow shall be sent on that same FCIP_DE. This rule is in place to honor any in-order delivery guarantees that may have been made between the two end points of the FC flow.

If a TCP connect request is received and that request adds a new TCP connection to an existing FCIP_LEP, the procedures described in 5.7.2.3.1 shall be followed.

5.7.4 Procedures for error detection recovery

5.7.4.1 Procedures for handling invalid FC frames

Data corruption is detected at two different levels, TCP checksum and FC frame encapsulation errors. Data corruption detected at the TCP level shall be recovered via TCP data recovery mechanisms. The recovery for FC frame errors is described below. The TCP and FC frame recovery operations are performed independently.

Fibre Channel frame errors and the expected resolution of those errors are described in RFC 3821 and summarized below:

NOTE 10 – The behavior given below is that of the FCIP Entity.

- a) all incoming frames on the FC receiver port are verified for correct header, proper format, valid length and valid CRC. A frame having an incorrect header or CRC shall be discarded or processed in accordance with the rules for the particular type of FC_Port;
- b) all frames transmitted by the encapsulated frame transmitter are valid FC encapsulations of valid FC frames with correct TCP check sums on the correct TCP/IP connection;
- c) the FC frames contained in incoming encapsulated frames on the encapsulated frame receiver port are verified for a valid header, proper content, proper SOF and EOF values, and valid length. FC frames that are not valid according to those checks are managed according to the following rules:
 - A) the frame may be discarded; or
 - B) the frame may be transmitted in whole or in part by the FC transmitter port and ended with an EOF indicating that the content of the frame is invalid; and
- d) if there is any discrepancy between statements in this subclause and RFC 3821, then RFC 3821 shall prevail.

5.7.4.2 Procedures for error recovery

The FC Entity shall recover from events that the FCIP Entity is unable to handle, such as:

- a) loss of synchronization with FCIP frame headers from the Encapsulated Frame Receiver Portal requiring resetting the TCP connection; and
- b) recovering from FCIP frames that are discarded as a result of synchronization problems.

The FC Entity may recover from connection failures.

Since FC Primitive Signals and Primitive Sequences are not exchanged between FCIP devices, there may be times when an FC frame is lost within the IP network. If this event occurs it is the responsibility of the communicating FC devices to detect and correct the errors based on the features defined in FC-FS-4.

In order to facilitate faster detection of loss of link connectivity, FC Entities shall make use of the Link Keep Alive (LKA) ELS (see FC-LS-3). The LKA ELS is exchanged across the Virtual ISL as shown in Figure 15 (i.e., E_Port implementation) or Figure 18 (i.e., B_Port implementation). The exact number of lost LKA heartbeats that forces the FC Entity to mark the link down is a configurable parameter with a default value of 2. Once the link has been marked down, the FC Entity shall attempt to re-establish the link via the FCIP Entity.

5.7.5 FC-BB_IP system parameters

5.7.5.1 FC timers

FC has two important timeouts, E_D_TOV and R_A_TOV.

E_D_TOV determines the life of an individual FC frame in any particular Fabric element. The effects of E_D_TOV on the Fabric as a whole are typically cumulative since each Fabric element contains its own E_D_TOV timers for any frame received.

R_A_TOV determines the life of an individual FC frame in the Fabric as a whole. For a Fabric, R_A_TOV implies that no particular frame shall remain in, and thus be emitted from, the Fabric after the timer expires.

K_A_TOV is a timer defined in this standard that is used by the Link Keep Alive (LKA) ELS (see FC-LS-3) as a trigger for issuing LKA. The LKA should be sent at least every K_A_TOV if no traffic has been sent and/or received on the connection. The default value for K_A_TOV is $1/2$ E_D_TOV.

5.7.5.2 TCP timers

Given the multitude of current and probable TCP implementations, IETF Requests For Comments related to TCP, applications network requirements, etc., it is impossible to provide even rudimentary guidance in suggesting values for the tunable parameters associated with TCP.

5.7.5.3 Maximum number of attempts to complete an encapsulated FC frame transmission

This is an unspecified parameter and is implementation specific.

5.7.5.4 Maximum number of outstanding encapsulated FC frames

This is an unspecified parameter and is implementation specific.

5.8 FC-BB_IP service considerations

5.8.1 Latency delay

The time required for a frame to pass from one FC-BB_IP device to another across the IP network is variable and beyond the direct control of the FC/FCIP Entity pair. However, the IP network transit time affects the FC Entity's ability to meet FC timeout requirements (e.g., the R_A_TOV requirements of the Fabric). Therefore, the FC Entity is required to use facilities provided by the FCIP Entity to compute the IP network transit time for frames. See 5.3.4.5.2.

Class F frames may be excepted from IP network transit time checking, however, all other classes of frames shall have their IP network transit time computed and checked. If a frame is found to have an IP network transit time that causes the frame's lifetime in the Fabric to exceed FC requirements, then the FC Entity shall discard the frame.

5.8.2 Throughput

5.8.2.1 How timeouts affect throughput

Both FC and TCP timeouts affect throughput as follows:

- a) small R_A_TOV values may cause encapsulated FC frames to be discarded frequently in the FCIP_DE necessitating FC end-node retransmissions;
- b) large TCP timeouts may result in encapsulated FC frames becoming stale in the IP network, leading the FCIP_DE to discard them again necessitating FC end-node retransmissions; and
- c) discarding encapsulated FC frames due to improper settings of timeout values and errors in the IP network lowers the effective throughput.

The FC/FCIP Entities have little or no control over TCP timeouts. The FC/FCIP Entities never initiate retransmissions, that is done either by TCP or by the FC end nodes.

5.8.2.2 How loss affects throughput

TCP retransmissions occur due to loss or corruption of TCP segments. If TCP retransmissions cause the allowed transit time to exceed a threshold, then encapsulated FC frames shall be discarded. Either case is likely to cause the effective throughput to be reduced.

5.8.2.3 Other factors that affect throughput

Throughput may be affected by a mismatch in the effective rates of data transfer across the FC and the IP network interfaces. This mismatch may occur due to differences in the physical line speeds at the FC network and the IP network interfaces or due to the fundamental difference in the two flow control mechanisms.

FC uses BB_Credit flow control and TCP uses a sliding window based flow control. FC-BB_IP does not specify the mechanism that aligns the two flow control schemes, although it is thought that performance may be affected if this aspect is not considered. The FC-BB_IP device needs to ensure that the TCP connections are able to handle the frame arrival rate from the FC Fabric. The FC Entity shall work cooperatively with the FCIP Entity to manage flow control problems in either the IP network or FC Fabric.

In order to achieve better TCP aggregate throughput properties in the face of packet losses, a pair of peer FC-BB_IP devices may use multiple DEs between them, and use appropriate policies for mapping FC frames to these connections.

5.8.3 Reliability

5.8.3.1 Loss of connectivity

The FC-BB_IP device has the capability of detecting loss of connectivity with its remote peer (see 5.7.4.2). Upon detecting a loss of connectivity, an FC-BB_IP device establishes a new connection, or uses an existing TCP connection to the same FC-BB_IP device endpoint. An FC-BB_IP device shall not retransmit an encapsulated FC frame on the new connection. This is to ensure exactly-once delivery semantics to the FC endpoint.

The FC Entity may test for failed TCP connections. Should such a test detect a failed TCP connection, the FC Entity shall disconnect that connection following the procedures in 5.7.3.4.

5.8.3.2 Loss of synchronization

The FC Entity shall recover from events that the FCIP Entity is unable to handle, such as:

- a) loss of synchronization with FC-BB_IP encapsulated FC frame headers from the Encapsulated Frame Receiver Portal requiring resetting the TCP connection; and
- b) recovering from FC-BB_IP encapsulated FC frames that are discarded as a result of synchronization problems (see RFC 3821).

5.8.3.3 Loss or corruption of TCP segments

TCP flow control and error control has mechanisms to detect lost or corrupted TCP segments. TCP retransmits the TCP segments that were lost or corrupted.

TCP flow control provides the ability to regulate the flow of data on the IP network interface based on the perceived IP network congestion conditions, potentially avoiding large losses of data.

5.8.3.4 Loss or corruption of FC frames

The FC interface of the FC-BB_IP device has no mechanisms to detect lost data but only to detect corrupted frames. Corrupted frames detected prior to transmission into the IP network, are discarded and not sent over the IP network.

FC BB_Credit flow control provides the ability to regulate the flow of data on the FC network interface with no loss.

5.8.3.5 FCIP error reporting

The FC Entity receives notifications from the FCIP Entity due to a number of errors detected by the FCIP Entity. As a result, the E_Port implementation of the FC Entity shall report those errors to the local FC switch element via the local VE_Port (see Figure 13). Similarly the B_Port implementation shall report the error to the local B_Access (see Figure 17). In addition, the FC Entity may pass these error reports to the local PMM for inclusion in a local event log.

The FC Entity shall convert the error message received from the FCIP Entity into a Registered Link Incident Report (RLIR) (see FC-LS-3). It is the RLIR that is forwarded from the FC Entity to either the VE_Port (see Figure 13) or B_Access (see Figure 17).

On receipt of the message from the FC Entity, VE_Port or B_Access shall immediately forward the RLIR to the Domain Controller of the Switch.

As a minimum the FC Entity shall accept the following information from the FCIP Entity:

- a) loss of FC frame synchronization (see RFC 3821);
- b) failure to setup TCP connection (see RFC 3821);
- c) duplicate connect request (see RFC 3821);
- d) TCP connect request timeout (see RFC 3821);
- e) successful completion of FC Entity request to close TCP connection (see RFC 3821);
- f) loss of TCP connectivity (see RFC 3821);
- g) excessive number of dropped datagrams (see RFC 3821);
- h) any confidentiality violations (see RFC 3821);
- i) SA parameter mis-match (see RFC 3821); and
- j) LKA timeout notification (see FC-LS-3).

The FC Entity shall generate and forward an RLIR to the management server for the following:

- a) loss of FC frame synchronization (see RFC 3821);
- b) failure to setup additional TCP connection (see RFC 3821); and
- c) additional duplicate TCP connect request (see RFC 3821).

5.8.4 Quality of Service (QoS)

The FC-BB_IP protocol may use TCP/IP QoS features to support FC capabilities.

5.8.5 Delivery order

Each VE_Port/FCIP_LEP pair defines a separate FCIP Link. FCIP_DEs within an FCIP_LEP share the FCIP Link. Multiple FCIP_DEs between FCIP_LEPs introduce multiple traffic paths (e.g., Class F, Class 2/3). The order in which the FCIP_DEs are serviced on the FCIP Link is not specified. One possibility is providing different priority levels to each traffic path changing the overall delivery order.

The only delivery order guarantee provided by TCP is correctly ordered delivery of FC-BB_IP encapsulated FC frames between a pair of FCIP_DEs. The FC Entity is expected to specify and handle all other FC frame delivery ordering requirements.

NOTE 11 – The order of the FC frames sent by the encapsulated frame transmitter may not be the same as the order sent by the source FC end node. This is due to the fact that some types of FC login allow FC frames to be re-ordered in the FC Fabric before reaching the FC receiver port.

5.8.6 IP multicast and broadcast

An FC-BB_IP device shall not make use of IP multicast and broadcast.

5.8.7 Security and authentication

The IETF security standards referenced by RFC 3821 provide numerous mechanisms for securing TCP connections between FC/FCIP Entity pairs (e.g., IPsec packet authentication and confidentiality). It is important to note that the public Internet IP network is subject to a large variety of security attacks, meaning that serious consideration should be given to enabling the full suite of security features described in FCIP whenever the public Internet IP network is to be used to transmit FCIP frames.

The TCP connection authentication mechanism described in 5.7.2.3.1 provides FC-BB-6 specific authentication for the second, third, etc., TCP connections in an FCIP Link and its associated Virtual ISL as long as the first TCP connection is authenticated using the mechanisms described in 5.7.2.3 and FC-SP-2.

6 Transparent FC-BB (FC-BB_GFPT and FC-BB_PW) Structure and Concepts

6.1 Applicability

Clause 4 specifies the FC-BB_GFPT and FC-BB_PW reference models. This clause specifies the FC-BB_GFPT and FC-BB_PW functional models.

6.2 FC-BB_GFPT overview

This clause discusses further aspects of FC-BB_GFPT operation, including initialization, flow control, and procedures for adaptation of FC information for transport using the Asynchronous Transparent Generic Framing Procedure (GFPT). Mapping FC-BB_GFPT into Asynchronous GFPT allows for applications where the WAN transport rate is less than the attached FC_Port data rate.

Figure 22 illustrates the protocol levels and layers involved in FC-BB_GFPT processes and devices.

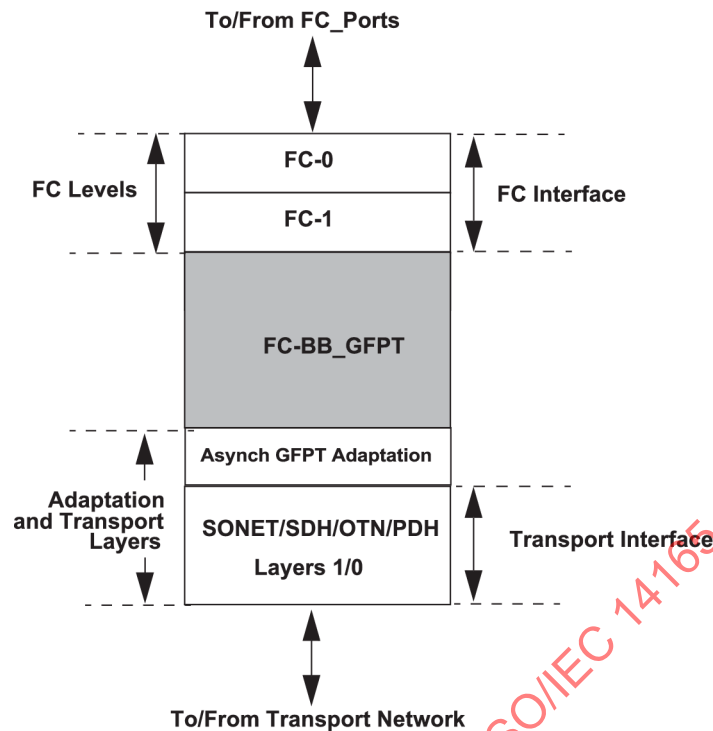


Figure 22 – FC-BB_GFPT protocol levels and layers

The FC-BB_GFPT level, the functions of which are defined by this standard, produces a stream of 8B/10B codewords in either direction. In the FC interface direction, it produces a full FC rate, synchronous codeword stream, constructed from the FC Ordered Sets (i.e., frames, Primitive Signals and Primitive Sequences) delivered by the Asynchronous GFPT adaptation level, and emulates a standard FC-2 level interfacing to a standard FC-1 level. In the WAN network direction, the FC-BB_GFPT level produces a filtered stream of codewords that includes all FC frames, selected Primitive Signals and selected Ordered Sets of Primitive Sequences that are forwarded by the FC-1 level, as well as GFPT_WAN Primitive Signals that are used for WAN flow control and management purposes. The Asynchronous GFPT adaptation level and the transport (i.e., SONET/SDH/OTN/PDH) layers are described by ITU-T standards (see 2.4).

If the FC-BB_GFPT device supports frame compression (see ANSI INCITS 241-1994 (R1999)), then the FC Frames in the WAN network direction may be encoded and compressed to allow greater throughput for cases where the provisioned WAN bandwidth is less than the attached FC_Port data rate.

Buffering of selected codewords and Ordered Sets, in both directions of propagation (i.e., from the FC-1 level, and from the GFPT adaptation level), is one function of the FC-BB_GFPT level components.

6.3 FC-BB_PW overview

This subclause discusses further aspects of FC-BB_PW operation, including initialization and procedures for adaptation of FC information for transport over MPLS networks (see RFC 3031).

The FC-BB_PW functionality is separated into two layers as shown in Figure 23.

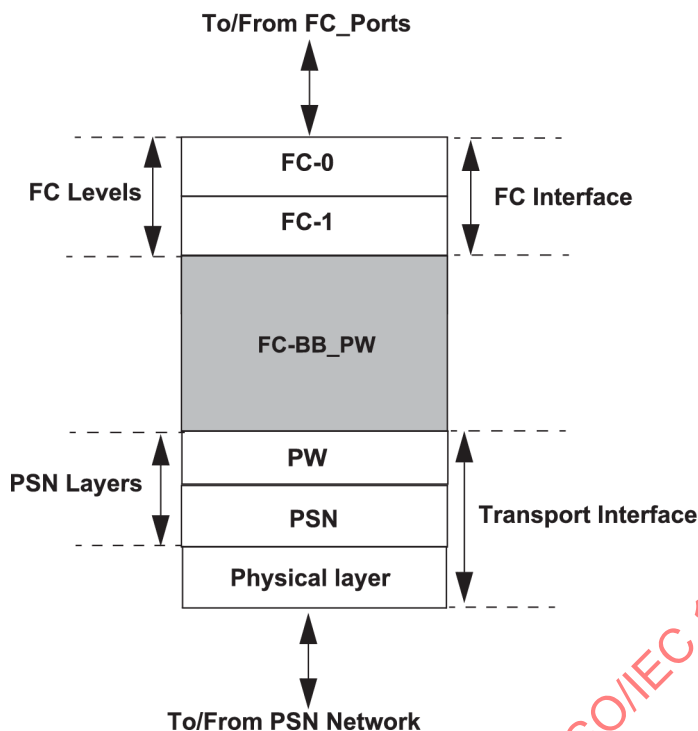


Figure 23 – FC-BB_PW protocol levels and layers

The FC-BB_PW level, specified in this standard, produces a full FC rate, synchronous stream of 8B/10B codewords in the FC interface direction. The stream is constructed from FC frames, Primitive Signals, and Primitive Sequences delivered by the PW Termination layer. This layer emulates a standard FC-2 layer to the underlying standard FC-1 layer. In the WAN direction, the FC-BB_PW layer produces a filtered stream of bytes that include all FC frames, selected Primitive Signals, and selected Primitive Sequences that are delivered by the FC-1 layer. The FC-BB_PW Termination layer is described in RFC 6307, and the MPLS layer is described in RFC 3031.

6.4 Transparent FC-BB functional model

6.4.1 Transparent FC-BB initialization

The FC physical interfaces on Transparent FC-BB devices may support link speed negotiation (see FC-FS-4) with the attached FC_Ports (e.g., to prevent use of physical variants that require 64B/66B encoded information).

Other than link speed negotiation, Transparent FC-BB devices do not directly participate in FC link initialization or FC_Port initialization. The remainder of FC link initialization and FC_Port initialization occurs between the attached FC_Port and the remote FC_Port. This is a key distinction between the Transparent FC-BB device model and other FC-BB-6 models.

Transparent FC-BB devices monitor and transport the FC_Port initialization Exchanges (i.e., ELP, FLOGI, and PLOGI) that take place between the attached FC_Port and the remote FC_Port, but only to capture and potentially modify the parameters that are relevant to link-level flow control.

If an FC-BB_GFPT device hosts more than one GFPT_WAN facility, then a separate state machine operates on each GFPT_WAN facility. The state machines in each FC-BB_GFPT device on the same GFPT_WAN facility operate independently of one another.

If an FC-BB_PW device hosts more than one PW facility, then a separate state machine operates on each PW facility. The state machines in each FC-BB_PW device on the same PW facility operate independently of one another.

6.4.2 Transparent FC-BB initialization state machine

6.4.2.1 Initialization state machine keywords

The keywords used in the Transparent FC-BB initialization state machine diagram (see Figure 24) are specified in Table 14.

Table 14 – Transparent FC-BB initialization state machine keywords

Keyword	Description
WAN	Received from remote Transparent FC-BB device.
FC	Received from attached FC_Port.
WAN-Error	For FC-BB_GFPT, WAN-Error indicates the reception of 10B_ERR (i.e., a GFPT-defined character used to represent an FC illegal codeword or running disparity error at ingress, or an irresolvable character error produced during GFPT_WAN transmission. Interpretation of these characters is described in 6.4.8.1). For FC-BB_PW, WAN-Error indicates the reception of an errored FC frame as specified in FC-FS-4 (e.g., CRC error, invalid EOF), or the reception of an error indication control frame.
FC-Error	8B/10B character error or running disparity error.
PSig	Primitive Signal
PSeq	Primitive Sequence
Arb-Loop	Arbitrated Loop

6.4.2.2 Initialization state machine

The Transparent FC-BB initialization state machine is specified in Figure 24.

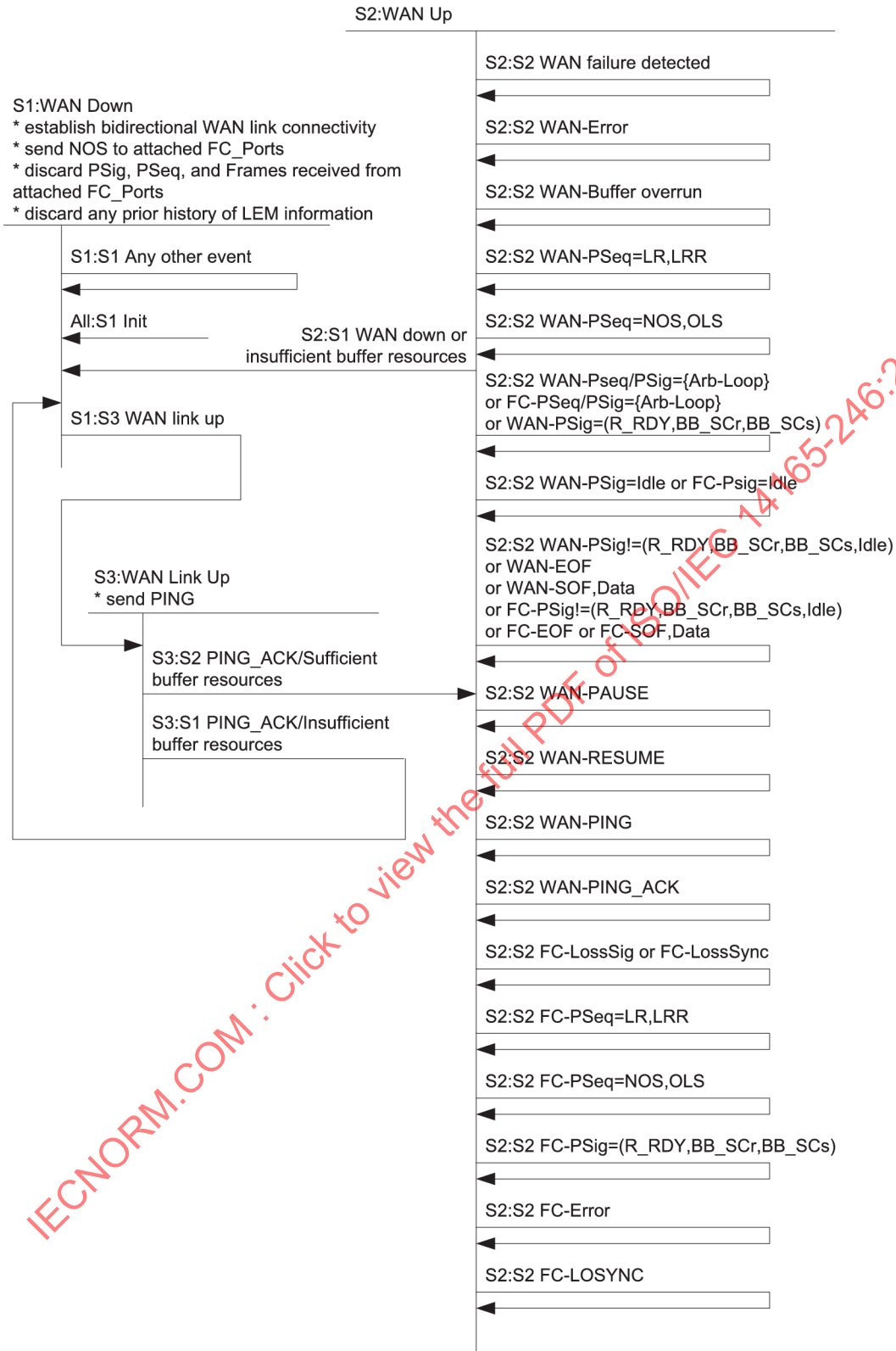


Figure 24 – Transparent FC-BB initialization state machine

Transition All:S1 Init. This transition occurs if an initialization event occurs in a state where it is not already handled. An initialization event may be:

- a) a power-on reset condition;
- b) outside intervention requesting an initialization event; or
- c) a vendor-specific initialization event.

State S1: WAN Down. In this state, Transparent FC-BB devices shall:

- a) initiate establishment of WAN links, either Transport Trail with a fully operational GFP server in both directions (see 6.4.8.1) or PW;
- b) send the Not Operational (NOS) Primitive Sequence to attached FC_Ports, if any;
- c) discard any Primitive Signals, Primitive Sequences, or frames received from attached FC_Ports; and
- d) discard any prior history of Login Exchange Monitor (LEM) information (see 6.4.3).

Transition S1:S1 Any other event. This transition occurs if an event other than what is specified in state S1 occurs.

Transition S1:S3 WAN link up. This transition occurs if a WAN link up event is detected.

State S3: WAN Link Up. In this state, the Transparent FC-BB device shall transmit one or more PING signals to the remote Transparent FC-BB device.

Transition S3:S2 PING_ACK/Sufficient buffer resources. This transition occurs upon reception of at least one PING_ACK signal from the remote Transparent FC-BB device and sufficient buffer resources are available to prevent data loss (see 6.4.7). This transition is not coordinated between the Transparent FC-BB devices at each end of the WAN link.

Transition S3:S1 PING_ACK/Insufficient buffer resources. This transition occurs upon reception of at least one PING_ACK signal from the remote Transparent FC-BB device and WAN latency measurements indicate insufficient buffer resources are available within the device to prevent data loss (see 6.4.7).

State S2: WAN Up. In this state, the WAN link is normally (i.e., excluding WAN protection events) operational and the attached FC_Ports at either end of the link are permitted to communicate with each other.

Validated (see 6.4.8.1) Primitive Sequences received from the attached FC_Ports shall be transmitted between attached FC_Ports by the Transparent FC-BB devices according to the rate adaptation and other rules described in 6.4.8.1.

If a Primitive Sequence is received from an attached FC_Port while a Transparent FC-BB device has ceased transmitting information across the WAN link because it has received one or more PAUSES, the Transparent FC-BB device shall:

- a) flush its WAN-facing buffer;
- b) clear the WAN pause condition; and
- c) transmit the Primitive Sequence to the remote Transparent FC-BB device.

If a Primitive Sequence is received from a remote Transparent FC-BB device while a Transparent FC-BB device is unable to transmit frames to an attached FC_Port because it has no available BB_Credit, the Transparent FC-BB device shall:

- a) flush its attached FC_Port-facing buffer; and
- b) transmit the Primitive Sequence to the attached FC_Port.

If FC-BB_GFPT devices have no data to transmit to the WAN, then the FC-BB_GFPT device shall follow the rules specified in ITU-T Rec. G.7041/Y.1303.

Transition S2:S1 WAN down or insufficient buffer resources. This transition occurs if:

- a) a WAN down event is detected. A WAN down event occurs if GFPT_WAN recovery does not occur within WAN_HOLDOFF_TOV (see 6.4.9) of the time the GFPT_WAN failure is detected; or
- b) WAN latency measurements (i.e., using PING and PING_ACK signals) indicate insufficient buffer resources are available within the device to prevent data loss (see 6.4.7).

This transition is not coordinated between the Transparent FC-BB devices at each end of the WAN link.

Transition S2:S2 WAN failure detected. This transition occurs if WAN failure is detected. WAN failure is detected according to criteria that are WAN specific and outside the scope of this standard (e.g., LOS, LOP, AIS, in the case of SONET/SDH, see ITU-T Rec. G.806 and ITU-T Rec. G.783, or RFI, LDP signaling failure in the case of PW, see RFC 4447) (see 6.4.8.1).

The Transparent FC-BB device shall start the WAN_HOLDOFF_TOV timer (see 6.4.9) upon detection of a WAN failure.

WAN link interruptions or protection events may be concealed from the attached FC equipment in a manner beyond the scope of this standard. During such interruptions, the Transparent FC-BB device may operate as though the WAN link were operational and transmit to attached FC_Ports:

- a) the Primitive Sequence that they were transmitting at the time the WAN failure was detected;
- b) Idles if they were not transmitting a Primitive Sequence at the time the WAN failure was detected; or
- c) for an FC-BB_GFPT device, 8B/10B-encoded interpretations of 10B_ERR characters (see ITU-T Rec. G.7041/Y.1303).

Transition S2:S2 WAN-Error. For an FC-BB_GFPT device, this transition occurs if a 10B_ERR character is received from the WAN (see 6.4.8.1). For an FC-BB_PW device, this transition occurs if an errored frame (see FC-FS-4) or error indication control frame is received from the WAN (see 6.4.8.2).

Transition S2:S2 WAN-Buffer overrun. This transition occurs if a buffer overrun is detected. This transition is not expected to occur, but if it does, the number of dropped FC frames shall be counted.

Transition S2:S2 WAN-PSeq=LR,LRR. This transition occurs if a LR or LRR Primitive Sequence is received from the WAN. BB_Credit_CNT shall be set to zero, and BB_Credit shall be set to the value that was established by the last LEM (see 6.4.3).

Transition S2:S2 WAN-PSeq=NOS,OLS. This transition occurs if a NOS or OLS Primitive Sequence is received from the WAN. Any existing history of LEM information shall be discarded, BB_Credit_CNT shall be set to zero, and BB_Credit shall be set to one.

Transition S2:S2 WAN-PSig={Arb-Loop} or FC-PSig={Arb-Loop} or WAN-PSig=(R_RDY,BB_SCr,BB_SCs). This transition occurs if an Arbitrated Loop Primitive Sequence or Primitive Signal is received from the WAN or the attached FC_Port, or if an R_RDY, BB_SCr, or BB_SCs Primitive Signal is received from the WAN. These Primitive Sequences and Primitive Signals are not expected and shall be discarded.

Transition S2:S2 WAN-PSig=Idle or FC-PSig=Idle. This transition occurs if an Idle Primitive Signal is received from the WAN or from the attached FC_Port.

Idle Primitive Signals shall be handled in the same manner as Primitive Sequences if they are received by a Transparent FC-BB device in a direction of propagation that has most recently seen reception of a Primitive Sequence, as opposed to a frame or FC Primitive Signal. At all other times (i.e., if they appear in a direction of propagation that has most recently seen reception of a frame or FC Primitive Signal, as opposed to a Primitive Sequence), Idle Primitive Signals shall be:

- a) discarded by FC-BB_PW devices; or
- b) discarded or transported end-to-end by FC-BB_GFPT devices without impeding the forwarding of FC frames and other Primitive Signals, which have higher priority.

In all circumstances, Idle Primitive Signals shall be generated as required for forwarding to attached FC_Ports, consistent with the rules defined in FC-FS-4.

Transition S2:S2 WAN-PSig!=(R_RDY,BB_SCr,BB_SCs,Idle) or WAN-EOF or WAN-SOF,Data or FC-PSig!=(R_RDY,BB_SCr,BB_SCs,Idle) or FC-EOF or FC-SOF,Data. This transition occurs if a Primitive Signal other than R_RDY, BB_SCr, BB_SCs, or Idle, an EOF, or SOF with subsequent data is received from the WAN or a Primitive Signal other than R_RDY, BB_SCr, BB_SCs, or Idle, an EOF, or SOF with subsequent data is received from the attached FC_Port.

These Primitive Signals, Ordered Sets, and data shall be transported end-to-end without modification in the order in which they were received, except if flow control conditions require the flushing of buffers. The Ordered Sets shall be transported using the rate adaptation rules described in 6.4.8.1.

If a valid ELP, FLOGI, or PLOGI request frame (see FC-SW-6 and FC-LS-3) is received from the WAN or the attached FC_Port, the Transparent FC-BB device shall follow the Login Exchange Monitor (LEM) rules specified in 6.4.3.

Valid ELP, FLOGI, PLOGI, SW_ACC, and LS_ACC frames, may be modified by Transparent FC-BB devices according to the rules specified in 6.4.4, but only if such frames open or close a LEM as specified in 6.4.3.

Transition S2:S2 WAN-PAUSE. This transition occurs if a PAUSE signal is received from the WAN. Transmission of frames and Primitive Signals over the WAN shall be paused. See 6.4.7.

Transition S2:S2 WAN-RESUME. This transition occurs if a RESUME signal is received from the WAN. Transmission of frames and Primitive Signals over the WAN shall resume. See 6.4.7.

Transition S2:S2 WAN-PING. This transition occurs if a PING signal is received from the WAN. The Transparent FC-BB device shall transmit a PING_ACK to the remote Transparent FC-BB device. See 6.4.7.

Transition S2:S2 WAN-PING_ACK. This transition occurs if a PING_ACK signal is received from the WAN. See 6.4.7.

Transition S2:S2 FC-LossSig or FC-LossSync. This transition occurs if Loss of Signal or Loss of Synchronization (see FC-FS-4) is detected on the link to the attached FC_Port for a period of time greater than R_T_TOV. The Transparent FC-BB device shall transmit the NOS Primitive Sequence for the duration of the Loss of Signal or Loss of Synchronization condition to the remote Transparent FC-BB device to initiate the Link Failure protocol (see FC-FS-4).

Transition S2:S2 FC-PSeq=LR,LRR. This transition occurs if a LR or LRR Primitive Sequence is received from the attached FC_Port. Any existing WAN Pause shall be cleared, BB_Credit_CNT shall be set to zero, and BB_Credit shall be set to the value that was established by the last LEM (see 6.4.3).

Transition S2:S2 FC-PSeq=NOS,OLS. This transition occurs if a NOS or OLS Primitive Sequence is received from the attached FC_Port. Any existing WAN Pause shall be cleared, any existing history of LEM information shall be discarded, BB_Credit_CNT shall be set to zero, and BB_Credit shall be set to one.

Transition S2:S2 FC-PSig=(R_RDY,BB_SCr,BB_SCs). This transition occurs if an R_RDY, BB_SCr, or BB_SCs Primitive Signal is received from the attached FC_Port. These Primitive Signals shall be terminated and shall not be forwarded to the remote Transparent FC-BB device. The R_RDY Primitive Signal is used for BB_Credit management with the attached FC_Port. The BB_SCr and BB_SCs Primitive Signals are used for BB_Credit Recovery with the attached FC_Port. See 6.4.5.

Transition S2:S2 FC-Error. This transition occurs if an 8B/10B character error or running disparity error is detected on the link to the attached FC_Port. See 6.4.8.1 for FC-BB_GFPT and 6.4.8.2 for FC-BB_PW.

Transition S2:S2 FC-LOSYNC. This transition occurs if a Loss of Synchronization is detected on the link to the attached FC_Port for a period of time less than R_T_TOV.

An FC-BB_GFPT device shall transmit the 10B_ERR character to the remote FC-BB_GFPT device for the duration of the Loss of Synchronization condition.

An FC-BB_PW shall indicate Loss of Synchronization to the remote FC-BB_PW device by transmitting to the WAN:

- a) an FC frame with EOFni if it was in the middle of transmitting a frame at the time the FC-LOSYNC was detected; or
- b) an error indication control frame (see 6.4.8.2.2) (i.e., if it was transmitting either Primitive Sequences or Idles at the time the FC-LOSYNC was detected). In this case the FC-BB_PW device shall continue transmitting to the WAN:
 - A) the Primitive Sequence that it was transmitting at the time the FC-LOSYNC was detected; or
 - B) Idles if it was not transmitting a Primitive Sequence at the time the FC-LOSYNC was detected.

6.4.3 Login Exchange Monitors

Login Exchange Monitors (LEMs) identify frames belonging to the ELP, FLOGI, and PLOGI Exchanges that take place between the attached FC_Ports that are directly interconnected by two Transparent FC-BB devices across a WAN link. The function of a LEM is to determine, and possibly modify, the BB_Credit and BB_SC_N values that are negotiated between the attached FC_Ports at each end of the link. LEMs operate according to a state machine that is described in Table 15.

Six different LEMs are defined and designated using the notation LEM(rank, direction), where rank is either ELP, FLOGI, or PLOGI and direction is either outbound (see 3.3.11) or inbound (see 3.3.8).

The outbound direction is used if an ELP, FLOGI, or PLOGI request frame has been received from the attached FC_Port. The inbound direction is used if an ELP, FLOGI, or PLOGI request frame has been received from the remote Transparent FC-BB device.

Candidate LEM frames (see 6.4.8.1) shall be valid frames (see FC-FS-4).

The three LEM ranks are hierarchical as follows:

- 1) ELP (i.e., highest rank);
- 2) FLOGI; and
- 3) PLOGI (i.e., lowest rank).

All six LEMs have two states, open, and closed. LEMs may transition between opened and closed during WAN initialization in state S2 (see 6.4.2). LEMs have no significance in state S1 (see 6.4.2).

If a valid ELP, FLOGI, or PLOGI request frame (see FC-SW-6 and FC-LS-3) is observed in either direction, then a corresponding, direction-specific LEM is opened:

- a) if no history has been saved for a successfully closed LEM in the specified direction; or
- b) if the rank of the most recent successfully closed LEM in the specified direction is less than the rank of the valid ELP, FLOGI, or PLOGI request frame being processed.

LEM are opened separately and independently according to both rank and direction of propagation.

One or more LEMs per direction of propagation (i.e., outbound or inbound) may be open at one time. If a LEM is already in the open state, then the observation of an ELP, FLOGI, or PLOGI request frame in the same direction (i.e., that may open a LEM corresponding to the same direction) either:

- a) forces closure of the open LEM, and opening of the LEM corresponding to the observed ELP, FLOGI, or PLOGI request frame, if the new LEM has the same or higher rank than the already open LEM; or
- b) if the new LEM has a lower rank than the already open LEM, the ELP, FLOGI, or PLOGI request frame and subsequent Exchange are saved (i.e., multiple LEM ranks may be open).

An open LEM is successfully closed by:

- a) observation of a valid ELP, FLOGI, or PLOGI request frame in the direction associated with the LEM and of the same or higher rank (see 6.4.3). In this case, a new LEM is also opened; or
- b) observation of a corresponding valid SW_ACC or LS_ACC frame in the direction opposite to the one associated with the LEM with the N_Port/F_Port bit set appropriately (i.e., zero for LS_ACC to PLOGI and one for LS_ACC to FLOGI).

Transparent FC-BB devices shall store the rank (i.e., ELP, FLOGI, or PLOGI) of the successful LEM closure. The successful closure of a LEM, in either direction, forces unsuccessful closure of all other LEMs in both directions.

If a LEM closes successfully, then BB_Credit is initialized as per the newly negotiated parameters (see 6.4.5). LEM closure and BB_Credit initialization are not directly coordinated between the Transparent FC-BB devices at each end of the WAN link.

An open LEM is not successfully closed by:

- a) observation of a corresponding valid SW_RJT, LS_RJT, F_RJT, P_RJT, F_BSY, P_BSY, or ABTS frame in the direction opposite to the one associated with the LEM; or
- b) observation of a corresponding valid SW_ACC or LS_ACC frame in the direction opposite to the one associated with the LEM with the N_Port/F_Port bit set inappropriately (i.e., one for LS_ACC to PLOGI and zero for LS_ACC to FLOGI).

Table 15 provides a current state/next state description of the LEM state machine. For convenience of presentation, the six LEMs are collapsed into a single generic one. Every observation of candidate LEM frames shall be evaluated with respect to all six combinations of rank and direction. In most cases, the applicable row of Table 15 is different among the six LEMs.

If successful LEM closure occurs in response to input event (5), then input event (6) shall be applied to the other five LEMs.

Table 15 – Login Exchange Monitor (LEM) state machine

	Current State			
	LEM(Rank=A, Dir=X) Closed		LEM(Rank=A, Dir=X) Open	
Input Event:	Next State(s)	Comments	Next State(s)	Comments
1) Observation of valid ELP/FLOGI/PLOGI request frame, where rank of frame > A, in direction X	LEM(A,X) Closed	No action	LEM(A,X) Closed	Unsuccessful LEM closure
2) Observation of valid ELP/FLOGI/PLOGI request frame, where rank of frame = A, in direction X	LEM(A,X) Open	New LEM opening	LEM(A,X) Open	LEM “re-opening” - i.e., new information
3a) Observation of a valid FLOGI/PLOGI request frame, where rank of LEM = ELP, in direction X	LEM(A,X) Closed	No action	LEM(A,X) Open	No action
3b) Observation of a valid PLOGI request frame, where rank of LEM = FLOGI, in direction X	LEM(A,X) Closed	No action	LEM(A,X) Open	Open PLOGI LEM
4) Observation of valid ELP/FLOGI/PLOGI request frame, of any rank, in direction opposite to X	LEM(A,X) Closed	No action	LEM(A,X) Open	No action
5) Observation of valid SW_ACC/LS_ACC frame, corresponding to LEM(A,X)	LEM(A,X) Closed	Not expected - disregard	LEM(A,X) Closed	Successful LEM closure: rank = A; apply input event (6) against other LEMs
6) Successful closure of a LEM other than LEM(A,X)	LEM(A,X) Closed	No action	LEM(A,X) Closed	Unsuccessful LEM closure
7) Observation of valid SW_RJT/LS_RJT/F_BSY/P_BSY/F_RJT/P_RJT frame corresponding to LEM(A,X) or ABTS frame in the direction opposite to the one associated with LEM(A,X)	LEM(A,X) Closed	Not expected - disregard	LEM(A,X) Closed	Unsuccessful LEM closure
8) Primitive Sequence (LR,LRR) received in either direction	LEM(A,X) Closed	No action	LEM(A,X) Closed	Unsuccessful LEM closure
9) Primitive Sequence (OLS,NOS) received in either direction	LEM(PLOGI (credit=1), both directions) closed	No action	LEM(PLOGI (credit=1), both directions) closed	Unsuccessful LEM closure

Actions associated with LEM openings and closings are of two types, those associated with BB_Credit initialization, and those associated with the capture or modification of parameters that are relevant to link-level flow control in frames processed by LEMs. These actions are discussed in detail, in 6.4.2 and 6.4.4 respectively, and are not considered in Table 15.

6.4.4 Port initialization parameter observation and modification

Transparent FC-BB devices shall note the ISL Flow Control Mode value on inbound (see 3.3.8) and outbound (see 3.3.11) ELP request/reply SW_ACC frames of open LEMs. If the LEM is successfully closed and flow control other than either R_RDY flow control or VC_RDY flow control has been established between the interconnected FC_Ports, then the Transparent FC-BB devices behavior is outside the scope of this standard.

If the use of R_RDY or VC_RDY flow control has been negotiated between the interconnected FC_Ports, then the FC-BB_GFPT devices shall operate using unchannelized ASFC (see 6.4.7) and FC-BB_PW devices shall operate using FC-BB_PW ASFC (see 6.4.7). Transparent FC-BB devices shall note the BB_Credit value on outbound ELP/FLOGI/PLOGI request/reply SW_ACC/LS_ACC frames of open LEMs, for subsequent use by BB_Credit flow control management. Similarly, Transparent FC-BB devices shall note the BB_Credit value on inbound ELP/FLOGI/PLOGI request/reply SW_ACC/LS_ACC frames of valid or open LEMs. If the BB_Credit value exceeds that corresponding to the size of the Transparent FC-BB device's outbound buffer for the attached FC_Port, then the BB_Credit value shall be overwritten with the value that corresponds to the outbound buffer size. Transparent FC-BB devices are responsible for recalculating the FC frame CRC in such cases. The number of BB_Credits to be used by flow control management at any time, and the governance of (re-)initialization of BB_Credit values, are specified in 6.4.2 and 6.4.5.

If VC_RDY flow control has been negotiated between the interconnected FC_Ports, then the use of channelized ASFC (see 6.4.7) is not defined in this standard.

Transparent FC-BB devices shall note the BB_Credit Management bit value on outbound FLOGI/PLOGI request/reply LS_ACC frames of open LEMs, for appropriate use by BB_Credit flow control management. If the use of alternate BB_Credit management has been indicated by an attached FC_Port and the Transparent FC-BB device does not support such management, then the Transparent FC-BB device behavior is outside the scope of this standard.

Transparent FC-BB devices shall support BB_Credit Recovery (see 6.4.5). Transparent FC-BB devices shall note the BB_SC_N value in both inbound and outbound ELP/FLOGI/PLOGI reply SW_ACC/LS_ACC frames of open LEMs. The larger such value shall be used for BB_Credit Recovery purposes, unless either such value is zero, in which case BB_Credit Recovery shall not operate (see FC-FS-4).

The use of the RPSC (Report Port Speed Capabilities) ELS, by either attached FC_Port on a WAN facility, may in principle result in a mismatch of negotiated and reported port speed capabilities. The interception and local handling of RPSC ELSs by Transparent FC-BB devices, to prevent such mismatch, is not possible in general, since RPSC ELSs may be secured, depending on the security relationships that may be established between the communicating FC_Ports. However, any such reported mismatch should be strictly a second-order management issue and should not in any way impact the functioning of the FC_Ports and devices, the Transparent FC-BB devices, or the end-to-end data link.

6.4.5 Handling of BB_SCs, BB_SCr, and R_RDY Primitive Signals and BB_Credit initialization

Reception of BB_SCs and BB_SCr Primitive Signals from attached FC_Ports shall be noted for use in BB_Credit Recovery at the FC physical interface on the Transparent FC-BB device. Transparent FC-BB devices shall generate BB_SCs and BB_SCr Primitive Signals, for transmission to attached FC_Ports, as required to properly perform BB_Credit Recovery with those ports. Adjustment of BB_Credit as specified in FC-FS-4 may be required on reception of a BB_SCs or BB_SCr Primitive Signal.

Reception of R_RDY Primitive Signals from attached FC_Ports shall be noted for credit/buffer management purposes. R_RDY Primitive Signals shall be generated by Transparent FC-BB devices, for transmission to attached FC_Ports, under the direction of credit/buffer managers.

The BB_Credit values assumed by credit/buffer managers, significant only in state S2, are generally those established by the most recently successfully closed LEM, and credit values/counts are initialized to these values immediately following LEM closure or upon transit of LR or LRR Primitive Sequences in either direction. In state S2, credit/buffer managers shall use a BB_Credit value of 1 in the absence of a history of successful LEM closure, and they shall also initialize credit values/counts to 1 upon transit in either direction of a Primitive Sequence other than LR or LRR.

The reception of R_RDY Primitive Signals, by Transparent FC-BB devices, from attached FC_Ports shall increment the credit available for transmission of frames to those ports, subject to the limitation that such credit counts/values shall not be incremented beyond the values of BB_Credit established by the most recently successfully closed LEM. The transmission of frames to attached FC_Ports shall decrement the credit available for transmission of frames to those ports, subject to the limitation that such credit counts/values shall not be reduced to a value less than zero. Frames shall be transmitted to attached FC_Ports only if current credit values are positive.

The generation of R_RDY Primitive Signals, by Transparent FC-BB devices, shall be managed such that buffer overflow and frame loss within the Transparent FC-BB devices, is precluded.

An example of a “start-up” sequence, showing the normal progression of state machine transitions, as well as R_RDY and BB_Credit handling, is shown in Figure 25.

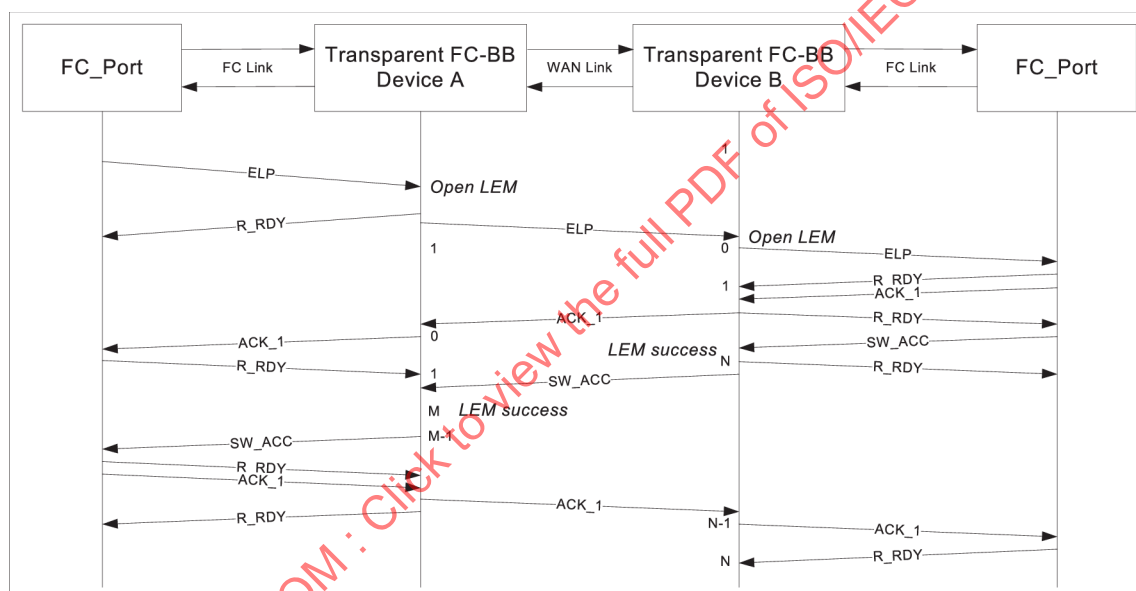


Figure 25 – Example port initialization process

Figure 25 illustrates an example port initialization process for ISL extension using Transparent FC-BB devices (i.e., attached port initialization is ELP). Simultaneous initiation of ELP by both ports represents a straightforward extension of the illustrated process. Only one of the ELP Exchange leads to successful LEM closure in both Transparent FC-BB devices. Both Transparent FC-BB devices are shown as beginning in state S2 and link initialization is assumed to have been completed between the attached E_Ports. The LEM opening and closing (i.e., success) events are shown for both devices. The evolution of credit counts used by Transparent FC-BB devices, to pace the delivery of frames to attached devices are also shown. LEM closure initializes the active values of those credit counts to M and N for Transparent FC-BB Device A and Transparent FC-BB Device B, respectively. M and N correspond to the BB_Credit values reported by the E_Ports in the ELP and SW_ACC frames, respectively. Corruption/loss of frames and/or R_RDY Primitive Signals, at any point in the end-to-end system, produces situations not qualitatively different, or different in terms of method of recovery, from corresponding events on standard ISLs.

If BB_Credit_CNT remains at zero for an interval of E_D_TOV or greater while the LEM is successfully closed, then the Transparent FC-BB device shall transmit NOS to the local attached FC_Port and the remote attached FC_Port for 100ms +/- 25ms.

6.4.6 Transparent FC-BB Primitive Signals

Transparent FC-BB WAN signals are generated and terminated by Transparent FC-BB devices. Transparent FC-BB WAN signals are not sent to FC_Ports, nor do Transparent FC-BB devices expect to receive them from FC_Ports.

FC-BB_GFPT WAN signals are Ordered Sets defined as GFPT_WAN Primitive Signals. The most significant byte of GFPT_WAN Primitive Signals is K28.1. This character is chosen to prevent any possibility of conflict with Primitive Signals or other Ordered Sets that may be defined in any FC standard, for generation by any FC_Port.

FC-BB_PW WAN signals are control frames transmitted between FC-BB_PW devices.

6.4.7 Transparent FC-BB flow control

6.4.7.1 Overview

Alternate Simple Flow Control (ASFC) shall be supported between FC-BB_GFPT devices (see 6.4.7.2) and between FC-BB_PW devices (see 6.4.7.3).

6.4.7.2 FC-BB_GFPT Alternate Simple Flow Control (ASFC)

ASFC defines two Primitive Signals, ASFC_PAUSE and ASFC_RESUME (see Table 16), that may be sent by one FC-BB_GFPT device to a remote FC-BB_GFPT device.

Table 16 specifies the Ordered Set values for the ASFC_PAUSE and ASFC_RESUME FC-BB_GFPT WAN Primitive Signals.

Table 16 – Values of FC-BB_GFPT ASFC_PAUSE and ASFC_RESUME Primitive Signals

FC-BB_GFPT Primitive Signal	Ordered Set (Value)
ASFC_PAUSE	K28.1 D00.0 D00.0 D00.0 - unchannelized
ASFC_RESUME	K28.1 D04.0 D00.0 D00.0 - unchannelized

An FC-BB_GFPT device sends ASFC_PAUSE to a remote FC-BB_GFPT device to direct that device to cease forwarding FC frames and non-GFPT_WAN Primitive Signals. Reception of a single ASFC_PAUSE Primitive Signal suffices to trigger such cessation, which shall take effect:

- within 25 microseconds of reception of the ASFC_PAUSE Primitive Signal; or
- immediately upon completion of the forwarding of a frame or Primitive Signal if forwarding of a frame or non-GFPT_WAN Primitive Signal is in progress while the ASFC_PAUSE Primitive Signal is received and the time required for such completion is greater than 25 microseconds.

If FC frame and non-GFPT_WAN Primitive Signal forwarding has been suspended due to reception of an ASFC_PAUSE Primitive Signal, then it shall remain suspended until at least one ASFC_RESUME Primitive Signal has been received. FC-BB_GFPT devices shall:

- not insert ASFC_PAUSE or ASFC_RESUME Primitive Signals within any FC Order Set;
- insert ASFC_PAUSE or ASFC_RESUME Primitive Signals on transmission word boundaries; and
- not insert more than two ASFC_PAUSE and ASFC_RESUME Primitive Signals within the boundaries of any frame.

Otherwise, ASFC_PAUSE and ASFC_RESUME Primitive Signals are sent at times, in numbers, and at frequencies at the discretion of the sending FC-BB_GFPT device.

6.4.7.3 FC-BB_PW Alternate Simple Flow Control (ASFC)

ASFC defines two control frames, ASFC_PAUSE and ASFC_RESUME (see Figure 26 and Table 17), that may be sent by one FC-BB_PW device to a remote FC-BB_PW device.

Figure 26 specifies the control frame formats for ASFC_PAUSE and ASFC_RESUME.

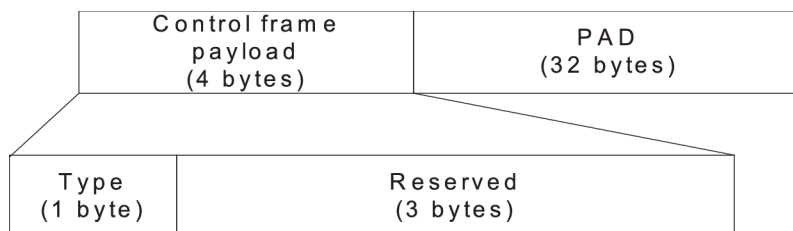


Figure 26 – FC-BB_PW error indication control frame format

Table 17 specifies the control frame payloads for ASFC_PAUSE and ASFC_RESUME.

**Table 17 – FC-BB_PW
ASFC_PAUSE and ASFC_RESUME
control frame payload values**

Field	Value	Description
Type	41h 42h	ASFC_PAUSE ASFC_RESUME
PAD	Zeroes	Set to all zeroes.

This standard specifies the use of Type field value 11h for transmission of error indications (see 6.4.8.2.2) and specifies the use of Type field values 81h and 82h for transmission of FC-BB_PW PING and PING_ACK (see 6.4.7.4.3).

An FC-BB_PW device sends ASFC_PAUSE to a remote FC-BB_PW device to direct that device to cease forwarding FC frames and FC Primitive Signals. Reception of a single ASFC_PAUSE control frame suffices to trigger such cessation, which shall take effect:

- within 1 millisecond of reception of the ASFC_PAUSE control frame; or
- immediately upon completion of the forwarding of an FC frame or FC Primitive Signal if forwarding of a frame or Primitive Signal is in progress while the ASFC_PAUSE control frame is received and the time required for such completion is greater than 1 millisecond.

If FC frame and FC Primitive Signal forwarding has been suspended due to reception of an ASFC_PAUSE control frame, then it shall remain suspended until at least one ASFC_RESUME control frame has been received. FC-BB_PW devices may send ASFC_PAUSE and ASFC_RESUME control frames at times, in numbers, and at frequencies at the discretion of the sending FC-BB_PW device.

6.4.7.4 PING and PING_ACK signals

6.4.7.4.1 Overview

To assist in buffer management, as well as for other purposes, it may be useful to measure the latency on the WAN link. To facilitate this, two additional Transparent FC-BB signals are defined, PING and PING_ACK. For FC-BB_GFPT devices these signals are defined as Primitive Signals (see 6.4.7.4.2). For FC-BB_PW devices these signals are defined as control frames (see 6.4.7.4.3). As well as facilitating latency measurements, PING and PING_ACK signals are also used to communicate selected operational information between Transparent FC-BB devices.

The PING signal may be sent at any time by either Transparent FC-BB device, but not more frequently than once every 100 milliseconds. A single PING_ACK signal shall be transmitted by an Transparent FC-BB device in response to each received PING signal, within 25 microseconds of reception of the latter. FC-BB_GFPT devices shall respect FC Ordered Set delineation while inserting PING and PING_ACK Primitive Signals.

Transparent FC-BB devices shall begin transmission of PING signals as soon as possible upon establishment of WAN connectivity in state S1, and shall not exit state S1 until at least one PING_ACK signal has been received from remote Transparent FC-BB devices. This ensures that communication of support for channelized ASFC capabilities takes place between FC-BB_GFPT devices before they exit state S1. Transmission of PING signals is otherwise undertaken at the discretion of individual Transparent FC-BB devices, within the frequency limits specified above.

6.4.7.4.2 FC-BB_GFPT PING and PING_ACK

Table 18 specifies the Ordered Set values for the PING and PING_ACK FC-BB_GFPT WAN Primitive Signals.

Table 18 – FC-BB_GFPT PING and PING_ACK Primitive Signal values

Primitive Signal	Ordered Set (Value)
PING	K28.1 D16.0 D(XNCCRRRb) D(YZZZZZZb)
PING_ACK	K28.1 D00.4 D(XNCCRRRb) D(YZZZZZZb)

The nomenclature D(MMMMMMMb), MSB left to LSB right, is interpreted as the 8B/10B D-character equivalent of the binary value MMMMMMMb.

The X bit indicates support (X=1b) or lack of support (X=0b) of channelized ASFC capability in the FC-BB_GFPT device transmitting the Primitive Signal. Since channelized ASFC is not defined by this standard, the value of the X bit shall be ignored by FC-BB_GFPT devices receiving PING and PING_ACK Primitive Signals.

The N bit may be set to 0b or 1b by devices transmitting PING, and the PING_ACK generated in response shall use the same value of N received in the associated PING.

Table 19 specifies the CCC bit field values.

Table 19 – FC-BB_GFPT PING and PING_ACK CCC bit field values

Value	Description	Reference
000b	No compression	
001b	LZS compression	6.4.10.3
010b-111b	Reserved	

The values of the R bits are reserved and shall be set to 0b.

The Y bit specifies vendor-specific use (i.e., Y=1b), or standard use (i.e., Y=0b), of the remaining bits (i.e., bits denoted Z) of the Primitive Signal. If Y=0b, the values of the bits denoted Z are reserved and shall be set to 0b. If Y=1b, the values assigned of the bits denoted Z are vendor specific.

6.4.7.4.3 FC-BB_PW PING and PING_ACK

The FC-BB_PW PING and PING_ACK control frame format is specified in Figure 27.

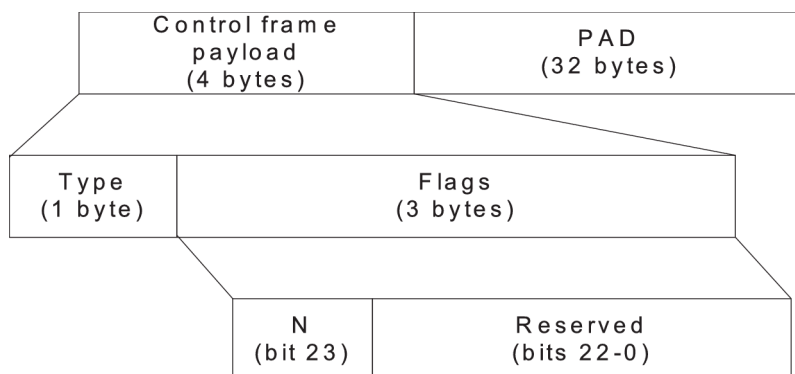


Figure 27 – FC-BB_PW PING and PING_ACK control frame format

The FC-BB_PW PING and PING_ACK control frame payload shall be set as specified in Table 20.

Table 20 – FC-BB_PW PING and PING_ACK control frame payload values

Field	Value	Description
Type	81h 82h	PING PING_ACK
N	0b or 1b	The N bit is set to alternating 0b and 1b. For PING_ACK the N bit is set to the same value as the associated PING.
PAD	Zeroes	Set to all zeroes.

This standard specifies the use of Type field value 11h for transmission of error indications (see 6.4.8.2.2) and specifies the use of Type field values 41h and 42h for ASFC_PAUSE and ASFC_RESUME (see 6.4.7.3).

The FC-BB_PW PING and PING_ACK control frames are encapsulated and formatted for transmission as specified in RFC 6307.

6.4.8 Adaptation of FC information for Transparent FC-BB

6.4.8.1 Adaptation of FC information for GFPT transport in FC-BB_GFPT

Adaptation and de-adaptation of attached FC_Port data in FC-BB_GFPT devices shall use the Asynchronous Transparent Generic Framing Procedure (GFPT) methodology (see ITU-T Rec. G.7041/Y.1303). Arbitrary concatenations of 8B/10B characters may be transported using this methodology, so that frame or message-oriented encapsulation of attached FC_Port data is not required. As such:

- Ordered Sets (i.e., selected Primitive Signals and sub-rated Primitive Sequences) are directly adapted for transport using Asynchronous GFPT;
- with no compression (see Table 19), FC frames are directly adapted for transport using Asynchronous GFPT;
- with compression (see Table 19), at the ingress FC-BB_GFPT device, FC frames are encoded into GFPT_WAN compressed FC frames before entering the GFPT layer and at the egress FC-BB_GFPT device GFPT_WAN compressed frames are decoded back into FC frames; and
- a GFPT_WAN compressed FC frame is composed of a special K28.7 SOF delimiter, compressed frame payload, and an EOF delimiter (6.4.10.1).

Asynchronous GFPT allows for adaptation to transport facilities of arbitrary bandwidths. Characters selected for transport are delivered in order from the de-adaptation process, irrespective of the GFPT_WAN link bandwidth. In this sense, the adaptation of FC frames and Primitive Signals is transparent.

Primitive Sequence adaptation is not purely transparent since the GFPT_WAN link may be sub-rate. Valid Primitive Sequences received from an attached FC_Port shall be adapted for transport by presenting at least three instances of the corresponding FC Ordered Set to the Asynchronous GFPT adaptation engine. The maximum rate of presentation of the corresponding FC Ordered Set to the Asynchronous GFPT adaptation engine shall not exceed the rate at which the transport facility is able to transport them. Primitive Sequences received from the GFPT_WAN link shall be recognized as valid if three Ordered Sets, corresponding to the Primitive Sequence, have been received in succession, excluding both 65B_Idle characters (see ITU-T Rec. G.7041/Y.1303) and GFPT_WAN Primitive Signals. If a valid Primitive Sequence is received from the GFPT_WAN link, then the corresponding full-rate Primitive Sequence shall be constructed for forwarding to the FC_Port. Such forwarding shall continue until superceded by reception, from the remote FC-BB_GFPT device, of a different, valid Primitive Sequence, or a non-GFPT_WAN Primitive Signal or FC frame.

The handling of Idle Primitive Signals is described in 6.4.2.

In general, FC frames shall be forwarded without checking or modification of CRC values, and FC-BB_GFPT devices shall not discard or terminate any FC frames, except as dictated by 6.4.2 (i.e., while in state S1). Buffering that is otherwise necessary for CRC checking and potential frame discard based on the results of such checking, is therefore not required and should be avoided. FC frames should be transferred directly through the FC-BB_GFPT device without intermediate buffering to minimize overall latency, except as required for compression (see Table 19) which may require temporary intermediate storage for the frame being compressed or decompressed. FC frames with invalid CRC values shall not be candidates for LEMs (see 6.4.3). The transit of candidate LEM frames through an FC-BB_GFPT device may be delayed by a maximum of 100 milliseconds while CRC checking, and operations specified in 6.4.2, 6.4.3, and 6.4.4 are performed. Such delays shall not affect the ordering of FC frames, Primitive Signals and Primitive Sequences that transit the FC-BB_GFPT device. All applicable rules (see FC-FS-4) regarding running disparity shall be followed by FC-BB_GFPT devices, with respect to the forwarding of FC frames, Primitive Signals, and Primitive Sequences received from a remote FC-BB_GFPT device to attached FC_Ports.

A Transport Trail that is in use as a GFPT_WAN link between FC-BB_GFPT devices is identified by the presence of the appropriately set User Payload Identifier (UPI) value (i.e., 0Ch) in the Payload Header of the GFP frames borne on the facility (see ITU-T Rec. G.7041/Y.1303).

Ingress FC character errors, ingress FC running disparity errors, or character errors resulting from bit errors occurring during transport on the GFPT_WAN link facility, are represented by GFPT de-adaptation engines using the 10B_ERR character (see ITU-T Rec. G.7041/Y.1303). FC-BB_GFPT devices shall interpret the 10B_ERR character, if GFPT de-adaptation engines present it, as (i.e., they shall generate) an unrecognized 8B/10B neutral disparity codeword, depending on beginning running disparity (RD+ or RD-), as follows:

- a) either 001111 0001 (RD-) or 110000 1110 (RD+); or
- b) some other invalid transmission characters, provided the invalid transmission characters also meet all 8B/10B coding rules, are of neutral disparity, and contain a minimum of one transition within both the first four bits and the last four bits of the codeword.

Synchronous and Asynchronous GFPT has significant, inherent transport error detection and correction capabilities. All detected but uncorrected codeword errors lead to the generation of 10B_ERR characters (i.e., to the generation of 8B/10B-encoded characters at egress). This functionality is sufficient to prevent the possibility of ambiguity in Primitive Signal or Ordered Set interpretation due to bit errors produced on the transport facility. But it may also result in the effective reception of incomplete Ordered Sets (i.e., Ordered Sets consisting partially of 10B_ERR characters) by an FC-BB_GFPT device from the GFPT_WAN. Corrupted FC frames, possibly including corrupted SOF and/or EOF Ordered Sets, thus may be produced. Such cases are indistinguishable, at the remote FC-BB_GFPT device, from FC frames that were received corrupted from FC_Ports and propagated to the remote FC-BB_GFPT device. In either case, no frame delimiter clean-up actions are mandated by this standard, nor are any vendor-specific implementations of such actions precluded. Otherwise, corrupted Ordered Sets received from

remote FC-BB_GFPT devices shall be propagated through to attached FC_Ports, including during transmission of Primitive Sequences. Corrupted Ordered Sets shall not be interpreted as valid GFPT_WAN Primitive Signals.

GFPT error correction, if enabled, provides for minimization of such Ordered Set corruption during GFPT_WAN transport, and for improved channel performance in the face of an imperfect GFPT_WAN facility.

Communication of Loss of Client Signal and Loss of Client Synchronization using GFP Client Management Frames is not required by FC-BB_GFPT. If generated, these Client Management Frames shall not be used for FC-BB_GFPT state machine management or other purposes as specified in 6.4.2. FC-BB_GFPT devices are not required to generate or interpret these Client Management Frames, which may be used for administration and management purposes (e.g., to distinguish between Loss of Client Signal and Loss of Client Synchronization events).

6.4.8.2 Adaptation of FC information for PW transport in FC-BB_PW

6.4.8.2.1 Adaptation of FC information for PW transport in FC-BB_PW overview

Adaptation of FC data in FC-BB_PW equipment shall be performed by transparently encapsulating FC frames within PW packets. FC Ordered Sets (i.e., selected Primitive Signals and sub-rated Primitive Sequences) are encapsulated within FC-BB_PW control frames. The encapsulation method is outside the scope of this standard and is specified in RFC 6307.

FC Frames and Ordered Sets selected for transport are delivered in order, irrespective of the PW bandwidth. In this sense, the adaptation of FC frames and Primitive Signals is transparent.

Primitive Sequence adaptation is not purely transparent since the PW may be sub-rate. Valid Primitive Sequences received from an attached FC_Port shall be adapted for transport by generating a low rate stream of control frames corresponding to the FC Ordered Set at the PW termination engine. If a control frame corresponding to a FC Primitive Sequence is received from the PW, then the corresponding full-rate Primitive Sequence shall be constructed for forwarding to the FC_Port. Such forwarding shall continue until superseded by reception, from the remote FC-BB_PW device, of a different control frame, an FC Primitive Signal or an FC frame.

The handling of Idle Primitive Signals is described in 6.4.2.

In general, FC frames shall be forwarded without checking or modification of CRC values, and FC-BB_PW devices shall not discard any FC frames, except as dictated by 6.4.2 (i.e., while in state S1). Buffering that is otherwise necessary for CRC checking and potential frame discard based on the results of such checking, is therefore not required and should be avoided. FC frames should be transferred directly through the FC-BB_PW device without intermediate buffering to minimize overall latency. FC frames with invalid CRC values shall not be candidates for LEMs (see 6.4.3). The transit of candidate LEM frames through an FC-BB_PW device may be delayed by a maximum of 100 milliseconds while CRC checking, and operations specified in 6.4.2, 6.4.3, and 6.4.4 are performed. Such delays shall not affect the ordering of FC frames, Primitive Signals and Primitive Sequences that transit the FC-BB_PW device. All applicable rules (see FC-FS-4) regarding running disparity shall be followed by FC-BB_PW devices, with respect to the forwarding of FC frames, Primitive Signals, and Primitive Sequences received from a remote FC-BB_PW device to attached FC_Ports.

If the FC-BB_PW device detects ingress FC character errors, ingress FC running disparity errors, or character errors resulting from bit errors, then it shall transmit to the WAN:

- a) an FC frame with EOFni if it was in the middle of transmitting a frame at the time the error was detected (i.e., an error has occurred in at least one of the characters of the FC frame); or
- b) an error indication control frame (see 6.4.8.2.2) (i.e., if it was transmitting either Primitive Sequences or Idles at the time the character or running disparity error was detected). In this case the FC-BB_PW shall continue transmitting to the WAN:
 - A) the Primitive Sequence that was transmitting at the time the error was detected; or
 - B) Idles if it was not transmitting a Primitive Sequence at the time the error was detected.

If an FC-BB_PW device receives an error indication control frame from the WAN, then it shall generate a single unrecognized 8B/10B neutral disparity codeword, depending on beginning running disparity (RD+ or RD-), as follows:

- a) either 001111 0001 (RD-) or 110000 1110 (RD+); or
- b) some other invalid transmission characters, provided the invalid transmission characters also meet all 8B/10B coding rules, are of neutral disparity, and contain a minimum of one transition within both the first four bits and the last four bits of the codeword.

After transmitting this codeword the FC-BB_PW device shall resume normal operation (i.e., transmitting FC frames, Primitive Sequences or Idle Primitive Signals as necessary).

Layer 2 technologies used to transport PW/MPLS frames have significant, inherent transport error detection capabilities. If a codeword error is detected in an FC frame, then the associated EOF Ordered Set shall be replaced with an EOFni Ordered Set. If a codeword error is detected in an FC-BB_PW control frame, then the control frame shall be discarded. This functionality is sufficient to prevent the possibility of errors in Primitive Signal or Ordered Set generation at egress due to bit errors produced on the transport facility. Corrupted FC frames, including an EOFni, may be transmitted. Such cases are indistinguishable, at the remote FC-BB_PW device, from FC frames that were received corrupted from FC_Ports and propagated to the remote FC-BB_PW device with an EOFni. In either case, no frame delimiter error correction actions are mandated by this standard, nor are any vendor-specific implementations of such actions precluded.

6.4.8.2.2 FC-BB_PW error indication control frame

The FC-BB_PW error indication control frame format is specified in Figure 28.

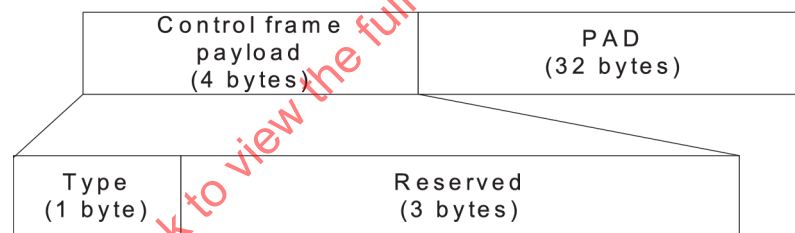


Figure 28 – FC-BB_PW error indication control frame format

The FC-BB_PW error indication control frame payload shall be set as specified in Table 21.

Table 21 – FC-BB_PW error indication control frame payload values

Field	Value	Description
Type	11h	FC-ERROR - indicates a received character error or Loss of Synchronization.
PAD	Zeroes	Set to all zeroes.

This standard specifies the use of Type field values 81h and 82h for transmission of FC-BB_PW PING and PING_ACK (see 6.4.7.4.3) and specifies the use of Type field values 41h and 42h for ASFC_PAUSE and ASFC_RESUME (see 6.4.7.3).

The FC-BB_PW error indication control frame is encapsulated and formatted for transmission as specified in RFC 6307.

6.4.9 WAN Holdoff Timeout Value (WAN_HOLDOFF_TOV)

The WAN_HOLDOFF_TOV is the maximum time between detection of a GFPT_WAN link failure and state machine transition from S2 to S1 (see 6.4.2). The default value of WAN_HOLDOFF_TOV is 100 ms (i.e., 2 times the normal SONET/SDH Trail restoration time limit).

6.4.10 Transparent FC-BB frame compression encoding

6.4.10.1 FC-BB_GFPT FC frame compression

If an ingress FC-BB_GFPT device is operating with compression enabled (see 6.4.7.4.2), then the ingress FC-BB_GFPT device:

- a) shall send errored FC frames as FC frames; and
- b) for error-free FC frames, optionally encode them into GFPT_WAN compressed FC frames as follows:
 - 1) replace the K28.5 character of the SOF with a K28.7 character and leave the remaining three characters of the SOF unchanged;
 - 2) send all the data words in the FC frame between the SOF and the EOF through the compression algorithm;
 - 3) pad the end of the encoded data stream to a transmission word boundary prior to the EOF; and
 - 4) append the original EOF.

NOTE 12 – A compression algorithm may not compress some frames due to throughput performance or compression performance reasons.

After encoding, the stream is converted using Asynchronous GFPT.

If an egress FC-BB_GFPT device is operating with compression allowed (see 6.4.7.4.2), then the egress FC-BB_GFPT device shall process a frame as follows:

- a) Convert the entire stream from Asynchronous GFPT encoding into FC encoding;
- b) if the frame begins with a K28.7 SOF:
 - 1) translate the FC frame K28.7 SOF back to the original K28.5 SOF;
 - 2) send all the data words in the FC frame between the SOF and the EOF through the decompression algorithm; and
 - 3) append the original EOF; and
- c) forward the data stream to the FC layer for further processing (e.g., LEM).

6.4.10.2 FC-BB_PW FC frame compression

An FC-BB_PW device does not use frame compression.

6.4.10.3 LZS compression algorithm

The LZS algorithm (see ANSI INCITS 241-1994 (R1999)) is a general purpose lossless compression algorithm for use with a wide variety of data types.

The sender shall reset the LZS decompression history prior to processing each compressed FC frame. This ensures that each FC frame may be decompressed independently of any other and prevents errors in one frame from causing decompression errors in any subsequent frames that the receiver decompresses.

The decompression process of each compressed frame shall be performed as specified in ANSI INCITS 241-1994 (R1999).

7 FC-BB_E Structure and Concepts

7.1 Applicability

Clause 4 specifies the FC-BB_E reference model. This clause specifies the FC-BB_E functional models.

7.2 FC-BB_E overview

This clause specifies aspects of the FC-BB_E mapping, including initialization and procedures for the mapping of Fibre Channel frames over Ethernet.

Figure 29 shows how FC-BB_E maps the Fibre Channel levels and sublevels over IEEE 802.3 layers.

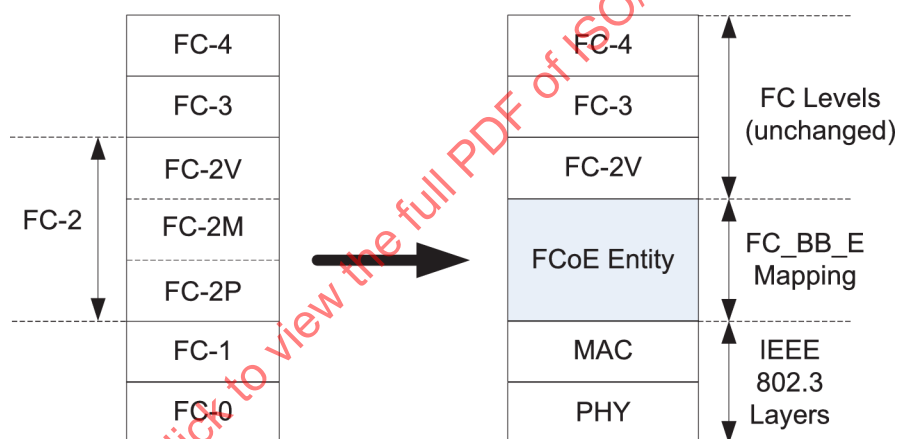


Figure 29 – FC-BB_E mapping

Figure 30 shows how the FC-BB_E mapping applies to FCoE Forwarders (FCFs) and FCoE Nodes (ENodes).

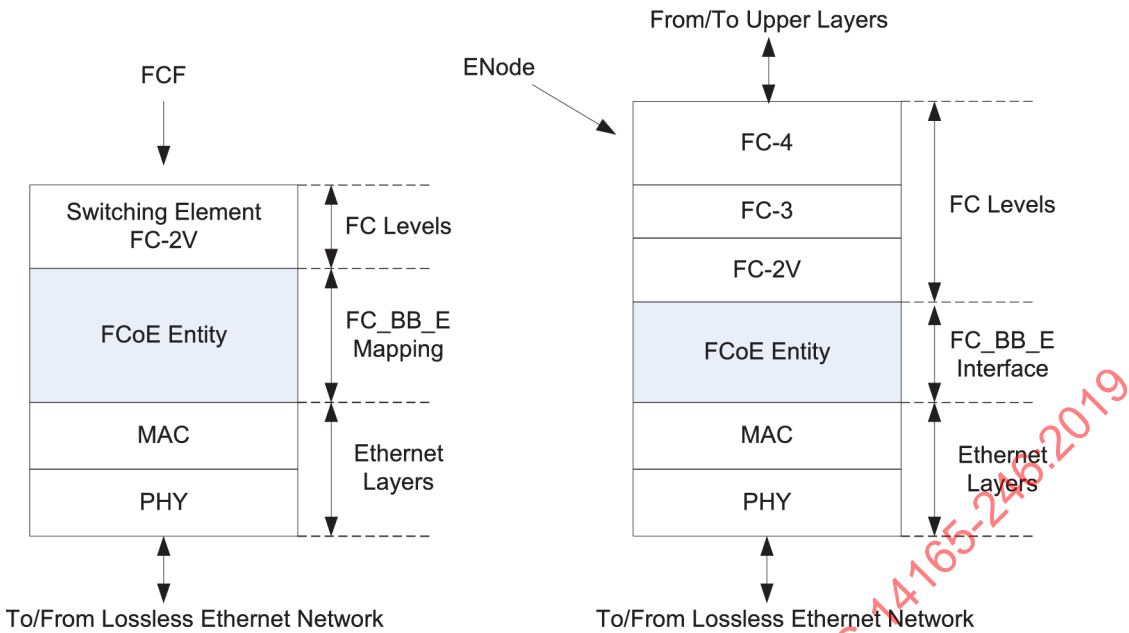


Figure 30 – FC-BB_E protocol levels and layers

FC-BB_E defines a mapping of Fibre Channel over Ethernet (FCoE). Although a generic Ethernet network may lose frames due to congestion, a proper implementation of appropriate Ethernet extensions (see 4.4.6) allows a full duplex Ethernet link to provide a lossless behavior equivalent to the one provided by the buffer-to-buffer credit mechanism (see FC-FS-4). The protocol mapping defined by FC-BB_E is referred to as Fibre Channel over Ethernet (FCoE) and shall use an underlying lossless Ethernet layer (i.e., composed of full duplex links only and providing a lossless behavior when carrying FCoE frames (see 4.4.4)).

In native Fibre Channel, Fibre Channel Nodes (see FC-FS-4) and Switches (see FC-SW-6) communicate through FC_Ports. Fibre Channel links connect PN_Ports to PE_Ports, PE_Ports to PE_Ports, and PA_Ports to PA_Ports. Fibre Channel links also connect PN_Ports to PN_Ports in the point-to-point and Arbitrated Loop topologies.

In Fibre Channel over Ethernet, FCoE Nodes (ENodes), FCoE Forwarders (FCFs), and FCoE Data-Plane Forwarders (FDFs) communicate through Ethernet ports over a Lossless Ethernet network. FCoE Virtual Links replace the physical Fibre Channel links by encapsulating FC frames in Ethernet frames. FCoE supports VE_Port to VE_Port Virtual Links, VN_Port to VF_Port Virtual Links, VN_Port to VN_Port Virtual Links, and VA_Port to VA_Port Virtual Links. ENodes supporting VN_Port to VN_Port Virtual Links are called VN2VN ENodes. Virtual Links are identified by the pair of MAC addresses of the two link end-points.

Figure 31 shows an example FCoE VN_Port to VF_Port network configuration.

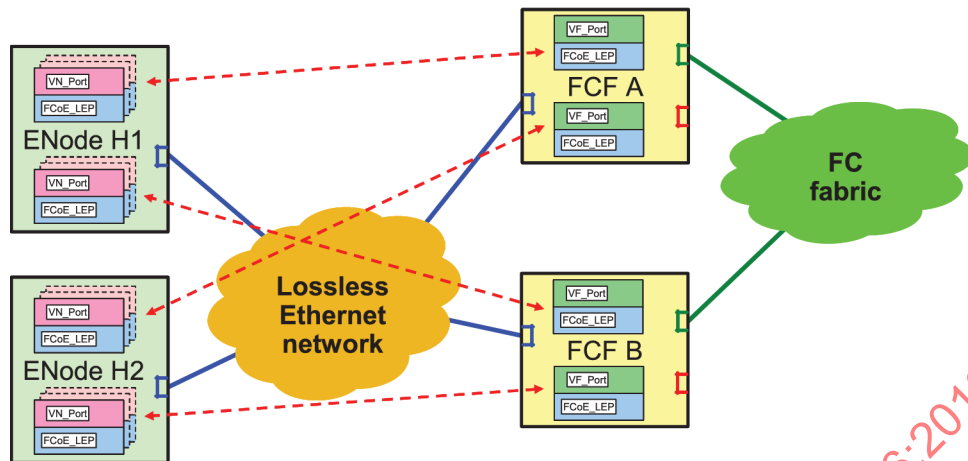


Figure 31 – FCoE VN_Port to VF_Port network configuration example

Each of the two ENodes H1 and H2 depicted in Figure 31 has a single physical Ethernet connection to the Lossless Ethernet network. Each of the two FCFs, FCF A and FCF B, has a single physical Ethernet connection to the Lossless Ethernet network. Each ENode may instantiate multiple VN_Ports, connected to VF_Ports instantiated by the FCFs through FCoE Virtual Links. The dashed lines in Figure 31 depict possible VN_Port to VF_Port Virtual Links. In this case, a Lossless Ethernet network is reduced by FCoE to a set of point-to-point VN_Port to VF_Port Virtual Links where the VN_Port to VF_Port Fibre Channel protocols are able to operate.

Figure 32 shows an example FCoE VE_Port to VE_Port network configuration.

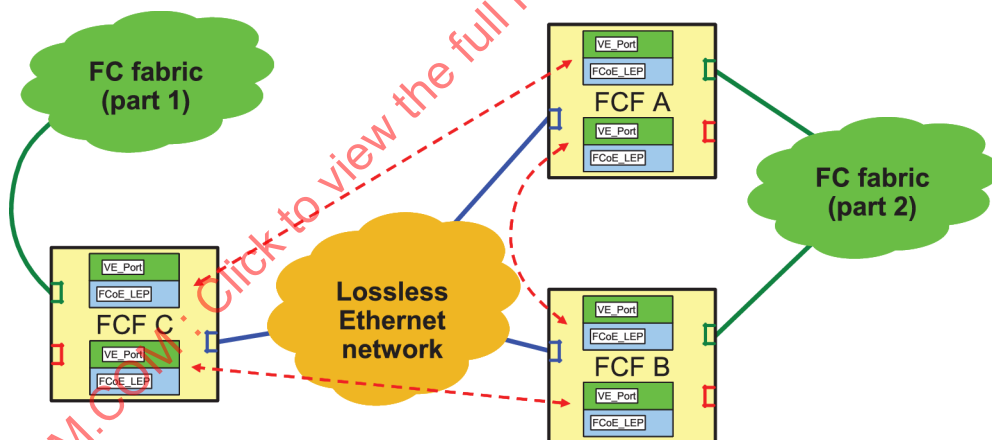


Figure 32 – FCoE VE_Port to VE_Port network configuration example

Each of the three FCFs, FCF A, FCF B, and FCF C, depicted in Figure 32 has a single physical Ethernet connection to the Lossless Ethernet network. Each FCF may instantiate multiple VE_Ports, connected to other VE_Ports through FCoE Virtual Links. The dashed lines in Figure 32 depict possible VE_Port to VE_Port Virtual Links. In this case, a Lossless Ethernet network is reduced by FCoE to a set of point-to-point VE_Port to VE_Port Virtual Links where the VE_Port to VE_Port Fibre Channel protocols are able to operate.

As shown in Figure 31 and Figure 32, Fibre Channel over Ethernet enables some additional features with respect to native Fibre Channel:

- an ENode may establish VN_Port to VF_Port Virtual Links (i.e., perform Fabric Login) with multiple FCFs through a single Lossless Ethernet MAC;
- an FCF may establish VN_Port to VF_Port Virtual Links (i.e., accept Fabric Login) with multiple ENodes through a single Lossless Ethernet MAC; and

- c) an FCF may establish VE_Port to VE_Port Virtual Links with multiple other FCFs through a single Lossless Ethernet MAC.

Figure 33 shows an example FCoE VN_Port to VN_Port network configuration.

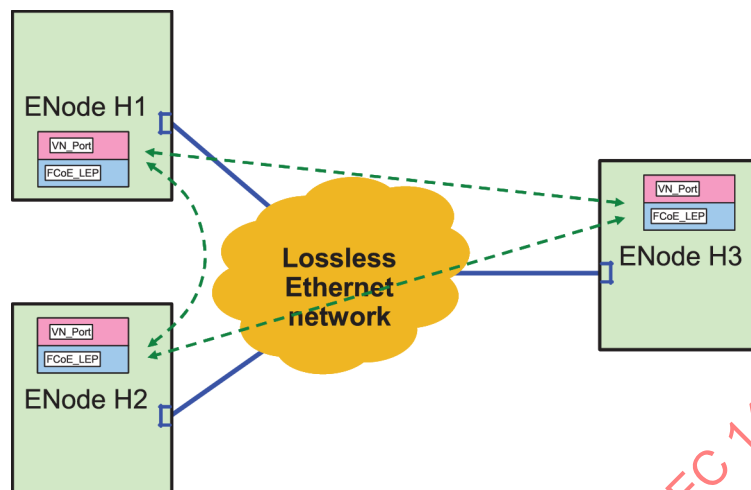


Figure 33 – FCoE multi-node VN_Port to VN_Port network configuration example

Each of the three VN2VN ENodes, ENode H1, ENode H2, and ENode H3, depicted in Figure 33 has a single physical Ethernet connection to the Lossless Ethernet network. Each VN2VN ENode may instantiate one or more VN_Ports. Each of these VN_Ports may be connected to VN_Ports instantiated by other VN2VN ENodes through FCoE VN_Port to VN_Port Virtual Links. The dashed lines in Figure 33 depict possible VN_Port to VN_Port Virtual Links. In this case, a Lossless Ethernet network is reduced by FCoE to a set of point-to-point VN_Port to VN_Port Virtual Links where the VN_Port to VN_Port Fibre Channel point-to-point protocols are able to operate.

Figure 34 shows an example of a mixed FCoE network configuration, including both VN_Port to VN_Port Virtual Links and VN_Port to VF_Port Virtual Links. The operations of the VN_Port to VN_Port Virtual Links are independent from the operations of the VN_Port to VF_Port Virtual Links.

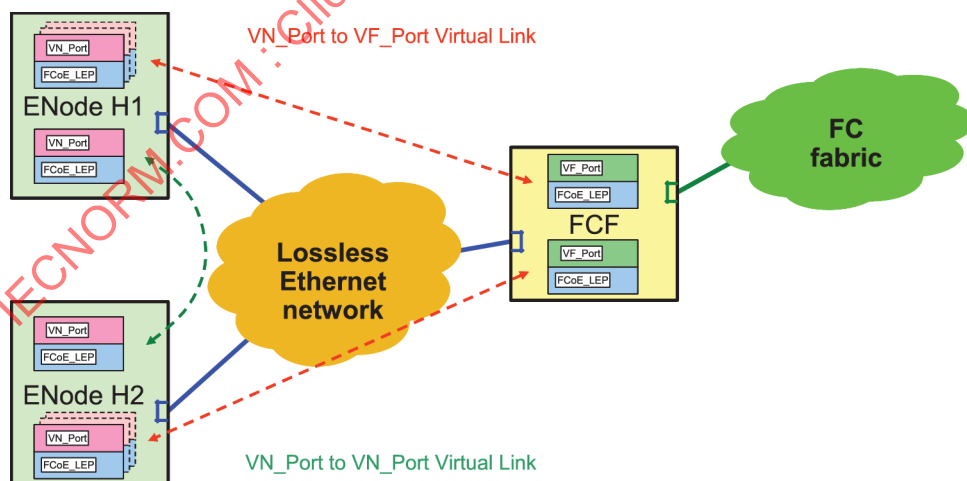


Figure 34 – FCoE mixed network configuration example

Each of the two VN2VN ENodes, ENode H1 and ENode H2, depicted in Figure 34 has a single physical Ethernet connection to the Lossless Ethernet network. The FCF has a single physical Ethernet connection to the Lossless Ethernet network.

Each VN2VN ENode may instantiate multiple VN_Ports, connected to VF_Ports instantiated by the FCF through FCoE VN_Port to VF_Port Virtual Links. The red dashed lines in Figure 34 depict possible VN_Port to VF_Port Virtual Links. In this case, a Lossless Ethernet network is reduced by FCoE to a set of point-to-point VN_Port to VF_Port Virtual Links where the VN_Port to VF_Port Fibre Channel protocols are able to operate.

Each VN2VN ENode may also instantiate a VN_Port connected to VN_Ports instantiated by other VN2VN ENodes through FCoE VN_Port to VN_Port Virtual Links. The green dashed line in Figure 34 depicts a possible VN_Port to VN_Port Virtual Link. In this case, a Lossless Ethernet network is reduced by FCoE to a set of point-to-point VN_Port to VN_Port Virtual Links where the VN_Port to VN_Port point-to-point Fibre Channel protocols are able to operate.

Figure 35 shows an example FCoE point-to-point VN_Port to VN_Port network configuration.

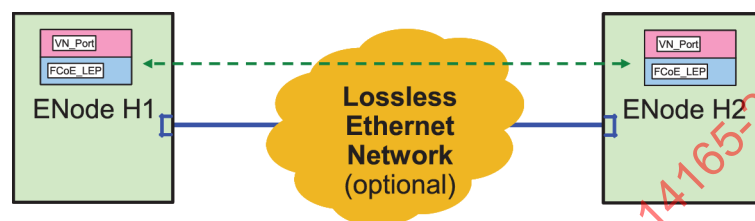


Figure 35 – FCoE point-to-point VN_Port to VN_Port network configuration example

Each of the two VN2VN ENodes, ENode H1 and ENode H2, depicted in Figure 35 has a single physical Ethernet connection to the Lossless Ethernet network. Each VN2VN ENode may instantiate a VN_Port connected to a VN_Port instantiated by other VN2VN ENode through a FCoE VN_Port to VN_Port Virtual Link. The dashed line in Figure 35 depicts a possible VN_Port to VN_Port Virtual Link. In this case, a Lossless Ethernet network is reduced by FCoE to a point-to-point VN_Port to VN_Port Virtual Link where the VN_Port to VN_Port point-to-point Fibre Channel protocols are able to operate.

The point-to-point configuration is a special case because certain environments require ensuring that a maximum of two VN2VN ENode MACs are present over a Lossless Ethernet network and operating in Fabric mode if one or more FCFs are detected (e.g., instantiating only the VN_Port to VF_Port Virtual Links in the example configuration shown in Figure 34). These requirements do not apply to the multi-node VN_Port to VN_Port configuration shown in Figure 33, therefore VN2VN ENode MACs shall provide a way to enable the appropriate behavior (i.e., to operate in multi-node mode or in point-to-point mode).

The point-to-point protocol enables faster initialization for the case of two VN2VN ENode MACs connected through a single cable or for the case of only two VN2VN ENode MACs visible to each other over a Lossless Ethernet network (i.e., N_Port_ID Probe Requests are not used).

Figure 36 shows an example of an FCoE VA_Port to VA_Port network configuration.

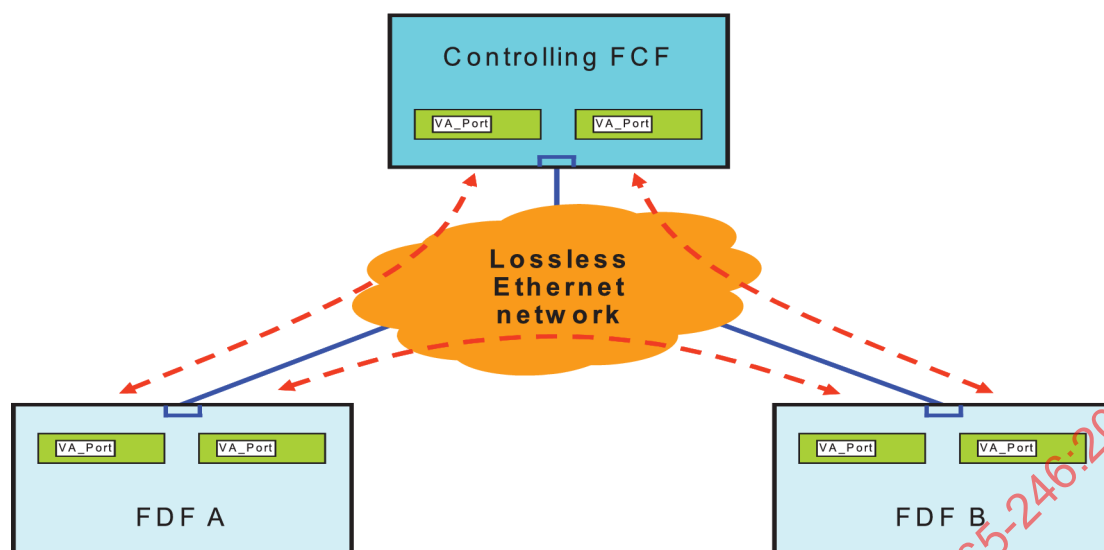


Figure 36 – FCoE VA_Port to VA_Port network configuration example

Each of the two FDFs, FDF A and FDF B, depicted in Figure 36 has a single physical Ethernet connection to the Lossless Ethernet network. The Controlling FCF has a single physical Ethernet connection to the Lossless Ethernet network. Each FDF and Controlling FCF may instantiate multiple VA_Ports, connected to other VA_Ports through FCoE Virtual Links. The dashed lines in Figure 36 depict possible VA_Port to VA_Port Virtual Links. In this case, a Lossless Ethernet network is reduced by FCoE to a set of point-to-point VA_Port to VA_Port Virtual Links where the VA_Port to VA_Port Fibre Channel protocols are able to operate (see FC-SW-6).

7.3 ENode functional model

Figure 37 shows the functional model of an ENode if operating in a Fabric, where the bracketed and dashed functional components are optional. An ENode is functionally composed of at least one Lossless Ethernet MAC (i.e., the ENode MAC), and an FCoE Controller function for each ENode MAC.

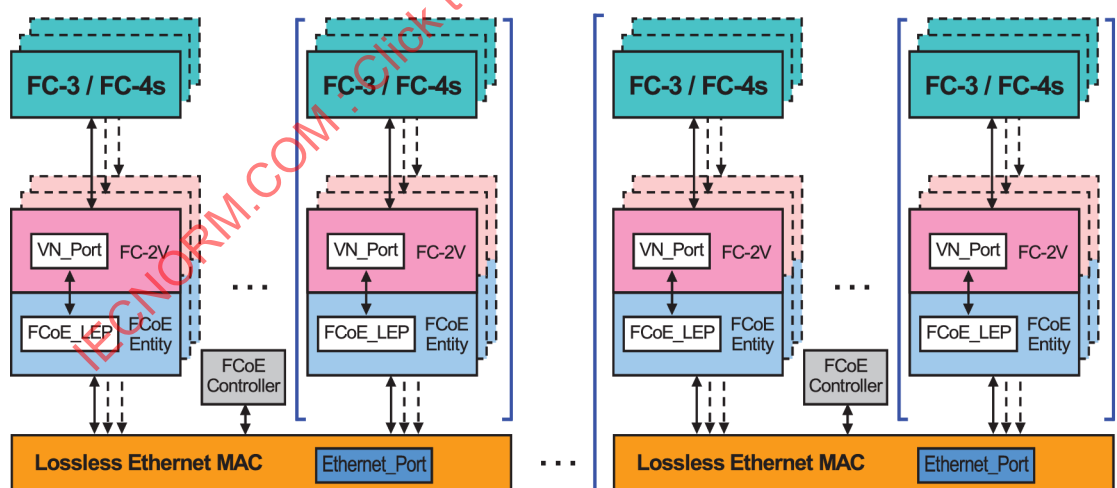


Figure 37 – ENode functional model

The FCoE Controller associated with an ENode MAC shall support the instantiation of VN_Port/FCoE_LEP pairs.

The FCoE Controller is the functional entity that performs the FCoE Initialization Protocol (FIP) and instantiates or de-instantiates VN_Port/FCoE_LEP pairs as needed.

For an ENode's MAC, the FCoE Controller:

- a) optionally initiates the FIP VLAN discovery protocol to discover FCoE VLANs;
- b) should initiate the FIP discovery protocol in order to discover VF_Port capable FCF-MACs or VF_Port capable FDF-MACs connected to the same Lossless Ethernet network;
- c) initiates FIP FLOGI Exchanges and instantiates a VN_Port/FCoE_LEP pair on successful completion of each FIP FLOGI Exchange with a VF_Port capable FCF-MAC or VF_Port capable FDF-MAC;
- d) optionally initiates FIP NPIV FDISC Exchanges and instantiates a VN_Port/FCoE_LEP pair on successful completion of each FIP NPIV FDISC Exchange with a VF_Port capable FCF-MAC or VF_Port capable FDF-MAC;
- e) de-instantiates a VN_Port/FCoE_LEP pair if that VN_Port is logged out from the Fabric;
- f) initiates FIP LOGO Exchanges if explicit Fabric logout is needed;
- g) de-instantiates the indicated VN_Port/FCoE_LEP pairs on receiving FIP Clear Virtual Links requests;
- h) transmits periodic FIP Keep Alive frames on behalf of the ENode MAC every FKA_ADV_PERIOD, unless the Disable Virtual Link Maintenance bit is set to one in received Discovery Advertisements (see 7.11.5.2);
- i) monitors the status of instantiated VN_Port/FCoE_LEP pairs and transmits periodic FIP Keep Alive frames on their behalf every FKA_VN_PERIOD, unless the Disable Virtual Link Maintenance bit is set to one in received Discovery Advertisements (see 7.11.5.2); and
- j) monitors the status of VF_Ports to which the instantiated VN_Port/FCoE_LEP pairs are logged in by maintaining timers and verifying periodic FIP Discovery Advertisements are received within every FKA_ADV_PERIOD, unless the Disable Virtual Link Maintenance bit is set to one in received Discovery Advertisements (see 7.11.5.2).

The FCoE Controller of an ENode MAC may perform FIP FLOGIs and FIP NPIV FDISCs with multiple VF_Port capable FCF-MACs or VF_Port capable FDF-MACs. VN_Ports instantiated upon successful FIP FLOGI and subsequent FIP NPIV FDISC Exchanges are all associated with the same VF_Port that was instantiated on successful completion of the FIP FLOGI Exchange. In Figure 37, each stack of VN_Port/FCoE_LEP pairs represents an association with a different VF_Port capable FCF-MAC or VF_Port capable FDF-MAC.

The FCoE_LEP is the functional entity performing the encapsulation of FC frames into FCoE frames during transmission and the decapsulation of FCoE frames into FC frames during reception. When encapsulating FC frames into FCoE frames, the MAC address of the local link end-point shall be used as the source address and the MAC address of the remote link end-point shall be used as the destination address of the generated FCoE frame. When decapsulating FC frames from FCoE frames, the FCoE_LEP shall verify that the destination address of the received FCoE frame is equal to the MAC address of the local link end-point and shall verify that the source address of the received FCoE frame is equal to the MAC address of the remote link end-point. If either check fails the FCoE frame shall be discarded.

For an FCoE_LEP of an ENode MAC, the MAC address of the local link end-point is the MAC address associated with its VN_Port and the remote link end-point address is the FCF-MAC or FDF-MAC address associated with the remote VF_Port. The VN_Port shall use an FPMA (see 7.9) as its MAC address.

A VN_Port is an instance of the FC-2V sublevel of Fibre Channel that operates as an N_Port (see FC-FS-4) and is dynamically instantiated together with its FCoE_LEP on successful completion of a FIP FLOGI Exchange or a FIP NPIV FDISC Exchange. A VN_Port receives FC frames from the upper FC levels and sends them to its FCoE_LEP for encapsulation and transmission over the Lossless Ethernet network. In a similar way, a VN_Port sends FC frames received from its FCoE_LEP to the upper FC levels. A VN_Port may support one or more FC-4s. A VN_Port is uniquely identified by an N_Port_Name Name_Identifier and is addressed by the N_Port_ID the Fabric assigned to it. The VN_Port behavior shall be as specified in FC-LS-3 and FC-FS-4, with the exception that a VN_Port is instantiated on successful completion of a FIP FLOGI Exchange or a FIP NPIV FDISC Exchange, ignoring the buffer-to-buffer flow

control parameters. When receiving FC frames from its FCoE_LEP, a VN_Port shall verify that the D_ID of the received FC frame is equal to its N_Port_ID. If the check fails the FC frame shall be discarded.

NOTE 13 – The receive checks performed by the VN_Port/FCoE_LEP pair verify that the correct MAC destination address, MAC source address, and D_ID are present in a received FCoE frame (see D.5).

7.4 VN2VN ENode functional model

Figure 38 shows the functional model of a VN2VN ENode, where the bracketed and dashed functional components are optional. A VN2VN ENode is functionally composed of at least one Lossless Ethernet MAC (i.e., the ENode MAC), and an FCoE Controller function for each ENode MAC.

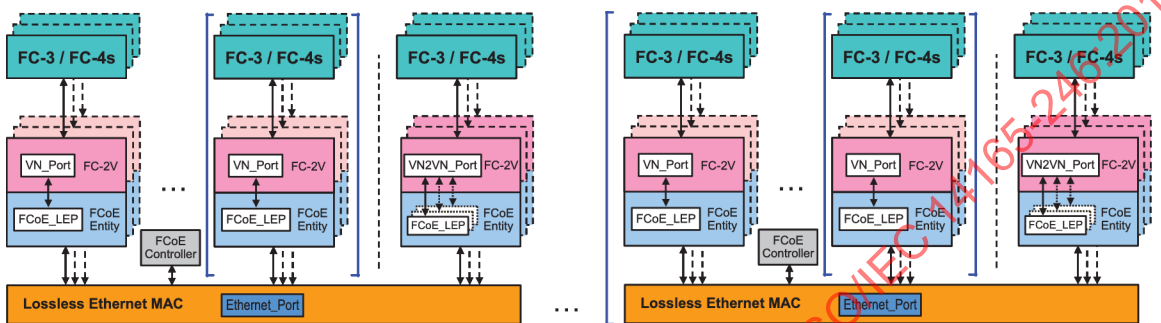


Figure 38 – VN2VN ENode functional model

The FCoE Controller of a VN2VN ENode MAC may instantiate one or more VN2VN_Ports (i.e., VN_Ports each of which is able to support VN_Port to VN_Port Virtual Links). If operating in multi-node mode, the FCoE Controller of a VN2VN ENode MAC may perform FIP FLOGIs with multiple remote VN2VN ENode MACs to instantiate multiple VN_Port to VN_Port Virtual Links. This results in multiple FCoE_LEPs associated with a VN2VN_Port. If operating in point-to-point mode, only one FCoE_LEP is associated with a VN2VN_Port.

Because there is no FCF that performs N_Port_ID selection, VN2VN ENode MACs shall select N_Port_IDs as specified in 7.11.6.

N_Port_IDs used for VN_Port to VN_Port Virtual Links shall be unique over the Lossless Ethernet network to which VN2VN ENode MACs are connected and are suitable to be used only for communications over that Lossless Ethernet network. These N_Port_IDs are called Locally Unique N_Port_IDs.

The MAC address used as VN_Port MAC address for a VN2VN_Port is determined by concatenating its Locally Unique N_Port_ID to the constant VN2VN-FC-MAP (see Table 53). This enables easy recognition of the MAC addresses used by FCoE for VN_Port to VN_Port communications, because they all share the same VN2VN-FC-MAP prefix.

Figure 34 shows a mixed FCoE network consisting of both VN_Port to VF_Port virtual links and VN_Port to VN_Port virtual links. In such a configuration, Locally Unique N_Port_IDs shall not conflict with and shall be independent from the N_Port_IDs assigned by a Fibre Channel Fabric. This ensures each VN_Port has a unique N_Port_ID. Locally Unique N_Port_IDs shall be in the range 000001h to 00FFFEh, inclusive. This ensures that they do not conflict with N_Port_IDs assigned by a Fibre Channel Fabric, because a Fabric does not assign N_Port_IDs with Domain_ID zero.

For a VN2VN ENode MAC, the FCoE Controller:

- may participate in Fabric operations (see 7.3);
- operates in either multi-node mode or point-to-point mode;
- optionally initiates the FIP VLAN discovery protocol to discover FCoE VLANs (see 7.11.2.4);
- selects a tentative Locally Unique N_Port_ID (see 7.11.6.1);

- e) probes the network about the selected Locally Unique N_Port_ID (see 7.11.6.2.1) if operating in multi-node mode;
- f) claims the selected Locally Unique N_Port_ID (see 7.11.6.2.2, 7.11.6.2.1, 7.11.6.3.1);
- g) instantiates VN_Port to VN_Port Virtual Links through FIP FLOGI Exchanges (see 7.11.4.3);
- h) optionally de-instantiates VN_Port to VN_Port Virtual Links through FIP Fabric LOGO Exchanges (see 7.11.4.3);
- i) transmits periodic FIP Beacon frames every BEACON_PERIOD; and
- j) monitors the status of VN_Port to VN_Port Virtual Links and the VN2VN Neighbor Set (see 7.11.6.2.2) by maintaining timers and verifying periodic FIP Beacons are received within every BEACON_PERIOD (see 7.11.5.4).

The FCoE_LEP is the functional entity performing the encapsulation of FC frames into FCoE frames during transmission and the decapsulation of FCoE frames into FC frames during reception. When encapsulating FC frames into FCoE frames, the MAC address of the local link end-point shall be used as the source address and the MAC address of the remote link end-point shall be used as the destination address of the generated FCoE frame. When decapsulating FC frames from FCoE frames, the FCoE_LEP shall verify that the destination address of the received FCoE frame is equal to the MAC address of the local link end-point and shall verify that the source address of the received FCoE frame is equal to the MAC address of the remote link end-point. If either check fails the FCoE frame shall be discarded. For an FCoE_LEP associated with a VN2VN_Port, the MAC address of the local link end-point is the MAC address associated with that VN2VN_Port and the remote link end-point address is the MAC address associated with the remote VN2VN_Port. Therefore the source MAC address of FCoE frames used for VN_Port to VN_Port Virtual Links is 'VN2VN-FC-MAP || S_ID' and the destination MAC address is 'VN2VN-FC-MAP || D_ID'. On receiving an FCoE frame, the FCoE_LEP associated with a VN2VN_Port shall verify that the least significant 24 bits of the source MAC address are equal to the S_ID of the encapsulated FC frame and that the least significant 24 bits of the destination MAC address are equal to the D_ID of the encapsulated FC frame. If either check fails the FCoE frame shall be discarded.

7.5 FCF functional model

Figure 39 shows the functional model of an FCF, where the bracketed and dashed functional components are optional. An FCF is functionally composed of a Fibre Channel Switching Element (see FC-SW-6) with at least one Lossless Ethernet MAC (FCF-MAC). Each FCF-MAC shall be coupled with an FCoE Controller function. Each FCF-MAC may be coupled with a Lossless Ethernet bridging element. The Fibre Channel Switching Element may be coupled with a Fibre Channel Fabric interface, providing native E_Port and F_Port connectivity. An FCF forwards FCoE frames addressed to one of its FCF-MACs based on the D_ID of the encapsulated FC frames.

The Fibre Channel component of an FCF consists of the set of:

- a) FCSwitching Element;
- b) VE_Ports;
- c) VF_Ports;
- d) E_Ports, if any; and
- e) F_Ports, if any.

The FCoE component of an FCF consists of the set of FCoE_LEPs and FCoE Controllers.

The Ethernet component of an FCF consists of the set of Lossless Ethernet MACs and Lossless Ethernet Bridging Elements, if any.

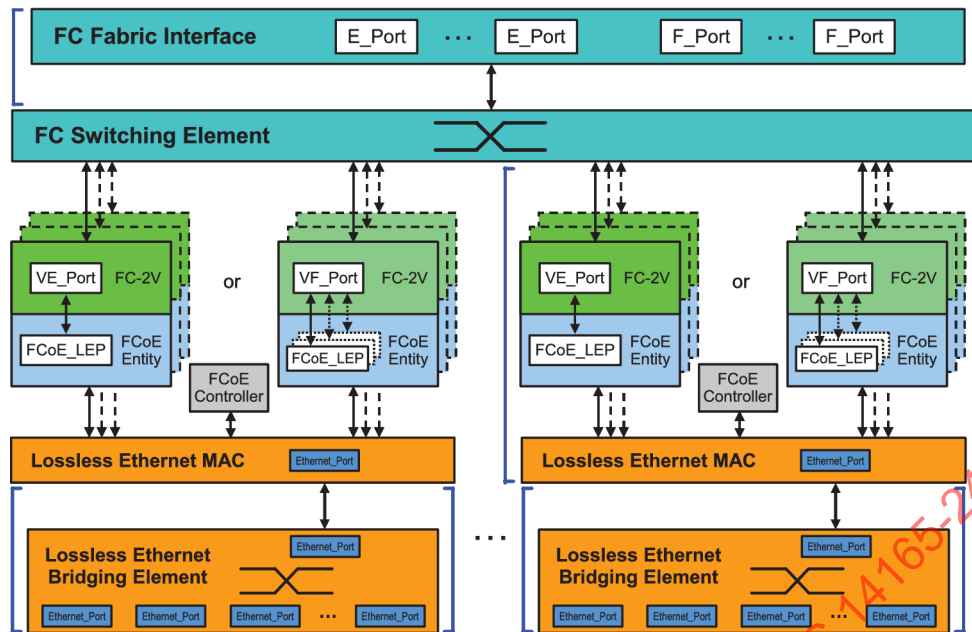


Figure 39 – FCF functional model

NOTE 14 – Other combinations of Lossless Ethernet bridging elements and Lossless Ethernet MACs are allowed.

If an FCF includes Lossless Ethernet bridging elements, then an FCF-MAC address may be accessible via multiple externally facing Ethernet ports on that FCF.

The FCoE Controller associated with an FCF-MAC shall support the instantiation of VE_Port/FCoE_LEP pairs or VF_Port/FCoE_LEP pairs. An FCF-MAC supporting the instantiation of VE_Port/FCoE_LEP pairs is referred to as a VE_Port capable FCF-MAC. An FCF-MAC supporting the instantiation of VF_Port/FCoE_LEP pairs is referred to as a VF_Port capable FCF-MAC. Support for both VE_Port/FCoE_LEP pairs and VF_Port/FCoE_LEP pairs on the same FCF-MAC is prohibited.

The FCoE Controller is the functional entity that performs the FCoE Initialization Protocol (FIP) and instantiates or de-instantiates VE_Port/FCoE_LEP pairs or VF_Port/FCoE_LEP pairs as needed.

For a VE_Port capable FCF-MAC, the FCoE Controller:

- optionally performs the FIP VLAN discovery protocol to discover FCoE VLANs;
- discovers other VE_Port capable FCF-MACs connected to the same Lossless Ethernet network using the FIP discovery protocol;
- instantiates a VE_Port/FCoE_LEP pair on successful completion of each FIP ELP Exchange with a remote FCF-MAC;
- de-instantiates a VE_Port/FCoE_LEP pair on receiving a FIP Clear Virtual Links request;
- monitors the status of the instantiated VE_Port/FCoE_LEP pairs;
- transmits FIP Clear Virtual Links requests as needed to terminate Virtual Links to other VE_Ports;
- transmits periodic FIP Discovery Advertisements to the All-FCF-MACs address every FKA_ADV_PERIOD; and
- monitors the status of remote VE_Ports by maintaining timers and verifying that periodic FIP Discovery Advertisements are received within every FKA_ADV_PERIOD.

For a VF_Port capable FCF-MAC, the FCoE Controller:

- a) participates in the FIP VLAN discovery protocol initiated by an ENode MAC;
- b) participates in the FIP discovery protocol initiated by an ENode MAC;
- c) instantiates a VF_Port/FCoE_LEP pair on successful completion of each FIP FLOGI Exchange initiated by an ENode MAC;
- d) instantiates an additional FCoE_LEP on successful completion of each FIP NPIV FDISC Exchange initiated by an already logged in ENode MAC. The resulting VF_Port/FCoE_LEP pair shares the VF_Port with the VF_Port/FCoE_LEP pair instantiated on successful completion of the FIP FLOGI Exchange;
- e) if a VN_Port is logged out, de-instantiates the FCoE_LEP associated to that VN_Port and de-instantiates the corresponding VF_Port if that FCoE_LEP was the only one associated with that VF_Port;
- f) transmits FIP Clear Virtual Links requests as needed to terminate Virtual Links to other VN_Ports;
- g) monitors the status of the instantiated VF_Port/FCoE_LEP pairs;
- h) transmits periodic FIP Discovery Advertisements to the All-ENode-MACs address every FKA_ADV_PERIOD;
- i) monitors the status of the logged in ENode MACs by verifying that periodic FIP Keep Alive frames are received within FKA_ADV_PERIOD, unless the Disable Virtual Link Maintenance bit is set to one in sent Discovery Advertisements (see 7.11.5.2); and
- j) monitors the status of the logged in VN_Ports by maintaining timers and verifying that periodic FIP Keep Alive frames are received within FKA_VN_PERIOD, unless the Disable Virtual Link Maintenance bit is set to one in sent Discovery Advertisements (see 7.11.5.2).

The FCoE Controller of an ENode MAC may perform FIP FLOGIs and FIP NPIV FDISCs with multiple VF_Port capable FCF-MACs. VN_Ports instantiated by the FCoE Controller of an ENode MAC on successful completion of FIP NPIV FDISC Exchanges with a VF_Port capable FCF-MAC are all associated with the same VF_Port. This VF_Port is instantiated by the FCoE Controller of that VF_Port capable FCF-MAC on successful completion of a FIP FLOGI Exchange.

The FCoE_LEP is the functional entity performing the encapsulation of FC frames into FCoE frames during transmission and the decapsulation of FCoE frames into FC frames during reception. When encapsulating FC frames into FCoE frames, the MAC address of the local link end-point shall be used as the source address and the MAC address of the remote link end-point shall be used as the destination address of the generated FCoE frame. When decapsulating FC frames from FCoE frames, the FCoE_LEP shall verify that the destination address of the received FCoE frame is equal to the MAC address of the local link end-point and shall verify that the source address of the received FCoE frame is equal to the MAC address of the remote link end-point. If either check fails the FCoE frame shall be discarded.

For a VE_Port capable FCF-MAC, the MAC address of the local link end-point is the FCF-MAC address and the MAC address of the remote link end-point is the MAC address of the remote FCF-MAC with which a FIP ELP Exchange has been successfully completed.

For a VF_Port capable FCF-MAC, the MAC address of the local link end-point is the FCF-MAC address and the MAC address of the remote link end-point is the MAC address associated with the remote logged in VN_Port. The remote VN_Port shall use an FPMA as its MAC address.

A VE_Port is an instance of the FC-2V sublevel of Fibre Channel that operates as an E_Port in accordance with FC-SW-6 and is dynamically instantiated together with its FCoE_LEP on successful completion of a FIP ELP Exchange. A VE_Port receives FC frames from the FC Switching Element and sends them to its FCoE_LEP for encapsulation and transmission over the Lossless Ethernet network. In a similar way, a VE_Port sends FC frames received from its FCoE_LEP to the FC Switching Element. A VE_Port is uniquely identified by an E_Port_Name Name_Identifier and is

addressed by the Fabric Controller N_Port_ID (i.e., FFFFFDh). The VE_Port behavior shall be as specified in FC-SW-6, with the exception that a VE_Port is instantiated on successful completion of a FIP ELP Exchange, ignoring the buffer-to-buffer flow control parameters.

A VF_Port is an instance of the FC-2V sublevel of Fibre Channel that operates as an F_Port in accordance with FC-SW-6 and is dynamically instantiated together with its FCoE_LEP on successful completion of a FIP FLOGI Exchange. A VF_Port receives FC frames from the FC Switching Element and sends them to the proper FCoE_LEP for encapsulation and transmission over the Lossless Ethernet network. In a similar way, a VF_Port sends FC frames received from one of its FCoE_LEPs to the Fibre Channel Switching Element. A VF_Port is uniquely identified by an F_Port_Name Name_Identifier and is addressed by the F_Port Controller N_Port_ID (i.e., FFFFFEh). The VF_Port behavior shall be as specified in FC-LS-3 and FC-FS-4, with the exception that a VF_Port is instantiated on successful completion of a FIP FLOGI Exchange, ignoring the buffer-to-buffer flow control parameters. When receiving FC frames from one of its FCoE_LEPs, a VF_Port shall verify that the S_ID of the received FC frame is equal to the N_Port_ID of the VN_Port associated to that FCoE_LEP. If the check fails the FC frame shall be discarded.

NOTE 15 – The receive checks performed by the VF_Port/FCoE_LEP pair verify that the correct MAC destination address, MAC source address, and S_ID are present in a received FCoE frame (see D.5).

The Fibre Channel Switching Element is the functional entity performing Fibre Channel switching among E_Ports, F_Ports, VE_Ports, and VF_Ports. A Fibre Channel Switching Element is uniquely identified by a Switch_Name Name_Identifier. The behavior of the Fibre Channel Switching Element shall be as specified in FC-SW-6.

7.6 Controlling FCF functional model

Figure 40 shows the functional model of a Controlling FCF, where the bracketed and dashed functional components are optional. A Controlling FCF is an FCF that supports the instantiation of at least one VA_Port with at least one Lossless Ethernet MAC (FCF-MAC), in addition to VE_Ports and VF_Ports. As for any FCF, each FCF-MAC of a Controlling FCF may be coupled with a Lossless Ethernet bridging element. A Controlling FCF may have a Fibre Channel Fabric interface, providing native E_Port, A_Port, and F_Port connectivity. Supporting the operation of a Controlling Switch (see FC-SW-6), a Controlling FCF is functionally modeled as having two FC Switching Elements, one for the Principal Domain and one for the Virtual Domain, connected by an internal VE_Port to VE_Port link. The Switching Element associated with the Principal Domain supports the instantiation of VF_Ports and VE_Ports, and the Switching Element associated with the Virtual Domain supports the instantiation of VA_Ports only.

The Fibre Channel component of a Controlling FCF consists of the set of:

- a) FC Switching Elements;
- b) VE_Ports;
- c) VF_Ports;
- d) VA_Ports;
- e) E_Ports, if any;
- f) F_Ports, if any; and
- g) A_Ports, if any.

The FCoE component of a Controlling FCF consists of the set of FCoE_LEPs and FCoE Controllers.

The Ethernet component of a Controlling FCF consists of the set of Lossless Ethernet MACs and Lossless Ethernet Bridging Elements, if any.

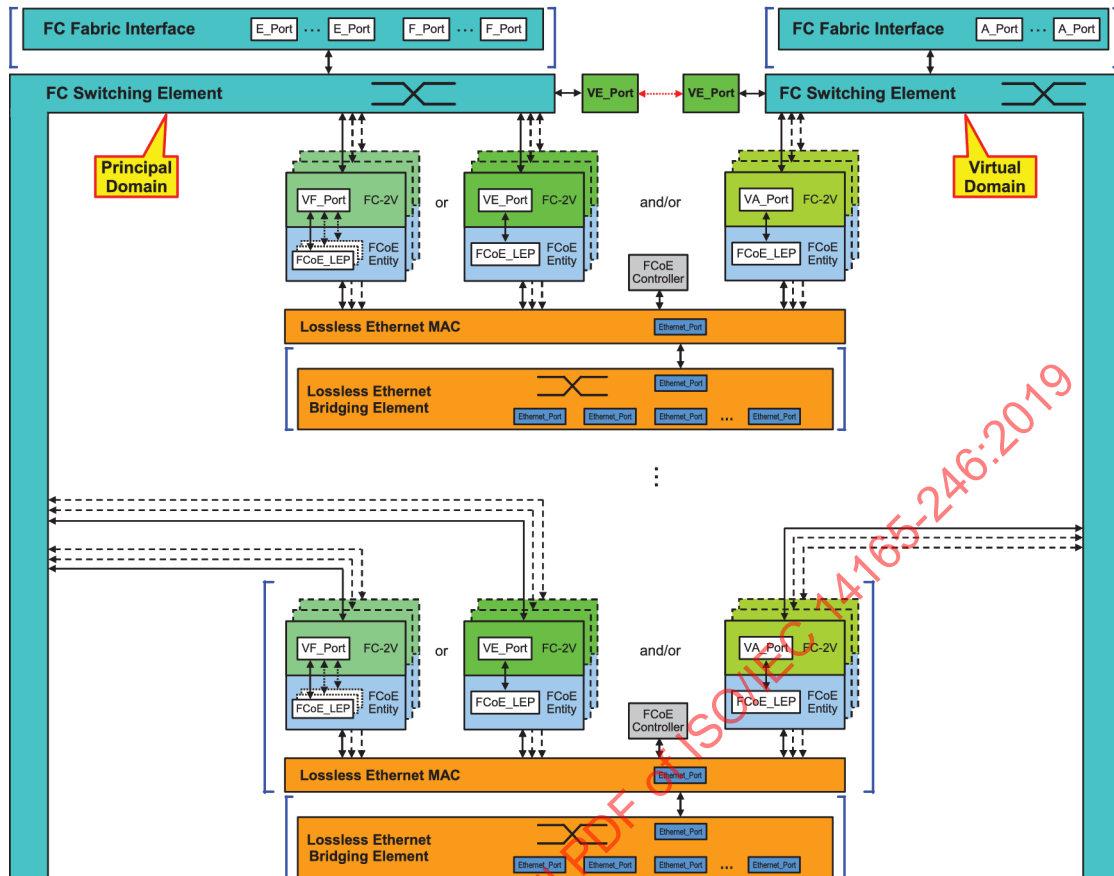


Figure 40 – Controlling FCF functional model

NOTE 16 – Other combinations of Lossless Ethernet bridging elements and Lossless Ethernet MACs are allowed.

VA_Ports and VE_Ports may coexist over the same FCF-MAC. VF_Ports coexist with neither VA_Ports nor VE_Ports over the same FCF-MAC. An FCF-MAC supporting the instantiation of VA_Ports and VE_Ports is referred to as a VA_Port/VE_port capable FCF-MAC.

The FCoE Controller is the functional entity that performs the FCoE Initialization Protocol (FIP) and instantiates or de-instantiates VA_Ports, VE_Ports, or VF_Ports, as needed.

A VF_Port capable FCF-MAC operates as specified in 7.5.

For a VA_Port/VE_Port capable FCF-MAC, the FCoE Controller:

- optionally performs the FIP VLAN discovery protocol to discover FCoE VLANs;
- discovers other VA_Port/VE_Port capable FCF-MACs, VE_Port capable FCF-MACs, and VA_Port capable FDF-MACs connected to the same Lossless Ethernet network using the FIP discovery protocol;
- instantiates a VE_Port/FCoE_LEP pair on successful completion of each FIP ELP Exchange with a remote VA_Port/VE_Port capable FCF-MAC or a remote VE_Port capable FCF-MAC;
- de-instantiates a VE_Port/FCoE_LEP pair on receiving a FIP Clear Virtual Link request;
- instantiates a VA_Port/FCoE_LEP pair on successful completion of each FIP ELP Exchange with a remote VA_Port capable FDF-MAC;
- de-instantiates a VA_Port/FCoE_LEP pair on receiving a FIP Clear Virtual Link request;

- g) monitors the status of the instantiated VE_Port/FCoE_LEP and VA_Port/FCoE_LEP pairs;
- h) initiates FIP Clear Virtual Link requests as needed to terminate Virtual Links to other VE_Ports or VA_Ports;
- i) transmits periodic FIP Discovery Advertisements to the All-FCF-MACs address every FKA_ADV_PERIOD; and
- j) monitors the status of remote VE_Ports and VA_Ports by maintaining timers and verifying that periodic FIP Discovery Advertisements are received within every FKA_ADV_PERIOD.

The FCoE_LEP is the functional entity performing the encapsulation of FC frames into FCoE frames during transmission and the decapsulation of FCoE frames into FC frames during reception. When encapsulating FC frames into FCoE frames, the MAC address of the local link end-point shall be used as the source address and the MAC address of the remote link end-point shall be used as the destination address of the generated FCoE frame. When decapsulating FC frames from FCoE frames, the FCoE_LEP shall verify that the destination address of the received FCoE frame is equal to the MAC address of the local link end-point and shall verify that the source address of the received FCoE frame is equal to the MAC address of the remote link end-point. If either check fails the FCoE frame shall be discarded.

For a VA_Port/VE_Port capable FCF-MAC, the MAC address of the local link end-point is the FCF-MAC address and the MAC address of the remote link end-point is the MAC address of the remote FCF-MAC or FDF-MAC with which a FIP ELP Exchange has been successfully completed.

A VA_Port is an instance of the FC-2V sublevel of Fibre Channel that is dynamically instantiated together with its FCoE_LEP on successful completion of a FIP ELP Exchange, according to the rules specified in 7.11.4.4. A VA_Port receives FC frames from the FC Switching Element and sends them to its FCoE_LEP for encapsulation and transmission over the Lossless Ethernet network. In a similar way, a VA_Port sends FC frames received from its FCoE_LEP to the FC Switching Element. A VA_Port is uniquely identified by an A_Port_Name Name_Identifier and is addressed by the A_Port Controller N_Port_ID (i.e., FFFFF9h).

7.7 FDF functional model

An FDF is a simplified FCoE switching entity that forwards FC frames among VA_Ports and VF_Ports through a FCDF Switching Element (see FC-SW-6). Figure 41 shows the functional model of an FDF, where the bracketed and dashed functional components are optional. An FDF is functionally composed of an FCDF Switching Element with at least one Lossless Ethernet MAC (FDF-MAC). Each FDF-MAC shall be coupled with an FCoE Controller function. Each FDF-MAC may be coupled with a Lossless Ethernet bridging element. An FDF supports the instantiation of VA_Ports and optionally VF_Ports over its FDF-MACs. The FCDF Switching Element may be coupled with a Fibre Channel Fabric interface, providing native A_Port and F_Port connectivity. An FDF forwards FCoE frames addressed to one of its FDF-MACs based on the D_ID of the encapsulated FC frames.

The Fibre Channel component of an FDF consists of the set of:

- a) FCDF Switching Element;
- b) VF_Ports;
- c) VA_Ports;
- d) F_Ports, if any; and
- e) A_Ports, if any.

The FCoE component of a FDF consists of the set of FCoE_LEPs and FCoE Controllers.

The Ethernet component of an FDF consists of the set of Lossless Ethernet MACs and Lossless Ethernet Bridging Elements, if any.

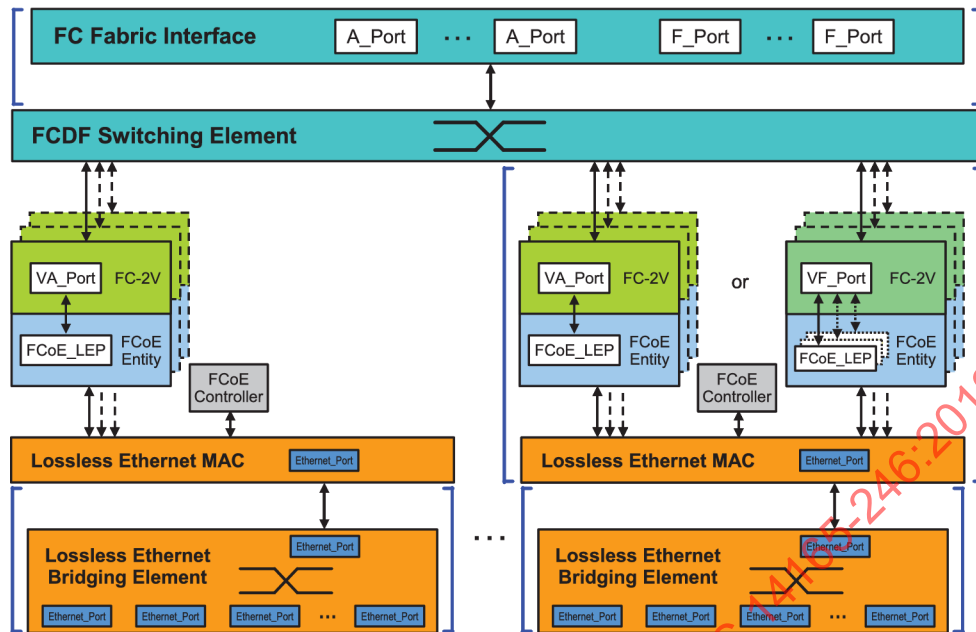


Figure 41 – FDF functional model

NOTE 17 – Other combinations of Lossless Ethernet bridging elements and Lossless Ethernet MACs are allowed.

An FDF does not support VE_Ports. VA_Ports and VF_Ports do not coexist over the same FDF-MAC. An FDF-MAC supporting the instantiation of VA_Ports is referred to as a VA_Port capable FDF-MAC. An FDF-MAC supporting the instantiation of VF_Ports is referred to as a VF_Port capable FDF-MAC. A VF_Port capable FDF-MAC behaves as a VF_Port capable FCF-MAC. An FDF shall support at least one VA_Port capable FDF-MAC and may support one or more VF_Port capable FDF-MAC.

The FCoE Controller is the functional entity that performs the FCoE Initialization Protocol (FIP) and instantiates or de-instantiates VA_Ports or VF_Ports, as needed.

A VF_Port capable FDF-MAC shall operate as a VF_Port capable FCF-MAC (see 7.5).

For a VA_Port capable FDF-MAC, the FCoE Controller:

- optionally performs the FIP VLAN discovery protocol to discover FCoE VLANs;
- discovers other VA_Port capable FDF-MACs and VA_Port/VE_Port capable FCF-MACs connected to the same Lossless Ethernet network using the FIP discovery protocol;
- instantiates a VA_Port/FCoE_LEP pair on successful completion of each FIP ELP Exchange with a remote VA_Port capable FDF-MAC or VA_Port/VE_Port capable FCF-MAC;
- de-instantiates a VA_Port/FCoE_LEP pair on receiving a FIP Clear Virtual Link request;
- monitors the status of the instantiated VA_Port/FCoE_LEP pairs;
- initiates FIP Clear Virtual Link requests as needed to terminate Virtual Links to other VA_Ports;
- transmits periodic FIP Discovery Advertisements to the All-FCF-MACs address every FKA_ADV_PERIOD; and
- monitors the status of remote VA_Ports by maintaining timers and verifying that periodic FIP Discovery Advertisements are received within every FKA_ADV_PERIOD.

The FCoE_LEP is the functional entity performing the encapsulation of FC frames into FCoE frames during transmission and the decapsulation of FCoE frames into FC frames during reception. When encapsulating FC frames into FCoE frames,

the MAC address of the local link end-point shall be used as the source address and the MAC address of the remote link end-point shall be used as the destination address of the generated FCoE frame. When decapsulating FC frames from FCoE frames, the FCoE_LEP shall verify that the destination address of the received FCoE frame is equal to the MAC address of the local link end-point and shall verify that the source address of the received FCoE frame is equal to the MAC address of the remote link end-point. If either check fails the FCoE frame shall be discarded.

For a VA_Port capable FDF-MAC, the MAC address of the local link end-point is the FDF-MAC address and the MAC address of the remote link end-point is the MAC address of the remote FDF-MAC or FCF-MAC with which a FIP ELP Exchange has been successfully completed.

A VA_Port is an instance of the FC-2V sublevel of Fibre Channel that is dynamically instantiated together with its FCoE_LEP on successful completion of a FIP ELP Exchange, according to the rules specified in 7.11.4.4. A VA_Port receives FC frames from the FCDF Switching Element and sends them to its FCoE_LEP for encapsulation and transmission over the Lossless Ethernet network. In a similar way, a VA_Port sends FC frames received from its FCoE_LEP to the FCDF Switching Element. A VA_Port is uniquely identified by an A_Port_Name Name_Identifier and is addressed by the A_Port Controller N_Port_ID (i.e., FFFF9h).

7.8 FCoE Virtual Links

Figure 42 shows how the model defined in 7.5 models VE_Port to VE_Port Virtual Links.

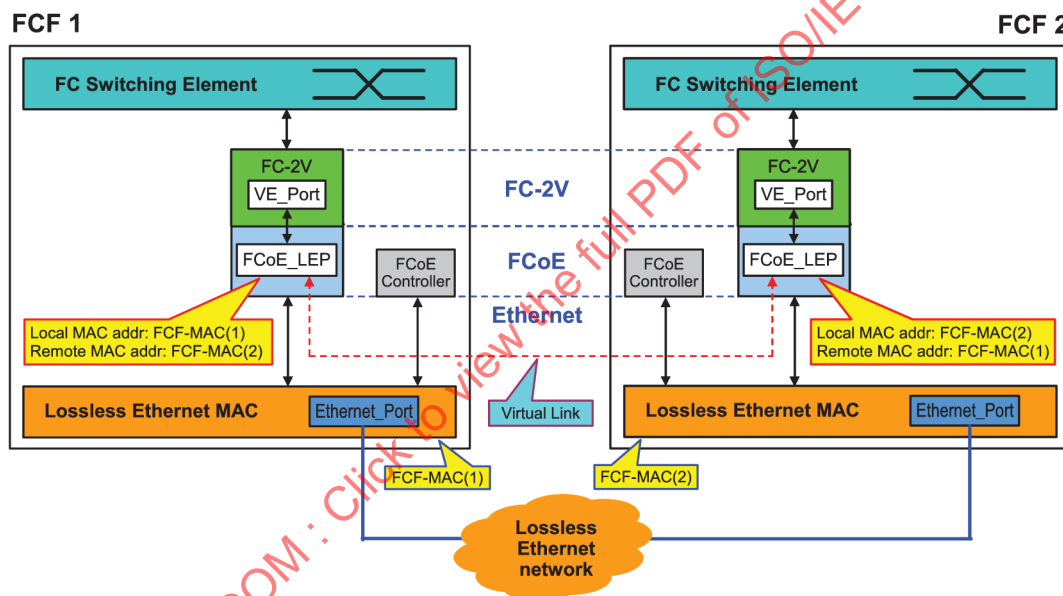


Figure 42 – VE_Port to VE_Port Virtual Links example

On successful completion of a FIP ELP Exchange, the FCoE Controllers of the two involved VE_Port capable FCF-MACs instantiate a VE_Port/FCoE_LEP pair. Figure 42 shows the Virtual Link end-points, that are the MAC addresses of the two involved VE_Port capable FCF-MACs (i.e., FCF-MAC(1) and FCF-MAC(2)).

Figure 43 shows how the models defined in 7.3 and 7.5 model VN_Port to VF_Port Virtual Links.

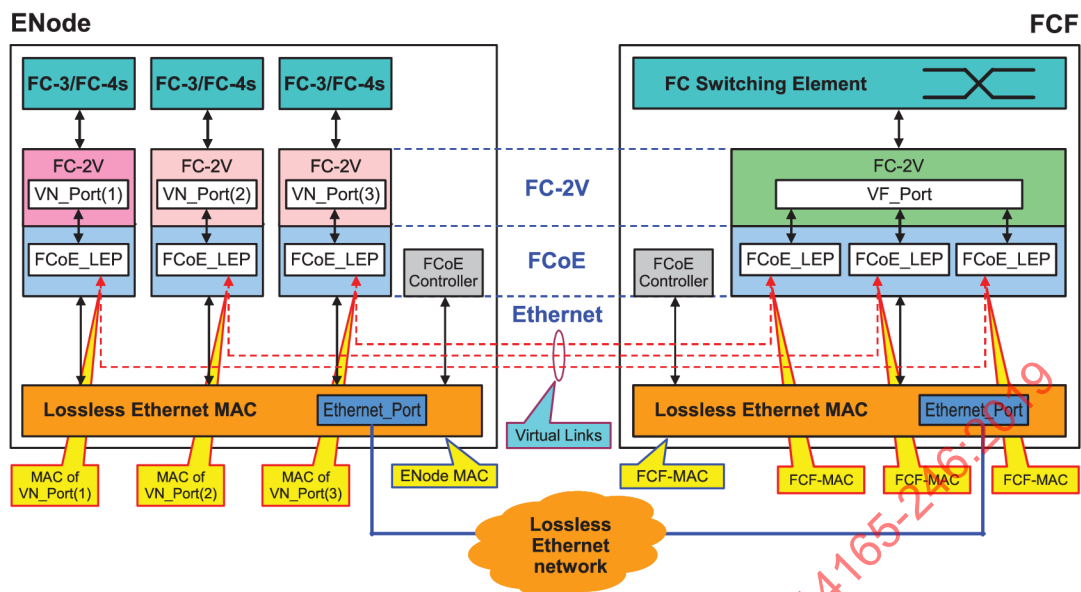


Figure 43 – VN_Port to VF_Port Virtual Links example

On successful completion of a FIP FLOGI Exchange, the FCoE Controller for an ENode MAC instantiates a VN_Port/FCoE_LEP pair (i.e., VN_Port(1) in Figure 43) and the FCoE Controller of a VF_Port capable FCF-MAC instantiates a VF_Port/FCoE_LEP pair.

On successful completion of a FIP NPIV FDISC Exchange, the FCoE Controller for an ENode MAC instantiates a VN_Port/FCoE_LEP pair (i.e., VN_Port(2) in Figure 43) and the FCoE Controller of a VF_Port capable FCF-MAC instantiates an additional FCoE_LEP to the instantiated VF_Port.

On successful completion of an additional FIP NPIV FDISC Exchange, the FCoE Controller of an ENode MAC instantiates a VN_Port/FCoE_LEP pair (i.e., VN_Port(3) in Figure 43) and the FCoE Controller of a VF_Port capable FCF-MAC instantiates an additional FCoE_LEP to the instantiated VF_Port.

Figure 43 shows the Virtual Link end-points, that are the MAC addresses used by the VN_Ports (i.e., MAC of VN_Port(1), MAC of VN_Port(2), and MAC of VN_Port(3)), and the FCF-MAC address.

Figure 44 shows how the functional model specified in 7.4 models VN_Port to VN_Port Virtual Links for the multi-node case shown in Figure 33.

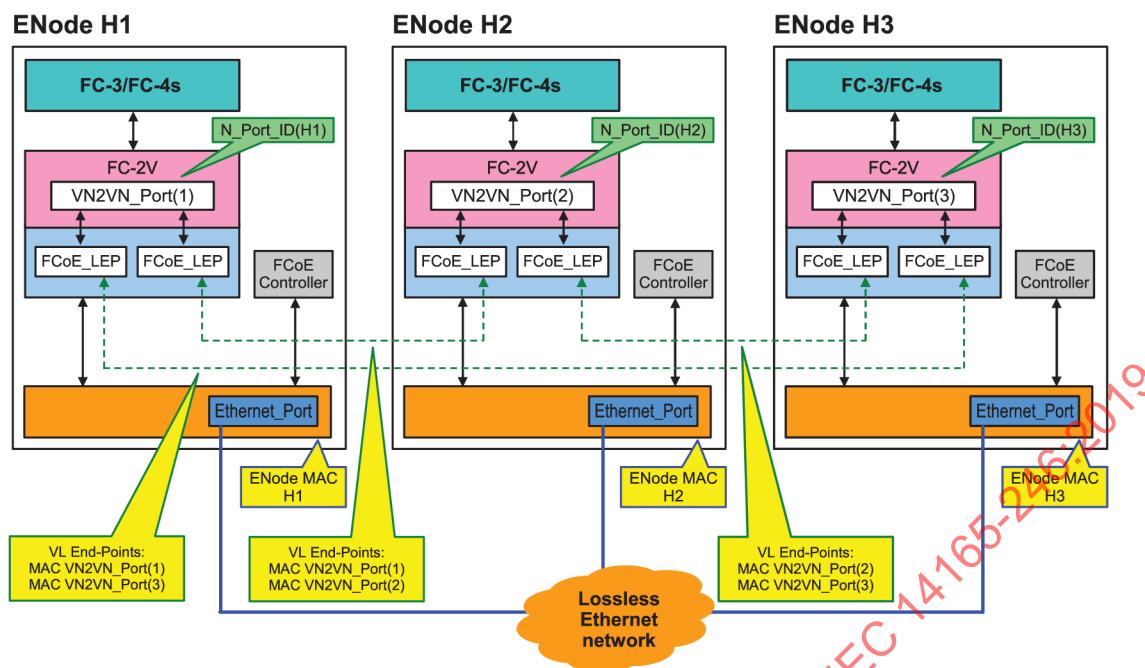


Figure 44 – VN_Port to VN_Port Virtual Links multi-node example

VN_Port to VN_Port Virtual Links are instantiated on successful completion of FIP FLOGI Exchanges (see 7.11.4.3) for a multi-node configuration. VN_Port to VN_Port Virtual Links are identified by the VN_Port MAC addresses associated with the involved VN2VN_Ports.

Figure 45 shows how the functional models specified in 7.4 and 7.5 model VN_Port to VN_Port and VN_Port to VF_Port Virtual Links for the mixed configuration case shown in Figure 34.

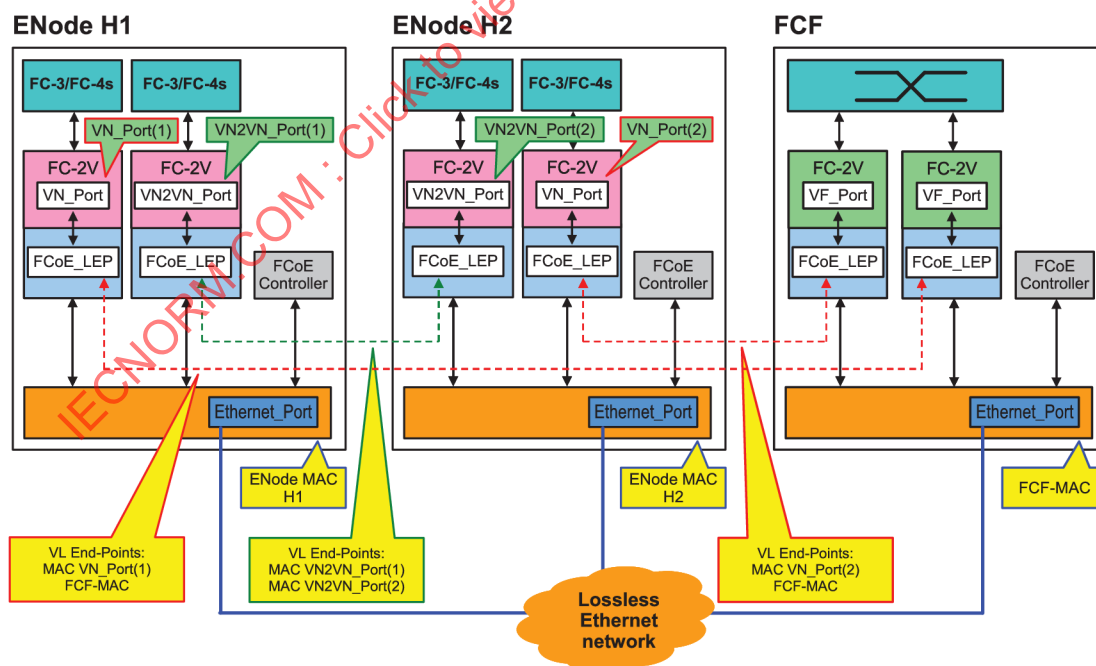


Figure 45 – VN_Port to VN_Port and VN_Port to VF_Port Virtual Links example

VN_Port to VN_Port Virtual Links are instantiated on successful completion of FIP FLOGI Exchanges (see 7.11.4.3) for a mixed configuration. VN_Port to VN_Port Virtual Links are identified by the VN_Port MAC addresses associated with the two involved VN2VN_Ports.

On successful completion of a FIP FLOGI Exchange, the FCoE Controller for an ENode MAC instantiates a VN_Port/FCoE_LEP pair (i.e., VN_Port(1) and VN_Port(2) in Figure 45) and the FCoE Controller of a VF_Port capable FCF-MAC instantiates a VF_Port/FCoE_LEP pair.

Figure 46 shows how the functional model specified in 7.4 models VN_Port to VN_Port Virtual Links for the point-to-point case shown in Figure 35.

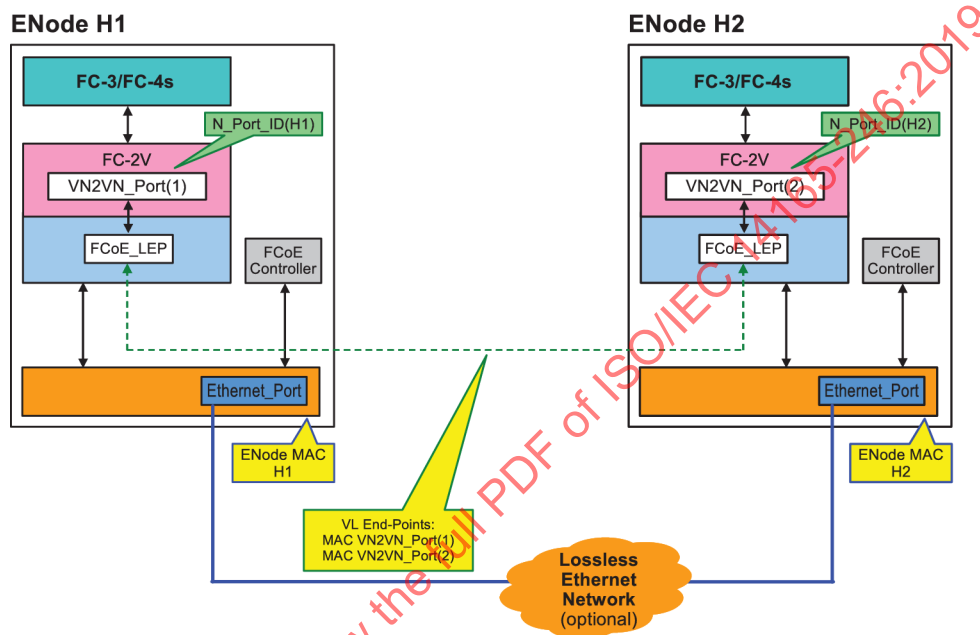


Figure 46 – VN_Port to VN_Port Virtual Link point-to-point example

VN_Port to VN_Port Virtual Links are instantiated on successful completion of FIP FLOGI Exchanges (see 7.11.4.3) for a point-to-point topology (see FC-LS-3). VN_Port to VN_Port Virtual Links are identified by the two VN_Port MAC addresses associated with the involved VN2VN_Ports.

Figure 47 shows how the functional models defined in 7.6 and 7.7 model a VA_Port to VA_Port Virtual Link between two FDFs.

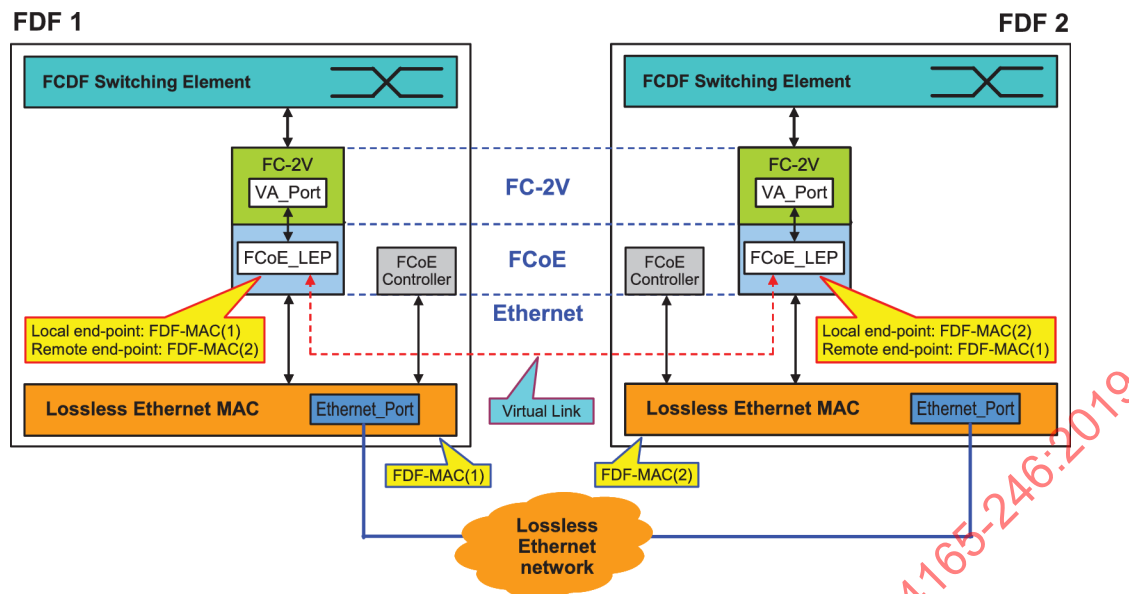


Figure 47 – VA_Port to VA_Port Virtual Link example

On successful completion of a FIP ELP Exchange, the FCoE Controllers of the two involved VA_Port capable FDF-MACs instantiate a VA_Port/FCoE_LEP pair. Figure 47 shows the Virtual Link end-points, that are the MAC addresses of the two involved VA_Port capable FDF-MACs (i.e., FDF-MAC(1) and FDF-MAC(2)).

7.9 VN_Port MAC addresses

For VN_Port to VF_Port Virtual Links, ENodes and FCFs shall use Fabric Provided MAC addresses (i.e., FPMAs) as VN_Port MAC addresses. For VN_Port to VN_Port Virtual Links, VN2VN ENodes shall use as VN_Port MAC address the address determined by concatenating the Locally Unique N_Port_ID of a VN2VN_Port to the constant VN2VN-FC-MAP (see Table 53). The FIP protocol is used to negotiate the VN_Port MAC addresses that are used between two ENodes (see 7.11.4.3) or between an ENode and an FCF (see 7.11.4.1).

FPMAs are assigned by FCFs while assigning an N_Port_ID to a VN_Port (see 7.11.4.1). A properly formed FPMA is one in which the 24 most significant bits equal the Fabric's FC-MAP value and the least significant 24 bits equal the N_Port_ID assigned to the VN_Port by the FCF. This guarantees that FPMAs are unique within the Fabric. The FC-MAP value is checked by the FIP discovery protocol (see 7.11.3) to ensure it is consistent across the Fabric. FPMAs should not be used for other protocols.

If the FC-MAP value is not administratively configured, then the FC-MAP value shall be set to DEFAULT_FC-MAP (see Table 53). If the FC-MAP value is administratively configured, then the FC-MAP value shall be in the range 0EFCC0h to 0EFCFFh, inclusive. The VN2VN-FC-MAP value shall not be used by FCFs as an FC-MAP value.

7.10 FCoE frame format

An FCoE frame is an Ethernet frame (see IEEE 802.3-2012) containing an FCoE PDU. FCoE frames shall be formatted in accordance with IEEE 802.3-2012 and the MAC Client Data field within the Ethernet frame shall contain an FCoE PDU (see Table 22). The use of an IEEE 802.1Q tag header is optional and additional IEEE 802.1 defined tags may be present in an FCoE frame. See annex B for examples of FCoE frames.

The format of an FCoE PDU is specified in Table 22.

Table 22 – FCoE PDU format

Bit Word	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0				
0	Type = FCoE_TYPE																Version				Reserved															
1	Reserved																																			
2	Reserved																																			
3	Reserved																								SOF											
4	(MSB)																																			
n+3	Encapsulated FC Frame (n words)																								(LSB)											
n+4	EOF								Reserved																											

The Type field in the Ethernet header shall be set to FCoE_TYPE (see Table 53). The Type field in the Ethernet header is not part of the FCoE PDU.

The Version field shall be set to FCoE_FRAME_VER (see Table 53).

The SOF field specifies the SOF delimiter for the encapsulated FC frame. The value of the SOF field shall be set as specified in Table 23.

Table 23 – FCoE SOF field

Value	Description	Reference
28h	SOFf	FC-FS-4
2Dh	SOFi2	FC-FS-4
35h	SOFn2	FC-FS-4
2Eh	SOFi3	FC-FS-4
36h	SOFn3	FC-FS-4

The Encapsulated FC Frame field shall contain:

- FC Extended Header(s) (see FC-FS-4), if any;
- the FC Frame_Header (see FC-FS-4);
- the FC Data_Field (see FC-FS-4); and
- the FC CRC (see FC-FS-4).

The EOF field specifies the EOF delimiter for the encapsulated FC frame. The value of the EOF field shall be set as specified in Table 24.

Table 24 – FCoE EOF field

Value	Description	Reference
41h	EOFn	FC-FS-4
42h	EOFt	FC-FS-4
49h	EOFni	FC-FS-4
50h	EOFa	FC-FS-4

7.11 FC-BB_E device initialization

7.11.1 FCoE Initialization Protocol (FIP) overview

The FCoE Initialization Protocol (FIP) is used to perform the functions of FC-BB_E device discovery, initialization, and maintenance. To perform these functions, encapsulated FIP operations (see 7.11.7.2) are specified.

The FIP Ethernet Type (see 7.11.7.1) is different than the FCoE Ethernet Type (see 7.10) to enable the distinction of discovery, initialization, and maintenance traffic from other FCoE traffic.

FIP frames are used to perform the following protocols:

- a) FIP VLAN discovery (see 7.11.2);
- b) FIP discovery (see 7.11.3);
- c) FCoE Virtual Link instantiation (see 7.11.4);
- d) FCoE Virtual Link maintenance (see 7.11.5); and
- e) Locally Unique N_Port_IDs (see 7.11.6).

FIP protocols shall be performed on a per-VLAN basis. It is recommended to use the FIP VLAN discovery protocol on the default VLAN. All other FIP protocols shall be performed in the VLANs selected for FC-BB_E operations.

If a Lossless Ethernet MAC is operating in VLAN unaware mode, this standard uses the term default VLAN to refer to either untagged frames or to the use of VLAN identifier zero (see IEEE 802.1Q-2011). If a Lossless Ethernet MAC is operating in VLAN aware mode, this standard uses the term default VLAN to refer to the IEEE 802.1Q-2011 port VLAN.

In order to provide FC-BB_E services on a VLAN, FCoE and FIP protocols, other than FIP VLAN discovery, shall both be performed on that VLAN.

Multiple Fabrics on one VLAN may produce unpredictable results (i.e., the provisions of this standard are not sufficient to ensure that FCoE frames remain associated with the correct Fabric if multiple Fabrics are used on the same VLAN).

On ENodes, the ENode MAC address shall be used as the source MAC address for all FIP frames, except the VN_Port FIP Keep Alive frame (see 7.11.8.5) and N_Port_ID Beacons (see 7.11.8.15). On FCFs, the FCF-MAC address shall be used as the source MAC address for all FIP frames. On FDFs, the FDF-MAC address shall be used as the source MAC address for all FIP frames.

ENode MACs shall listen to the All-ENode-MACs group address and, if the Locally Unique N_Port_ID protocol is supported, also to the All-VN2VN-ENode-MACs and All-PT2PT-ENode-MACs group addresses. FCF-MACs and FDF-

MACs shall listen to the All-FCF-MACs group address. ENode MACs, FCF-MACs, and FDF-MACs shall listen to the All-FCoE-MACs group address.

7.11.2 FIP VLAN discovery protocol

7.11.2.1 Overview

While becoming operational, an ENode MAC, FCF-MAC, VN2VN ENode MACs, or FDF-MACs may invoke the FIP VLAN discovery protocol to discover the VLANs in the Lossless Ethernet network over which FC-BB_E operations are supported.

The FIP VLAN discovery protocol is not needed if VLANs where FC-BB_E is used are already known or if VLANs are not used.

7.11.2.2 ENode/FCF VLAN discovery

An ENode MAC may send a FIP VLAN Request frame to the All-FCF-MACs MAC address over an available VLAN (e.g., the default VLAN). VF_Port capable FCF-MACs that receive a FIP VLAN Request frame shall respond with a unicast FIP VLAN Notification frame over the same VLAN. The FIP VLAN Notification frame should provide the list of VLAN IDs over which the originating FCF supports FC-BB_E operations. The ENode MAC that received a FIP VLAN Notification frame may enable one or more of these VLANs for subsequent operations. VF_Port capable FCF-MACs may limit the number of VLAN IDs listed in a FIP VLAN Notification frame on a per-requester basis.

Figure 48 informatively shows how an ENode MAC may determine the VLANs to use for FCoE.

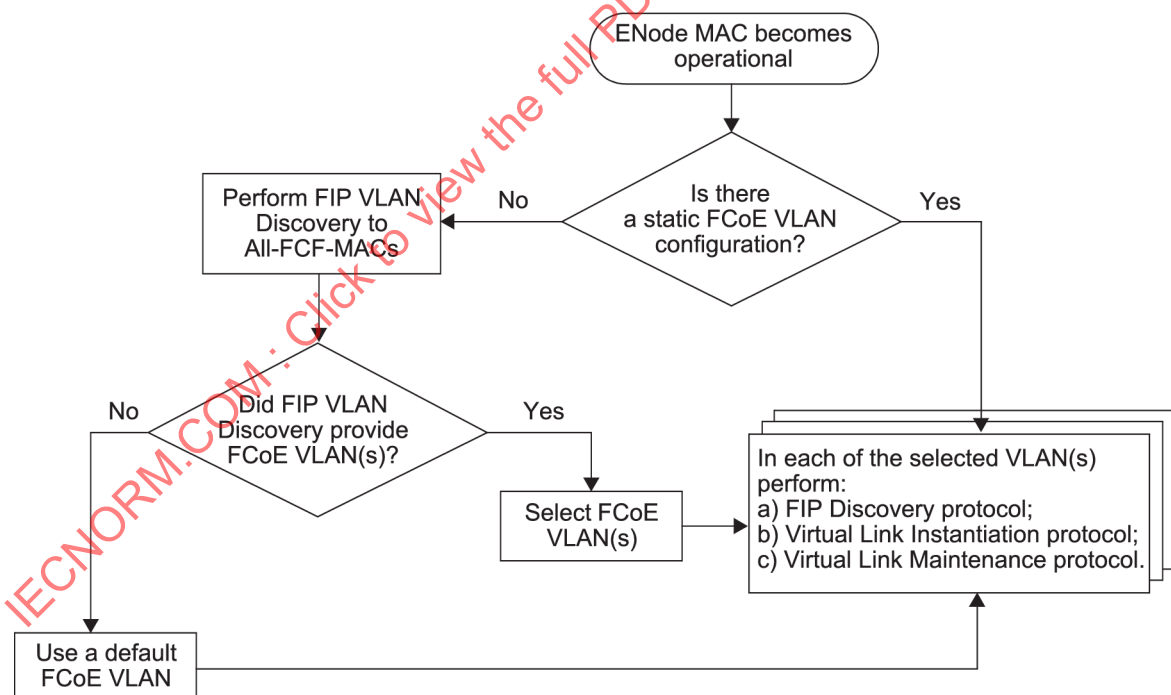


Figure 48 – VLAN discovery processing example in an ENode MAC (informative)

A VF_Port capable FCF-MAC shall discard a multicast FIP VLAN Request frame that has a source address equal to its FCF-MAC address, and the FIP VLAN Request frame should be reported in a vendor specific manner as an indication of a MAC address duplication.

VF_Port capable FCF-MACs shall listen to the All-FCF-MACs group address in the default VLAN and in other VLANs that ENodes may use to invoke this protocol.

If the configuration of VLANs on which a VF_Port capable FCF-MAC supports FC-BB_E operations changes, then the VF_Port capable FCF-MAC should send an unsolicited unicast FIP VLAN Notification frame to each ENode MAC address with which that VF_Port capable FCF-MAC has established Virtual Links. The unicast FIP VLAN Notification frame shall carry the revised list of VLAN IDs over which the originating VF_Port capable FCF-MAC supports FC-BB_E operations.

Upon reception of an unsolicited FIP VLAN Notification, the ENode MAC:

- a) may enable FC-BB_E operations for one or more of the received VLAN IDs currently not enabled; and
- b) may disable FC-BB_E operations for one or more of the VLAN IDs currently enabled and not listed in the received FIP VLAN Notification once all Virtual Links on those VLAN(s) have been de-instantiated.

Additional VLANs may support FC-BB_E operations if additional FCFs are connected to the network to which an ENode MAC is connected. In order to discover these additional VLANs, an ENode MAC may send a FIP VLAN Request frame to the All-FCF-MACs MAC address on a periodic basis.

NOTE 18 – Given that the availability of additional VLANs is due to physical network configuration changes, a recommended period value for the transmission of FIP VLAN Requests is 20 seconds.

7.11.2.3 FCF/FCF VLAN discovery

A VE_Port capable FCF-MAC may send a FIP VLAN Request frame to the MAC address All-FCF-MACs over an available VLAN (e.g., the default VLAN). VE_Port capable FCF-MACs that receive a FIP VLAN Request frame shall respond with a unicast FIP VLAN Notification frame over the same VLAN. The FIP VLAN Notification frame carries the list of VLAN IDs over which the originating FCF supports FC-BB_E operations. The VE_Port capable FCF-MAC that received a FIP VLAN Notification frame may enable one or more of these VLANs for subsequent operations.

A VE_Port capable FCF-MAC shall discard a multicast VLAN Request frame that has a source address equal to its FCF-MAC address. Such a VLAN Request frame should be reported in a vendor specific manner as an indication of a MAC address duplication.

VE_Port capable FCF-MACs shall listen to the All-FCF-MACs group address in the default VLAN and in other VLANs that FCFs may use to invoke this protocol.

If the configuration of VLANs on which a VE_Port capable FCF-MAC supports FC-BB_E operations changes, then the VE_Port capable FCF-MAC should send an unsolicited unicast FIP VLAN Notification frame to each FCF-MAC address with which that VE_Port capable FCF-MAC has established Virtual Links. The unicast FIP VLAN Notification frame shall specify the revised list of VLAN IDs over which the originating VE_Port capable FCF-MAC supports FC-BB_E operations.

Upon reception of an unsolicited FIP VLAN Notification, the FCF:

- a) may enable FC-BB_E operations for one or more of the received VLAN IDs currently not enabled; and
- b) may disable FC-BB_E operations for one or more of the VLAN IDs currently enabled and not listed in the received FIP VLAN Notification once all Virtual Links on those VLAN(s) have been de-instantiated.

Additional VLANs may support FC-BB_E operations if additional FCFs are connected to the network to which a VE_Port capable FCF-MAC is connected. In order to discover these additional VLANs, a VE_Port capable FCF-MAC may send a FIP VLAN Request frame to the All-FCF-MACs MAC address on a periodic basis.

NOTE 19 – Given that the availability of additional VLANs is due to physical network configuration changes, a recommended period value for the transmission of FIP VLAN Requests is 20 seconds.

7.11.2.4 VN2VN ENode VLAN discovery

A VN2VN ENode may discover VLANs that support fabric FC-BB_E operations as specified in 7.11.2.2.

The network administrator may also configure a subset of the VN2VN ENodes to provide VLAN information to the other VN2VN ENodes. A VN2VN ENode MAC may send a FIP VLAN Request frame to the All-VN2VN-ENode-MACs MAC address over an available VLAN (e.g., the port VLAN). A VN2VN ENode configured to provide VLAN information to the other VN2VN ENodes shall respond with a unicast FIP VN2VN VLAN Notification frame over the same VLAN. The FIP VN2VN VLAN Notification frame should provide the list of VLAN IDs over which the Locally Unique N_Port_ID protocol may be used. A VN2VN ENode MAC that receives a FIP VN2VN VLAN Notification frame may enable one or more of the provided VLANs for subsequent operations. VN2VN ENodes configured to provide VLAN information to the other VN2VN ENodes may limit the number of VLAN IDs listed in a FIP VN2VN VLAN Notification frame on a per-requester basis.

Figure 49 informatively shows how a VN2VN ENode MAC may determine the VLANs to use for FCoE.

NOTE: an implementation decides which path(s) to take when multiple choices are provided in this diagram

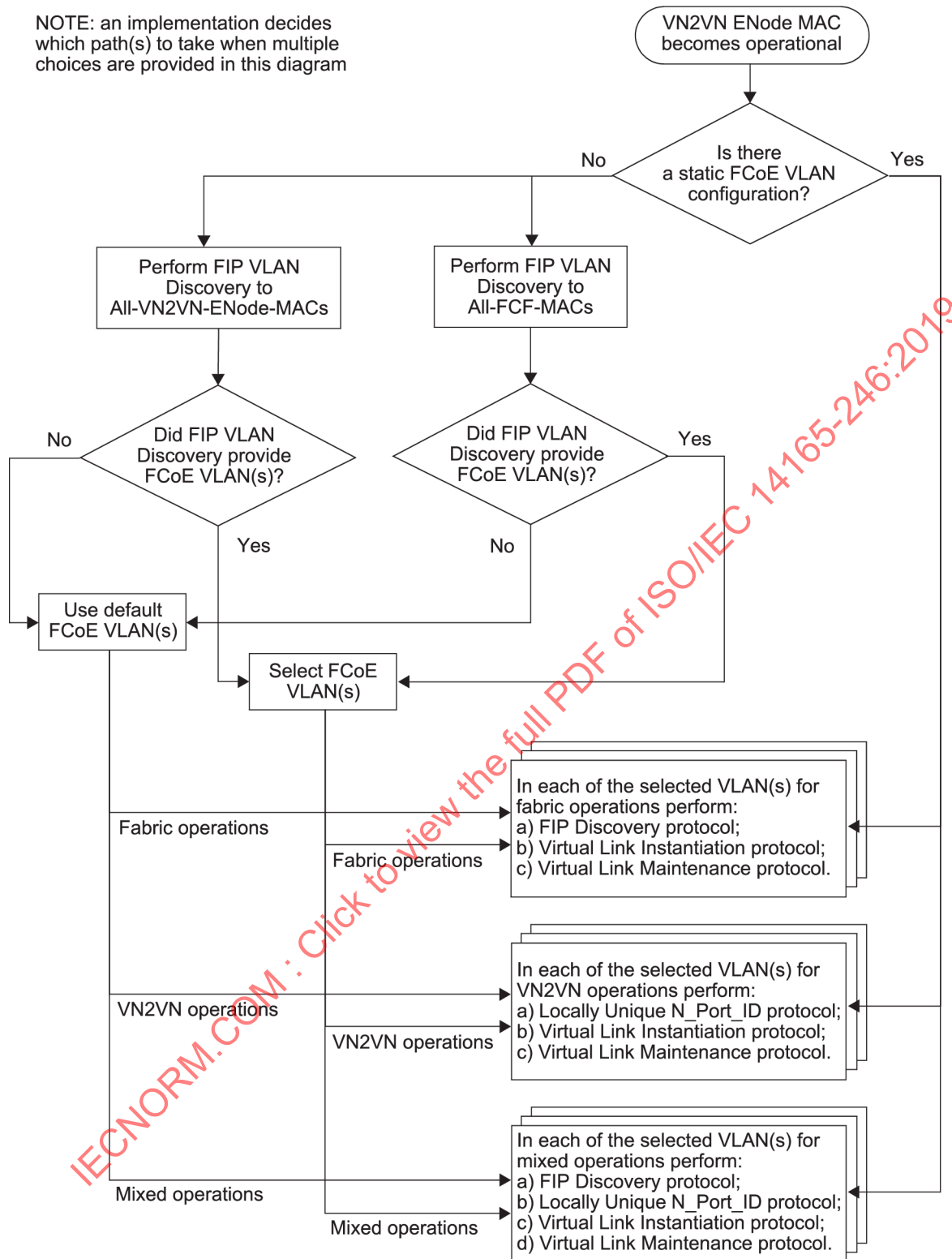


Figure 49 – VLAN discovery processing in a VN2VN ENode MAC

A VN2VN ENode configured to provide VLAN information to the other VN2VN ENodes shall discard a multicast FIP VLAN Request frame that has a source address equal to its MAC address, and the FIP VLAN Request frame should be reported in a vendor specific manner as an indication of a MAC address duplication.

If the configuration of VLANs on a VN2VN ENode configured to provide VLAN information to the other VN2VN ENodes changes, then the VN2VN ENode should send an unsolicited unicast FIP VLAN Notification frame to each

ENode MAC address with which that VN2VN ENode has established VN_Port to VN_Port Virtual Links. The unicast FIP VLAN Notification frame shall carry the revised list of VLAN IDs over which the Locally Unique N_Port_ID protocol may be used.

Upon reception of an unsolicited FIP VN2VN VLAN Notification, the VN2VN ENode MAC:

- a) may enable FC-BB_E operations for one or more of the received VLAN IDs currently not enabled; and
- b) may disable FC-BB_E operations for one or more of the VLAN IDs currently enabled and not listed in the received FIP VLAN Notification once all Virtual Links on those VLAN(s) have been de-instantiated.

Additional VLANs may be used for the Locally Unique N_Port_ID protocol if additional VN2VN ENodes configured to provide VLAN information to the other VN2VN ENodes are connected to the network to which a VN2VN ENode MAC is connected. In order to discover these additional VLANs, a VN2VN ENode MAC may send a FIP VLAN Request frame to the All-VN2VN-ENode-MACs MAC address on a periodic basis.

NOTE 20 – Given that the availability of additional VLANs is due to physical network configuration changes, a recommended period value for the transmission of FIP VLAN Requests is 20 seconds.

7.11.2.5 ENode/FDF VLAN discovery

ENode/FDF VLAN Discovery is performed as ENode/FCF VLAN Discovery (see 7.11.2.2), with VF_Port capable FDF-MACs operating as VF_Port capable FCF-MACs.

7.11.2.6 FDF/Controlling FCF VLAN discovery

FDF/Controlling FCF VLAN Discovery is performed as FCF/FCF VLAN Discovery (see 7.11.2.3), with VA_Port capable FDF-MACs operating as VE_Port capable FCF-MACs and VA_Port/VE_Port capable FCF-MACs operating as VE_Port capable FCF-MACs.

7.11.3 FIP discovery protocol

7.11.3.1 Overview

On a network deploying one or more VLANs, the FIP discovery protocol is performed in the VLANs where FC-BB_E services are offered if these VLANs are known (e.g., upon performing the FIP VLAN discovery protocol (see 7.11.2)).

7.11.3.2 ENode/FCF discovery

An ENode MAC does not distinguish between FCF-MACs and FDF-MACs (i.e., it processes them in the same manner as FCF-MACs).

The FCoE Controller of a VF_Port capable FCF-MAC or VF_Port capable FDF-MAC shall periodically transmit multicast Discovery Advertisements (see 7.11.8.3) to the All-ENode-MACs group address every FKA_ADV_PERIOD. The FKA_ADV_PERIOD period shall be randomized by adding a random delay uniformly distributed between 0 and 100 ms to avoid synchronized bursts of multicast traffic within the Ethernet network. The FCoE Controller of a VF_Port capable FCF-MAC should begin transmitting unsolicited multicast Discovery Advertisements on completion of Fabric configuration (see FC-SW-6) including FCF/FCF discovery (see 7.11.3.3). The FCoE Controller of a VF_Port capable FDF-MAC shall begin transmitting unsolicited multicast Discovery Advertisements upon enabling its ports for logins (see FC-SW-6).

The FCoE Controller of an ENode MAC shall create an entry for each discovered FCF-MAC or FDF_MAC in an internal FCF list.

NOTE 21 – The internal data structures used to describe this protocol are a model to express the behavior, not an implementation requirement.

Each entry in the FCF list has the following bits:

- a) 'Max FCoE Size Verified' - set to zero for entries created from unsolicited multicast Discovery Advertisements, set to one if a solicited unicast Discovery Advertisement is received; and
- b) 'Available for Login' - reflects the value of the Available for Login bit provided by the most recently received Discovery Advertisement from that VF_Port capable FCF-MAC or VF_Port capable FDF-MAC.

The FCoE Controller of an ENode MAC shall select for login a subset of the FCF-MACs in the FCF list having the 'Available for Login' bit set to one (i.e., the FCF Login Set) on the basis of a local policy that should default to selecting the one(s) with higher priority (i.e., lower priority value) in the absence of explicit configuration of another selection criteria. A FIP FLOGI may be performed with each FCF-MAC in the FCF Login Set having the 'Max FCoE Size Verified' flag set to one. In order to get the Max FCoE Size Verified bit set to one, so that a FIP FLOGI may subsequently be performed, the FCoE Controller of an ENode MAC shall transmit a unicast Discovery Solicitation (see 7.11.8.2) to that FCF-MAC address and receive a solicited unicast Discovery Advertisement in response.

The periodic reception of unsolicited multicast Discovery Advertisements allows the FCoE Controller of ENode MACs to continuously verify FCF-MAC and FDF-MAC connectivity. The Available for Login (A) bit in received Discovery Advertisements provides the information that the transmitting FCF-MAC or FDF-MAC is available for FIP FLOGI/FDISC, and this information is updated in the FCF list and FCF Login Set on reception of Discovery Advertisements. The Available for Login bit is informational and shall have no effect on existing logins.

Once the FCoE Controller of an ENode MAC becomes operational it should discover VF_Port capable FCF-MACs or VF_Port capable FDF-MACs with which it may perform FIP FLOGI by transmitting a multicast Discovery Solicitation to the All-FCF-MACs group address. In response to a Discovery Solicitation from an ENode MAC, a VF_Port capable FCF-MAC or VF_Port capable FDF-MAC shall transmit a solicited unicast Discovery Advertisement to the soliciting ENode MAC if it is configured to allow a FIP FLOGI from that ENode. The solicited unicast Discovery Advertisement shall be transmitted to the MAC address specified in the MAC address descriptor in the received Discovery Solicitation. The solicited unicast Discovery Advertisement shall be transmitted within ADV_TOV (see Table 53) upon reception of the Discovery Solicitation. Discovery Advertisements transmitted in response to a multicast Discovery Solicitation should be delayed by a random time uniformly distributed between 0 and 100 ms to avoid synchronized bursts of traffic within the Ethernet network. This delay should not be applied to solicited unicast Discovery Advertisements sent in response to unicast Discovery Solicitations. Solicited unicast Discovery Advertisements shall not be transmitted by an FCF until Fabric configuration (see FC-SW-6), including FCF/FCF discovery (see 7.11.3.3), is completed. Solicited unicast Discovery Advertisements shall not be transmitted by an FCF until the FDF enables its ports for logins (see FC-SW-6).

The comparison of information in solicited Discovery Advertisements from the available VF_Port capable FCF-MACs or VF_Port capable FDF-MACs allows the detection of multiple Fabrics on a VLAN, and waiting ADV_TOV assures that all solicited Discovery Advertisements are received for comparison. For this reason it is recommended that an ENode MAC after transmitting the Discovery Solicitation waits for ADV_TOV before performing the first FLOGI, in order to collect the Discovery Advertisements from the available VF_Port capable FCF-MACs or VF_Port capable FDF-MACs. In managed network configurations where multiple Fabrics per VLAN do not occur, this wait is not needed.

NOTE 22 – An ENode MAC may also wait to receive unsolicited multicast Discovery Advertisements and then send unicast Discovery Solicitations to the FCF-MACs selected for login from the FCF Login set. The comparison of information in solicited Discovery Advertisements from the available VF_Port capable FCF-MACs or VF_Port capable FDF-MACs allows the detection of multiple Fabrics on a VLAN, and waiting FKA_ADV_PERIOD assures that all solicited Discovery Advertisements are received for comparison. For this reason, if an ENode MAC waits to receive unsolicited multicast Discovery Advertisements, then it is recommended that the ENode MAC waits for FKA_ADV_PERIOD before performing the first FIP FLOGI, in order to collect the Discovery Advertisements from the available VF_Port capable FCF-MACs or VF_Port capable FDF-MACs. In managed network configurations where multiple Fabrics per VLAN do not occur, this wait is not needed.

A Discovery Solicitation shall carry in the Max FCoE Size descriptor the maximum FCoE PDU size the ENode MAC intends to use for FCoE traffic (see 7.11.7.3.7). The FIP PDU (see Table 25) in a solicited unicast Discovery Advertisement shall be extended to have a length that matches the Max_FCoE_Size field value in the Max FCoE Size descriptor in the Discovery Solicitation that the Discovery Advertisement is responding to (see 7.11.8.3).

NOTE 23 – If an ENode transmits an FCoE frame with an FCoE PDU size that is greater than its maximum FCoE PDU size, the network may not deliver the FCoE frame.

An ENode MAC may generate multiple Discovery Solicitations.

NOTE 24 – As an example, an ENode MAC that does not receive a solicited unicast Discovery Advertisement in response to a Discovery Solicitation may transmit additional Discovery Solicitations, unicast or multicast.

Reception of a solicited unicast Discovery Advertisement from an FCF-MAC or FDF-MAC shall set the 'Max FCoE Size Verified' bit to one in the entry for that FCF-MAC or FDF-MAC in the FCF Login Set of an ENode MAC.

It is possible for an FCF or FDF to receive a multicast Discovery Solicitation from the same ENode MAC on multiple FCF-MACs or FDF-MACs. In this case, a separate solicited unicast Discovery Advertisement shall be transmitted by each of the FCF-MACs or FDF-MACs that received the Discovery Solicitation. The ENode MAC that transmitted the multicast Discovery Solicitation is able to determine that it received multiple solicited unicast Discovery Advertisements from the same FCF or FDF since the value of the Name_Identifier field in the Name_Identifier descriptor is the same in each of the solicited unicast Discovery Advertisements (see 7.11.8.3). In this case the ENode MAC should select the FCF-MAC for Fabric login with that FCF or FDF based on the value of the Priority descriptor in the Discovery Advertisements.

It is possible for an ENode MAC to receive multiple unsolicited multicast Discovery Advertisements from multiple FCF-MACs of the same FCF or from multiple FDF-MACs of the same FDF. The ENode MAC is able to determine that those unsolicited multicast Discovery Advertisements are from the same FCF or FDF since the value of the Name_Identifier field in the Name_Identifier descriptor is the same in each of the unsolicited multicast Discovery Advertisements (see 7.11.8.3). In this case the ENode MAC should select the FCF-MAC for Fabric login with that FCF or FDF based on the value of the Priority descriptor in the Discovery Advertisements.

An ENode MAC shall discard any received Discovery Solicitation. A VF_Port capable FCF-MAC or VF_Port capable FDF-MAC shall discard any Discovery Solicitation originated by a VE_Port capable FCF-MAC (i.e., having the FCF bit set to one (see 7.11.7.2)).

An ENode MAC shall discard an unsolicited multicast Discovery Advertisement that has a source address equal to its ENode MAC address. Such a Discovery Advertisement should be reported in a vendor specific manner as an indication of a MAC address duplication.

A VF_Port capable FCF-MAC shall discard a multicast Discovery Solicitation that has a source address equal to its FCF-MAC address. Such a Discovery Solicitation should be reported in a vendor specific manner as an indication of a MAC address duplication.

A VF_Port capable FDF-MAC shall discard a multicast Discovery Solicitation that has a source address equal to its FDF-MAC address. Such a Discovery Solicitation should be reported in a vendor specific manner as an indication of a MAC address duplication.

Reception of Discovery Advertisements for more than one Fabric on the same VLAN should be reported by an ENode MAC in a vendor specific manner and no subsequent VN_Port to VF_Port Virtual Links shall be instantiated.

7.11.3.3 FCF/FCF discovery

The FCoE Controller of a VE_Port capable FCF-MAC shall periodically transmit multicast Discovery Advertisements (see 7.11.8.3) to the All-FCF-MACs group address every FKA_ADV_PERIOD. The FKA_ADV_PERIOD period shall be randomized by adding a random delay uniformly distributed between 0 and 100 ms to avoid synchronized bursts of multicast traffic within the Ethernet network.

On receiving Discovery Advertisements, the FCoE Controller of a VE_Port capable FCF-MAC shall create an entry per FCF-MAC in an internal FCF list.

NOTE 25 – The internal data structures used to describe this protocol are a model to express the behavior, not an implementation requirement.

Each entry in the FCF list has the following bits:

- a) 'Max FCoE Size Verified' - set to zero for entries created from unsolicited multicast Discovery Advertisements, set to one if a solicited unicast Discovery Advertisement is received; and
- b) 'Available for ELP' - reflects the value of the Available for Login bit provided by the most recently received Discovery Advertisement from that VE_Port capable FCF-MAC.

A FIP ELP shall be performed with each FCF-MAC in the FCF list having the 'Max FCoE Size Verified' bit set to one. In order to get the Max FCoE Size Verified bit set to one, so that a FIP ELP may subsequently be performed, the FCoE Controller of a VE_Port capable FCF-MAC shall transmit a unicast Discovery Solicitation (see 7.11.8.2) to that FCF-MAC address and receive a solicited unicast Discovery Advertisement in response.

The periodic reception of unsolicited multicast Discovery Advertisements allow the FCoE Controller of VE_Port capable FCF-MACs to continuously verify the FCF-MACs connectivity. The Available for Login (A) bit in received Discovery Advertisements provides the information that the transmitting FCF-MAC is available for FIP ELP, and this information is updated in the FCF list on reception of Advertisements. The Available for Login bit is informational and shall have no effect on existing VE_Port to VE_Port Virtual Links.

Once the FCoE Controller for a VE_Port capable FCF-MAC becomes operational it should discover other VE_Port capable FCF-MACs by transmitting a multicast Discovery Solicitation to the All-FCF-MACs group address. In response to a Discovery Solicitation from an FCF-MAC, a VE_Port capable FCF-MAC shall transmit a solicited unicast Discovery Advertisement to the soliciting FCF-MAC if the FC-MAP value in the Discovery Solicitation is compatible with the FC-MAP configured on the FCF and if it is configured to allow a Virtual Link with that FCF.

The solicited unicast Discovery Advertisement shall be transmitted to the MAC address specified in the MAC address descriptor in the received Discovery Solicitation. The solicited unicast Discovery Advertisement shall be transmitted within ADV_TOV (see Table 53) upon reception of the Discovery Solicitation. Discovery Advertisements transmitted in response to a multicast Discovery Solicitation should be delayed by a random time uniformly distributed between 0 and 100 ms to avoid synchronized bursts of traffic within the Ethernet network. This delay should not be applied to solicited unicast Discovery Advertisements sent in response to unicast Discovery Solicitations.

NOTE 26 – A VE_Port capable FCF-MAC may also wait to receive unsolicited multicast Discovery Advertisements and then send unicast Discovery Solicitations to the FCF-MACs in the FCF list.

A Discovery Solicitation shall specify in the Max FCoE Size descriptor (see 7.11.7.3.7) the maximum FCoE PDU size the VE_Port capable FCF-MAC intends to use for FCoE traffic. The FIP PDU (see Table 25) in a solicited unicast Discovery Advertisement shall be extended to have a length that matches the Max_FCoE_Size field value in the Max FCoE Size descriptor in the Discovery Solicitation that the Discovery Advertisement is responding to (see 7.11.8.3).

NOTE 27 – If a VE_Port capable FCF-MAC transmits an FCoE frame with an FCoE PDU size that is greater than its maximum FCoE PDU size, the network may not deliver the FCoE frame.

A VE_Port capable FCF-MAC may generate multiple Discovery Solicitations.

NOTE 28 – As an example, a VE_Port capable FCF-MAC that does not receive a solicited unicast Discovery Advertisement in response to a Discovery Solicitation may transmit additional Discovery Solicitations.

Reception of a solicited unicast Discovery Advertisement from an FCF-MAC shall set the 'Max FCoE Size Verified' bit to one in the entry for that FCF-MAC in the FCF list of the receiving VE_Port capable FCF-MAC.

It is possible for an FCF to receive multicast Discovery Solicitations from the same VE_Port capable FCF-MAC on multiple FCF-MACs. In this case, a separate solicited unicast Discovery Advertisement shall be transmitted by each of the FCF-MACs that received the Discovery Solicitation. The VE_Port capable FCF-MAC that transmitted the multicast Discovery Solicitation is able to determine that it received multiple solicited unicast Discovery Advertisements from the same FCF since the value of the Name_Identifier field in the Name_Identifier descriptor is the same in each of the solicited unicast Discovery Advertisements (see 7.11.8.3).

It is possible for a VE_Port capable FCF-MAC to receive multiple unsolicited multicast Discovery Advertisements from multiple FCF-MACs of the same FCF. The VE_Port capable FCF-MAC is able to determine that those unsolicited multicast Discovery Advertisements are from the same FCF since the value of the Name_Identifier field in the Name_Identifier descriptor is the same in each of the unsolicited multicast Discovery Advertisements (see 7.11.8.3).

After receiving a Discovery Solicitation originated by an FCF (i.e., the FCF bit is set to one), an FCF-MAC shall perform the following verification checks:

- a) the Name_Identifier field value in the Discovery Solicitation is different than the Switch_Name of the recipient FCF (see 7.11.8.2.2); and
- b) the Fabric Provided bit is set to one (see 7.11.7.2) and the FC-MAP value in the FC-MAP descriptor in the Discovery Solicitation is the same as the FC-MAP value of the recipient FCF.

If any verification check is false, then the Discovery Solicitation shall be discarded.

After receiving a Discovery Advertisement, an FCF-MAC shall perform the following verification checks:

- a) the Name_Identifier field value in the Discovery Advertisement is different than the Switch_Name of the recipient FCF (see 7.11.8.3); and
- b) the Fabric Provided bit is set to one (see 7.11.7.2) and the FC-MAP value in the Fabric descriptor in the Discovery Advertisement is the same as the FC-MAP value of the recipient FCF.

If any verification check is false, then the Discovery Advertisement shall be discarded.

NOTE 29 – It is possible for an FCF to receive a multicast Discovery Solicitation or a multicast Discovery Advertisement that it originated because FIP frames sent to the All-FCF-MACs group address may be forwarded to other ports on the same FCF by intermediate Ethernet bridges.

A VE_Port capable FCF-MAC shall discard any Discovery Solicitation originated by an ENode (i.e., having the FCF bit set to zero (see 7.11.7.2)).

A VE_Port capable FCF-MAC shall discard a multicast Discovery Solicitation that has a source address equal to its FCF-MAC address. Such a Discovery Solicitation should be reported in a vendor specific manner as an indication of a MAC address duplication.

Reception of Discovery Advertisements for more than one Fabric on the same VLAN should be reported by VE_Port capable FCF-MAC in a vendor specific manner.

7.11.3.4 FDF/Controlling FCF discovery

Controlling FCFs and FDFs behave as FCFs from a point of view of FIP discovery, therefore they discover each other through the FIP discovery protocol, as specified for FCF to FCF discovery (see 7.11.3.3). The Controlling FCF (C) bit and the FDF (D) bit are specified in the encapsulated FIP operation (see 7.11.7.2) to indicate if the originator of a FIP frame is a Controlling FCF or an FDF.

If an FDF is not part of the Distributed FCF internal topology (see FC-SW-6), then:

- a) all VA_Port capable FDF-MACs on that FDF shall transmit Discovery Advertisements with the Fabric_Name field of the Fabric descriptor set to zero; and
- b) all VF_Port capable FDF-MACs on that FDF shall not transmit Discovery Advertisements.

If an FDF is part of the Distributed FCF internal topology (see FC-SW-6), then all VA_Port capable and VF_Port capable FDF-MACs on that FDF shall transmit Discovery Advertisements having the Distributed FCF's Fabric_Name in the Fabric_Name field of the Fabric descriptor.

7.11.4 FCoE Virtual Link instantiation protocol

7.11.4.1 VN_Port to VF_Port Virtual Links

The FCoE Controller of an ENode MAC instantiates VN_Port to VF_Port Virtual Links on successful completion of a FIP Fabric login request. Fabric login (i.e., FLOGI, NPIV FDISC) shall be performed using FIP frames (see Table 25) and the associated FIP descriptor type (see Table 30). Fabric login (i.e., FLOGI, NPIV FDISC) shall not be performed using FCoE frames.

In addition to providing Fabric login, the FIP Fabric login provides a method to assign a MAC address for the VN_Port (see 7.11.8.4.2).

If the FCoE Controller of an ENode MAC transmits a FIP FLOGI Request or FIP NPIV FDISC Request, then it shall indicate that it intends to use FPMAs as VN_Port MAC addresses (i.e., set the Fabric Provided bit to one and the Server Provided bit to zero). The MAC address contained in the MAC address descriptor of the FIP FLOGI LS_ACC or FIP NPIV FDISC LS_ACC that is returned by the FCF or FDF shall be used as the VN_Port MAC address (see 7.9).

The MAC address descriptor contained in the FIP FLOGI LS_ACC or FIP NPIV FDISC LS_ACC that is returned by the FCF or FDF shall contain a properly formatted FPMA MAC address (see 7.9).

An ENode shall verify that a returned FPMA address is properly formed. If the FPMA verification fails, the ENode should log this event in a vendor specific manner and should perform an implicit Fabric Logout.

Explicit VN_Port to VF_Port Virtual Link de-instantiation is performed by an ENode MAC by performing Fabric logout. Fabric logout (i.e., Fabric LOGO) shall be performed by an ENode using FIP frames (see Table 25) and the associated FIP descriptor type (see Table 30). Fabric logout shall not be performed using FCoE frames.

In addition to providing Fabric logout, the FIP Fabric logout provides a method to de-assign a MAC address for the VN_Port (see 7.11.8.4.3).

7.11.4.2 VE_Port to VE_Port Virtual Links

The FCoE Controller of a VE_Port capable FCF-MAC instantiates VE_Port to VE_Port Virtual Links on successful completion of a FIP ELP request. ELP shall be performed using FIP frames (see Table 25) and the associated FIP descriptor type (see Table 30). ELP shall not be performed using FCoE frames.

A VA_Port/VE_Port capable FCF-MAC instantiates VE_Port to VE_Port Virtual Links with other FCF-MACs through a FIP ELP Exchange. VE_Port to VE_Port Virtual Links are instantiated if the FDF/FCDF bit is set to zero in both FIP ELP Request and FIP ELP SW_ACC.

A VA_Port/VE_Port capable FCF-MAC shall instantiate VE_Port to VE_Port Virtual Links with other VA_Port/VE_Port capable FCF-MACs and VE_Port capable FCF-MACs discovered by FIP discovery on the Lossless Ethernet network only after its Controlling FCF has established its role (see FC-SW-6).

In addition to providing ELP, the FIP ELP provides a method (i.e., the MAC address descriptor) to communicate the MAC address for the VE_Port (see 7.11.8.4.4).

7.11.4.3 VN_Port to VN_Port Virtual Links

The FCoE Controller of a VN2VN ENode MAC, operating in either multi-node mode or point-to-point mode, instantiates VN_Port to VN_Port Virtual Links on successful completion of a FIP FLOGI from either of the VN2VN ENode MACs. Both FIP FLOGI Request and LS_ACC shall have the Locally Unique N_Port_ID (see 7.11.6) of the originating VN2VN_Port as S_ID, the Locally Unique N_Port_ID of the destination VN2VN_Port as D_ID for the point-to-point FLOGI protocol, and the originating VN_Port MAC address in the MAC address descriptor. The MAC addresses of the FIP FLOGI Request and LS_ACC shall be the ENode MAC addresses of the involved VN2VN_Ports.

As specified in FC-LS-3, the VN2VN_Port with the greater N_Port_Name proceeds to N_Port Login, with the PLOGI ELSs encapsulated in FCoE. The FCoE PLOGI Request and LS_ACC shall have the Locally Unique N_Port_ID of the originating VN2VN_Port as S_ID and the Locally Unique N_Port_ID of the destination VN2VN_Port as D_ID. Upon completion of FCoE PLOGI the two involved VN_Ports operate as in the Fibre Channel point-to-point topology (see FC-LS-3).

A FIP FLOGI Request in a point-to-point topology coming from a VN2VN_Port not listed in the VN2VN Neighbor Set (see 7.11.6.2.2 and 7.11.6.3.1) shall be rejected with reason code 'FIP Error' (i.e., 20h) and reason code explanation 'VN2VN_Port Not in Neighbor Set' (i.e., 62h, see FC-LS-3).

Explicit VN_Port to VN_Port Virtual Link de-instantiation is accomplished by a VN2VN ENode MAC by transmitting a FIP Fabric LOGO, that de-instantiates the FCoE_LEPs and performs a N_Port logout. The S_ID and D_ID on the encapsulated LOGO ELS shall be set to the Locally Unique N_Port_IDs of the involved VN2VN_Ports. The MAC address field in the MAC address descriptor shall be set to the originating VN_Port MAC address. The MAC addresses of the FIP Fabric LOGO Request and LS_ACC shall be the ENode MAC addresses of the involved VN2VN_Ports.

7.11.4.4 VA_Port to VA_Port Virtual Links

Controlling FCFs and FDFs establish FCoE Virtual Links between themselves on successful completion of FIP ELP Exchanges. Two bits in the ELP payload, the Controlling FCF/Switch bit and the FDF/FCDF bit (see FC-SW-6), indicate if the originator of the FIP ELP Request or SW_ACC is a Controlling FCF or an FDF.

If a FIP ELP request is received with the Controlling FCF/Switch bit set to one and the FDF/FCDF bit set to one, then the FIP ELP request shall be rejected.

If a FIP ELP SW_ACC is received with the Controlling FCF/Switch bit set to one and the FDF/FCDF bit set to one, then a FIP Clear Virtual Link shall be transmitted to de-instantiate the Virtual Link, and its reception should be reported in a vendor specific manner.

A VA_Port/VE_Port capable FCF-MAC instantiates VA_Port to VA_Port Virtual Links with VA_Port capable FDF-MACs through a FIP ELP Exchange. These VA_Port to VA_Port Virtual Links are instantiated if the FDF/FCDF bit is set to one in the FIP ELP SW_ACC. A VA_Port/VE_Port capable FCF-MAC shall reject a received FIP ELP Request with the FDF/FCDF bit set to one with Reason Code 'Protocol Error' and Reason Code Explanation 'Invalid Request'.

A VA_Port/VE_Port capable FCF-MAC shall only instantiate VA_Port to VA_Port Virtual Links with VA_Port capable FDF-MACs that belong to FDFs that are part of the Distributed FCF's FDF_Set (see FC-SW-6) and have been discovered by FIP discovery on the Lossless Ethernet network. In this case, the FIP ELP Exchange shall be initiated by the VA_Port/VE_Port capable FCF-MAC and the VA_Port capable FDF-MAC shall process it irrespective of the value of the Switch_Name field in the ELP payload (i.e., acceptance or rejection shall be based on the other ELP parameters, not on the involved Switch_Names). A VA_Port/VE_Port capable FCF-MAC shall not establish VA_Port to VA_Port Virtual Links with VA_Port capable FDF-MACs belonging to FDFs that are not part of the Distributed FCF's FDF_Set. A VA_Port capable FDF-MAC shall not initiate a FIP ELP Exchange with a FCF-MAC belonging to a Controlling FCF.

NOTE 30 – FIP Discovery Advertisements provide the FDF Switch_Name that is checked against the FDF_Set.

A VA_Port capable FDF-MAC instantiates VA_Port to VA_Port Virtual Links with other FDF-MACs through a FIP ELP Exchange. These VA_Port to VA_Port Virtual Links shall only be instantiated if the FDF/FCDF bit is set to one in both FIP ELP Request and FIP ELP SW_ACC.

A VA_Port capable FDF-MAC shall initiate a FIP ELP Exchange with a discovered VA_Port capable FDF-MAC only if its FDF is part of the Distributed FCF internal topology (see FC-SW-6) and the discovered FDF-MAC belongs to an FDF in the Distributed FCF's FDF_Set.

A VA_Port capable FDF-MAC that has not initiated a FIP ELP Exchange shall reply to a received FIP ELP Request, irrespective of the value of the Switch_Name field in the ELP payload. A VA_Port capable FDF-MAC that has initiated a FIP ELP Exchange (i.e., sent a FIP ELP Request) shall reply to a received FIP ELP Request according to the normal ELP rules (i.e., acceptance or rejection includes considering the involved Switch_Names) (see FC-SW-6).

NOTE 31 – These rules enable an ordered establishment of VA_Port to VA_Port Virtual Links from the Controlling FCF(s) to the peripheral FDFs in a Distributed FCF with cascaded FDFs.

7.11.5 FCoE Virtual Link maintenance protocol

7.11.5.1 Virtual Link maintenance protocol overview

VN_Port to VF_Port Virtual Links (see Figure 31), VE_Port to VE_Port Virtual Links (see Figure 32), VN_Port to VN_Port Virtual Links (see Figure 33), and VA_Port to VA_Port Virtual Links (see Figure 36) overlay over a Lossless Ethernet network.

The Virtual Link maintenance protocol specifies how to deal with faults that may occur in a Lossless Ethernet network.

7.11.5.2 VN_Port to VF_Port Virtual Link maintenance protocol

To deal with local physical layer faults, an ENode MAC shall de-instantiate all its VN_Ports to VF_Port Virtual Links upon detecting that its physical layer is not operational. This condition shall be handled as an implicit Fabric logout (see FC-LS-3) for the involved VN_Ports. A VF_Port capable FCF-MAC or VF_Port capable FDF-MAC shall de-instantiate all its VF_Ports upon detecting that its physical layer is not operational.

To deal with non-local faults, the FCoE Controllers of an ENode MAC and of a VF_Port capable FCF-MAC or VF_Port capable FDF-MAC shall continuously verify the state of the VN_Port to VF_Port Virtual Link by transmitting appropriate FIP frames and by verifying received FIP frames. This behavior may be disabled by VF_Port capable FCF-MACs or VF_Port capable FDF-MACs under administrative control by setting the Disable Virtual Link Maintenance bit to one in the FKA_ADV_Period descriptor in Discovery Advertisements (see 7.11.7.3.13). The Disable Virtual Link Maintenance bit in the FKA_ADV_Period descriptor shall be set to one only in a direct-attach topology (i.e., if an ENode is directly connected to an FCF or FDF without any intermediate Ethernet bridges).

The FCoE Controller of an ENode MAC shall transmit a unicast FIP Keep Alive frame on behalf of the ENode MAC (i.e., with the ENode MAC address as source MAC address and without a Vx_Port Identification descriptor (see 7.11.7.3.12) in the FIP Descriptor list (see Table 51) to each VF_Port capable FCF-MAC or VF_Port capable FDF-MAC with which it has VN_Ports logged in. This ENode FIP Keep Alive frame shall be transmitted every FKA_ADV_PERIOD. The FKA_ADV_PERIOD is obtained from the Discovery Advertisements received from the VF_Port capable FCF-MACs or VF_Port capable FDF-MACs with which the ENode MAC has VN_Ports logged in.

In addition, the FCoE Controller of an ENode MAC shall transmit a unicast FIP Keep Alive frame on behalf of each VN_Port (i.e., with the VN_Port MAC address as source MAC address and containing a Vx_Port Identification descriptor for that VN_Port in the FIP Descriptor list, see Table 51) to the VF_Port capable FCF-MAC or VF_Port capable FDF-MAC with which that VN_Port is logged in. This VN_Port FIP Keep Alive frame shall be transmitted every FKA_VN_PERIOD.

The FCoE Controller of an ENode MAC shall monitor the status of a VF_Port with which it has VN_Ports logged in by verifying reception of unsolicited multicast Discovery Advertisements from that VF_Port capable FCF-MAC or VF_Port capable FDF-MAC. Unsolicited multicast Discovery Advertisements are expected to be received every FKA_ADV_PERIOD. If unsolicited multicast Discovery Advertisements are not received within $2.5 * FKA_ADV_PERIOD$, then all the VN_Port to VF_Port Virtual Links with that VF_Port shall be implicitly de-instantiated and the FCF-MAC or FDF-MAC associated with that VF_Port shall be removed from the FCF Login Set (see 7.11.3.2). This condition should be counted as a Virtual Link failure and shall be handled as an implicit Fabric logout (see FC-LS-3) for the involved VN_Ports. A subsequent FIP Fabric Login may be performed with an FCF-MAC or FDF-MAC in the current FCF Login Set as specified in 7.11.3.2.

The FCoE Controller of a VF_Port capable FCF-MAC or VF_Port capable FDF-MAC shall transmit an unsolicited multicast Discovery Advertisement to the All-ENode-MACs group address every FKA_ADV_PERIOD.

The FCoE Controller of a VF_Port capable FCF-MAC or VF_Port capable FDF-MAC shall monitor the status of an ENode MAC with which it has active VN_Port to VF_Port Virtual Links by verifying the reception of FIP Keep Alive frames from that ENode MAC and its VN_Ports. VN_Port FIP Keep Alive frames (i.e., those containing a Vx_Port

Identification descriptor) are expected to be received every FKA_VN_PERIOD and ENode FIP Keep Alive frames (i.e., those not containing a Vx_Port Identification descriptor) are expected to be received every FKA_ADV_PERIOD.

If VN_Port FIP Keep Alive frames are not received within $2.5 * \text{FKA_VN_PERIOD}$, then the associated VN_Port to VF_Port Virtual Link shall be explicitly de-instantiated (i.e., a FIP Clear Virtual Links frame listing the unreachable VN_Port shall be generated). This condition shall be handled as an implicit Fabric logout (see FC-LS-3) for the involved VN_Port. If ENode FIP Keep Alive frames are not received within $2.5 * \text{FKA_ADV_PERIOD}$, then all associated VN_Port to VF_Port Virtual Links shall be explicitly de-instantiated (i.e., a FIP Clear Virtual Links frame listing either all the unreachable VN_Ports or no VN_Ports shall be generated). This condition shall be handled as an implicit Fabric logout (see FC-LS-3) for the involved VN_Ports.

NOTE 32 – The use of a faster FKA_ADV_PERIOD rate for ENode FIP Keep Alive frames allows fast response times against loss of connectivity in the Ethernet realm with limited overhead. The use of VN_Port FIP Keep Alive frames transmitted at a slower FKA_VN_PERIOD rate allows the clearing of state associated with each individual VN_Port once that VN_Port becomes not operational.

Explicit VN_Port to VF_Port Virtual Link de-instantiation is invoked by a VF_Port capable FCF-MAC or VF_Port capable FDF-MAC by transmitting a FIP Clear Virtual Links frame (see 7.11.8.6). A FIP Clear Virtual Links frame provides a list of zero or more VN_Ports to be de-instantiated. If a FIP Clear Virtual Links frame provides a list of one or more VN_Ports, then an ENode MAC shall de-instantiate the listed VN_Ports upon reception of the FIP frame. If a FIP Clear Virtual Links frame provides zero VN_Ports, then an ENode MAC shall de-instantiate all VN_Ports logged in with the originating FCF-MAC or FDF-MAC upon reception of the FIP frame. This condition shall be handled as an implicit Fabric logout (see FC-LS-3) for the involved VN_Ports.

The size of a FIP Clear Virtual Links frame shall not exceed the standard Ethernet MAC Client Data size (i.e., 1 500 bytes for basic frames and 1 504 bytes for Q-tagged frames, see IEEE 802.3-2012). If the list of VN_Ports to be removed does not fit in a single FIP frame, then multiple FIP frames should be transmitted to convey the entire list.

On receiving a VN_Port FIP Keep Alive frame coming from a VN_Port that is not logged in, the FCoE Controller of a VF_Port capable FCF-MAC or VF_Port capable FDF-MAC shall transmit a FIP Clear Virtual Links frame listing that VN_Port.

On receiving an ENode FIP Keep Alive frame coming from an ENode MAC that is not logged in, the FCoE Controller of a VF_Port capable FCF-MAC or VF_Port capable FDF-MAC shall transmit a FIP Clear Virtual Links frame listing no VN_Ports. A FIP Clear Virtual Links frame listing no VN_Ports shall be handled by an ENode MAC by de-instantiating all VN_Port to VF_Port Virtual Links with that VF_Port capable FCF-MAC or VF_Port capable FDF-MAC. This condition shall be handled as an implicit Fabric logout (see FC-LS-3) for the involved VN_Ports.

An event that causes a VF_Port to perform implicit logout of a VN_Port should cause explicit VN_Port to VF_Port Virtual Link de-instantiation. If the event causes implicit logout of more than one VN_Port, then the FIP Clear Virtual Links frame shall list all affected VN_Ports. If the event that caused implicit logout was reception of a FIP FLOGI request, then the FIP Clear Virtual Links frame shall not be sent.

FIP Clear Virtual Links frames may be generated by FCFs or FDFs whenever appropriate to speed-up fault recovery.

NOTE 33 – As an example, in certain topologies an FCF or FDF may generate a FIP Clear Virtual Links frame to de-instantiate the VN_Port to VF_Port Virtual Links affected by a local physical layer fault on other ports upon detection of that fault.

The FKA_ADV_PERIOD value (see Table 53) may be changed on a FCF or FDF under administrative control. If this happens, each VF_Port capable FCF-MAC or VF_Port capable FDF-MAC of the FCF or FDF shall advertise the updated FKA_ADV_PERIOD in subsequent unsolicited multicast Discovery Advertisements.

If the FKA_ADV_PERIOD value is decreased, then a VF_Port capable FCF-MAC or VF_Port capable FDF-MAC shall transmit unsolicited multicast Discovery Advertisements at the interval specified by the updated value, but shall not use the updated value for detection of missing ENode FIP Keep Alives until five times the old value has elapsed since the transmission of the first updated unsolicited multicast Discovery Advertisement.

If the FKA_ADV_PERIOD value is increased, then a VF_Port capable FCF-MAC or VF_Port capable FDF-MAC shall transmit unsolicited multicast Discovery Advertisements at the interval specified by the old value until five times

the updated value has elapsed since the transmission of the first updated unsolicited multicast Discovery Advertisement, but shall use the updated value for detection of missing ENode FIP Keep Alives.

On detecting the updated value, an ENode having VN_Port to VF_Port Virtual Links instantiated with that FCF or FDF shall transmit ENode FIP Keep Alive frames at the interval specified by the updated FKA_ADV_PERIOD value and shall use the updated value for detection of missing unsolicited multicast Discovery Advertisements.

7.11.5.3 VE_Port to VE_Port Virtual Link maintenance protocol

To deal with local physical layer faults, a VE_Port capable FCF-MAC shall de-instantiate all its VE_Port to VE_Port Virtual Links upon detecting that its physical layer is not operational.

To deal with non-local faults, the FCoE Controllers for VE_Port capable FCF-MACs shall continuously verify the state of a VE_Port to VE_Port Virtual Link by transmitting unsolicited multicast Discovery Advertisements and by verifying received unsolicited multicast Discovery Advertisements.

The FCoE Controller for a VE_Port capable FCF-MAC shall transmit a multicast Discovery Advertisement to the All-FCF-MACs group address every FKA_ADV_PERIOD.

The FCoE Controller for a VE_Port capable FCF-MAC shall monitor the status of a VE_Port to VE_Port Virtual Link by verifying the reception of unsolicited multicast Discovery Advertisements. Unsolicited multicast Discovery Advertisements are expected to be received every FKA_ADV_PERIOD. If unsolicited multicast Discovery Advertisements are not received within $2.5 * FKA_ADV_PERIOD$, then the VE_Port to VE_Port Virtual Link associated with that FCF-MAC shall be explicitly de-instantiated along with the associated VE_Port. That FCF-MAC shall be removed from the FCF List (see 7.11.3.3).

Explicit VE_Port to VE_Port Virtual Link de-instantiation is performed by transmitting a FIP Clear Virtual Links frame from a VE_Port capable FCF-MAC. A FIP Clear Virtual Links frame transmitted to a VE_Port capable FCF-MAC provides the destination FCF-MAC address in the Vx_Port Identification descriptor. A VE_Port capable FCF-MAC shall de-instantiate the VE_Port listed in a FIP Clear Virtual Links frame upon reception of the FIP frame.

The FKA_ADV_PERIOD value (see Table 53) may be changed on a FCF under administrative control. If this happens, each VE_Port capable FCF-MAC of the FCF shall advertise the updated FKA_ADV_PERIOD in subsequent unsolicited multicast Discovery Advertisements and shall transmit them at the interval specified by the updated value.

On detecting the updated value in received unsolicited multicast Discovery Advertisements, a VE_Port capable FCF-MAC shall use the updated value for detection of missing unsolicited multicast Discovery Advertisements.

7.11.5.4 VN_Port to VN_Port Virtual Link and VN2VN Neighbor Set maintenance protocol

To deal with local physical layer faults, a VN2VN ENode MAC shall de-instantiate all its VN_Port to VN_Port Virtual Links and clear its VN2VN Neighbor Set upon detecting that its physical layer is not operational.

To deal with non-local faults, the FCoE Controller of a VN2VN ENode MAC operating in multi-node mode shall continuously verify the state of the VN_Port to VN_Port Virtual Links by verifying received N_Port_ID Beacons per each VN2VN_Port in the VN2VN Login Set (see 7.11.6.2.2). The FCoE Controller of a VN2VN ENode MAC operating in point-to-point mode shall continuously verify the state of the VN_Port to VN_Port Virtual Link by verifying received N_Port_ID P2P Beacons from the VN2VN_Port in the VN2VN Login Set.

For each VN2VN_Port in the VN2VN Neighbor Set, N_Port_ID Beacons and N_Port_ID P2P Beacons are expected to be received every BEACON_PERIOD. If N_Port_ID Beacons or N_Port_ID P2P Beacons from a VN2VN_Port are not received within $2.5 * BEACON_PERIOD$, beginning once the VN2VN_Port is added to the VN2VN Neighbor Set, then that VN2VN_Port shall be removed from the VN2VN Neighbor Set and from the VN2VN Login Set and VN_Port to VN_Port Virtual Links with that remote VN2VN_Port, if any, shall be implicitly de-instantiated.

Receiving a N_Port_ID Probe Request or a N_Port_ID Claim Notification from a VN2VN ENode with which a VN_Port to VN_Port Virtual Link is instantiated may be used by a VN2VN ENode as an indication that the remote VN2VN_Port implicitly logged out.

7.11.5.5 VA_Port to VA_Port Virtual Link maintenance protocol

VA_Port to VA_Port Virtual Link maintenance is performed as for VE_Port to VE_Port Virtual Links, with VA_Port capable FDF-MACs operating as VE_Port capable FCF-MACs and VA_Port/VE_Port capable FCF-MACs operating as VE_Port capable FCF-MACs (see 7.11.5.3).

In particular, the FCoE Controller for a VA_Port capable FDF-MAC or of a VA_Port/VE_Port capable FCF-MAC shall monitor the status of a VA_Port to VA_Port Virtual Link by verifying the reception of unsolicited multicast Discovery Advertisements. Unsolicited multicast Discovery Advertisements are expected to be received every FKA_ADV_PERIOD. If unsolicited multicast Discovery Advertisements are not received within $2.5 \times \text{FKA_ADV_PERIOD}$, then the VA_Port to VA_Port Virtual Link associated with that FCF-MAC or FDF-MAC shall be explicitly de-instantiated along with the associated VA_Port.

7.11.6 Locally Unique N_Port_IDs

7.11.6.1 Overview

The Locally Unique N_Port_IDs protocol enables the establishment of VN_Port to VN_Port Virtual Links across a Lossless Ethernet network. The Locally Unique N_Port_ID protocol supports both multi-node (see 7.11.6.2) and point-to-point operations (see 7.11.6.3). A VN2VN ENode shall operate in either multi-node mode or point-to-point mode based on its configuration.

Two multicast MAC addresses are used by the protocol:

- a) All-VN2VN-ENode-MACs, used while a VN2VN ENode MAC operates in multi-node mode; and
- b) All-PT2PT-ENode-MACs, used while a VN2VN ENode MAC operates in point-to-point mode.

After becoming operational, a VN2VN ENode MAC shall select a tentative Locally Unique N_Port_ID in the range 000001h to 00FFFEh. A tentative Locally Unique N_Port_ID should be a recorded Locally Unique N_Port_ID if available (see 7.11.6.4), otherwise a tentative Locally Unique N_Port_ID should be generated using a pseudo-random number generator with a uniform distribution in the range 000001h to 00FFFEh. The N_Port_Name of the VN2VN_Port for which a Locally Unique N_Port_ID is being selected should be used as one of the parameters to seed the pseudo-random number generator at its first invocation. In this way, different ENode MACs generate different numbers and a VN2VN_Port is usually associated with the same N_Port_ID each time it is enabled. The method to generate tentative Locally Unique N_Port_IDs shall be chosen so that different VN2VN ENode MACs are not likely to generate the same sequence of numbers (see annex Annex F:).

For the Locally Unique N_Port_IDs protocol, VN2VN ENodes shall use:

- a) a default value of 10 seconds for R_A_TOV and a default value of 2 seconds for E_D_TOV if operating in multi-node mode; or
- b) the default values specified in FC-LS-3 if operating in point-to-point mode.

7.11.6.2 Multi-node operation

7.11.6.2.1 Probing a Locally Unique N_Port_ID

After selecting a tentative Locally Unique N_Port_ID, a VN2VN ENode MAC operating in multi-node mode shall probe the Lossless Ethernet network to which it is connected to determine if the Locally Unique N_Port_ID is already in use. The VN2VN ENode MAC shall wait for a random time interval selected uniformly in the range zero to PROBE_WAIT, and shall then transmit two multicast N_Port_ID Probe Requests (see 7.11.8.11) to All-VN2VN-ENode-MACs, spaced PROBE_WAIT apart. If the selected tentative Locally Unique N_Port_ID is a recorded one (see 7.11.6.4), then the REC/P2P bit of the N_Port_ID Probe Requests shall be set to one, otherwise it shall be set to zero.

If during the period from the transmission of the first N_Port_ID Probe Request until ANNOUNCE_WAIT after the last N_Port_ID Probe Request is sent the VN2VN ENode MAC receives any N_Port_ID Probe Reply (see 7.11.8.12), N_Port_ID Claim Notification (see 7.11.8.13) claiming the selected tentative Locally Unique N_Port_ID, or N_Port_ID Beacon (see 7.11.8.15) announcing the selected tentative Locally Unique N_Port_ID, then the VN2VN ENode MAC shall select a new random tentative Locally Unique N_Port_ID and repeat the process (i.e., wait for a random time interval and then transmit two N_Port_ID Probe Requests).

If during the same period the VN2VN ENode MAC receives any N_Port_ID Probe Request probing the selected tentative Locally Unique N_Port_ID, then the VN2VN ENode MAC shall check the N_Port_Name carried in the Vx_Port Identification descriptor of the received N_Port_ID Probe Request and the value of the REC/P2P bit (see 7.11.7.2). If the REC/P2P bit is set to zero and the selected tentative Locally Unique N_Port_ID is a recorded one, then the VN2VN ENode MAC shall keep its selection and reply to the received N_Port_ID Probe Request with a N_Port_ID Probe Reply. If the REC/P2P bit is set to one and the selected tentative Locally Unique N_Port_ID is not a recorded one, then the VN2VN ENode MAC shall select a new random tentative Locally Unique N_Port_ID and repeat the process (i.e., wait for a random time interval and then transmit two N_Port_ID Probe Requests) without replying to the received N_Port_ID Probe Request. If the REC/P2P bit is set to one and the selected tentative Locally Unique N_Port_ID is a recorded one, or the REC/P2P bit is set to zero and the selected tentative Locally Unique N_Port_ID is not a recorded one, then the processing is based on the value of the received N_Port_Name. If the received N_Port_Name is greater than the N_Port_Name of its VN2VN_Port, then the VN2VN ENode MAC shall select a new random tentative Locally Unique N_Port_ID and repeat the process (i.e., wait for a random time interval and then transmit two N_Port_ID Probe Requests) without replying to the received N_Port_ID Probe Request, otherwise it shall keep its selection and reply to the received N_Port_ID Probe Request with a N_Port_ID Probe Reply.

N_Port_ID Probe Requests probing a different Locally Unique N_Port_ID, N_Port_ID Claim Notification claiming a different Locally Unique N_Port_ID, and N_Port_ID Beacon announcing a different Locally Unique N_Port_ID received during the same period shall be ignored.

A VN2VN ENode MAC should maintain a counter of the number of Locally Unique N_Port_ID conflicts it has experienced while trying to acquire a Locally Unique N_Port_ID. If the number of conflicts exceeds ten, then the VN2VN ENode MAC should report the situation in a vendor specific manner as an indication of potential network failure and shall limit the rate at which it probes for new Locally Unique N_Port_IDs to no more than one new N_Port_ID per RATE_LIMIT_INTERVAL. This is to prevent multicast storms in pathological failure cases, such as a rogue VN2VN ENode MAC that answers all N_Port_ID Probe Requests, causing legitimate VN2VN ENode MACs to go into an infinite loop while attempting to select a usable Locally Unique N_Port_ID.

NOTE 34 – An ENode MAC connecting to a network with 1 300 ENodes has a 98% chance of selecting an unused Locally Unique N_Port_ID on the first try and a 99.96% chance of selecting an unused Locally Unique N_Port_ID within two tries. The probability that it will have to try more than ten times is about 10^{-17} .

At any time, upon receiving a N_Port_ID P2P Claim Notification (see 7.11.8.13) or a N_Port_ID P2P Beacon (see 7.11.8.15), a VN2VN ENode MAC operating in multi-node mode shall transmit a N_Port_ID Probe Request probing its N_Port_ID to All-VN2VN-ENode-MACs to indicate to the sending VN2VN ENode MAC operating in point-to-point mode that it is not in its expected network configuration.

7.11.6.2.2 Claiming a Locally Unique N_Port_ID

If by ANNOUNCE_WAIT after the transmission of the last N_Port_ID Probe Request no conflicting N_Port_ID Probe Reply, N_Port_ID Probe Request, N_Port_ID Claim Notification, or N_Port_ID Beacon has been received, then the VN2VN ENode MAC shall claim the selected tentative Locally Unique N_Port_ID by transmitting a multicast N_Port_ID Claim Notification to All-VN2VN-ENode-MACs. An N_Port_ID Claim Notification notifies the other VN2VN ENode MACs on the Lossless Ethernet network of the selected Locally Unique N_Port_ID.

A VN2VN ENode MAC maintains the list of the reachable VN2VN_Ports in a VN2VN Neighbor Set. The VN2VN Neighbor Set is updated upon receiving N_Port_ID Claim Notifications and N_Port_ID Claim Responses (see 7.11.8.14) and checked upon receiving N_Port_ID Beacons.

Upon receiving a N_Port_ID Claim Notification claiming a different Locally Unique N_Port_ID, a VN2VN ENode MAC shall add the notifying VN2VN_Port to its VN2VN Neighbor Set and shall reply with a unicast N_Port_ID Claim

Response. The N_Port_ID Claim Response shall be delayed by a random time uniformly distributed between 0 and 100 ms. The N_Port_ID Claim Notification provides the maximum FCoE size the VN2VN ENode MAC intends to use for VN_Port to VN_Port Virtual Links, and the N_Port_ID Claim Response FIP PDU shall have a length equal to the minimum of the FCoE sizes supported by the two involved VN2VN ENode MACs. The two involved VN2VN ENode MACs shall not transmit FCoE PDUs larger than the minimum of the FCoE sizes they support.

Upon receiving a nonconflicting N_Port_ID Claim Response a VN2VN ENode MAC shall add the responding VN2VN_Port to its VN2VN Neighbor Set. Once a VN2VN_Port is part of the VN2VN Neighbor Set the VN2VN ENode MAC may instantiate a VN_Port to VN_Port Virtual Link with the VN2VN neighbor (see 7.11.4.3).

A VN2VN ENode MAC determines from its VN2VN Neighbor Set the VN2VN ENode MACs with which to establish VN_Port to VN_Port Virtual Links (i.e., the VN2VN Login Set), also on the basis of their FC-4 support. A VN2VN ENode wishing to discover all its VN2VN neighbors in order to select the VN2VN Login Set should wait for ANNOUNCE_WAIT (i.e., the N_Port_ID Claim Responses timeout) upon transmitting a N_Port_ID Claim Notification to collect N_Port_ID Claim Responses.

After ANNOUNCE_WAIT from sending the N_Port_ID Claim Notification, a VN2VN ENode MAC shall transmit a multicast N_Port_ID Beacon to All-VN2VN-ENode-MACs and shall continue to transmit multicast N_Port_ID Beacons periodically every BEACON_PERIOD. Upon transmitting the first N_Port_ID Beacon the VN2VN ENode MAC may instantiate VN_Port to VN_Port Virtual Links (see 7.11.4.3).

If after having claimed a Locally Unique N_Port_ID a VN2VN ENode MAC receives a N_Port_ID Claim Notification claiming its Locally Unique N_Port_ID or a N_Port_ID Beacon announcing its Locally Unique N_Port_ID, then the VN2VN ENode MAC shall check the N_Port_Name carried in the Vx_Port Identification descriptor of the received N_Port_ID Claim Notification or N_Port_ID Beacon.

NOTE 35 – This situation may happen only while joining two previously separated networks. In this case there is a chance of N_Port_ID conflict, resulting in one or more VN2VN_Ports needing to change their Locally Unique N_Port_IDs.

If the received N_Port_Name is greater than the N_Port_Name of its VN2VN_Port, then the VN2VN ENode MAC shall implicitly de-instantiate all its VN_Port to VN_Port Virtual Links and select a new Locally Unique N_Port_ID (see 7.11.6.2.1). If the received N_Port_Name is lower than the N_Port_Name of its VN2VN_Port, then the VN2VN ENode MAC shall transmit a N_Port_ID Claim Notification to All-VN2VN-ENode-MACs to notify all other VN2VN ENode MACs on the network that it has that N_Port_ID. A VN2VN ENode MAC with a VN2VN_Port that has a Virtual Link with a VN2VN_Port that is changing its N_Port_ID shall implicitly de-instantiate that Virtual Link upon receiving a N_Port_ID Claim Notification associating that N_Port_ID to a different N_Port_Name.

Reception of a N_Port_ID Beacon from a VN2VN ENode MAC not listed in the VN2VN Neighbor Set is an indication of a network join. If a N_Port_ID Claim Notification has not been sent in the past ANNOUNCE_WAIT, then a VN2VN ENode MAC shall transmit a N_Port_ID Claim Notification to All-VN2VN-ENode-MACs upon receiving such a N_Port_ID Beacon. This N_Port_ID Claim Notification shall be delayed by a random time uniformly distributed between 0 and 100 ms.

If after having claimed a Locally Unique N_Port_ID a VN2VN ENode MAC receives a N_Port_ID Probe Request probing its Locally Unique N_Port_ID, then the VN2VN ENode MAC shall reply to the N_Port_ID Probe Request with a N_Port_ID Probe Reply.

N_Port_ID Probe Requests probing a different Locally Unique N_Port_ID shall be ignored.

At any time, upon receiving a N_Port_ID P2P Claim Notification (see 7.11.8.13) or a N_Port_ID P2P Beacon (see 7.11.8.15), a VN2VN ENode MAC operating in multi-node mode shall transmit a N_Port_ID Probe Request probing its N_Port_ID to All-VN2VN-ENode-MACs to indicate to the sending VN2VN ENode MAC operating in point-to-point mode that it is not in its expected network configuration.

7.11.6.3 Point-to-point operation

7.11.6.3.1 Claiming a Locally Unique N_Port_ID

After selecting a tentative Locally Unique N_Port_ID, a VN2VN ENode MAC operating in point-to-point mode shall claim it by sending a N_Port_ID P2P Claim Notification to All-PT2PT-ENode-MACs.

NOTE 36 – A VN2VN ENode MAC operating in point-to-point mode does not send N_Port_ID Probe Requests.

The N_Port_ID P2P Claim Notification (see 7.11.8.13) provides the maximum FCoE size the VN2VN ENode MAC intends to use for VN_Port to VN_Port Virtual Links. A responding N_Port_ID P2P Claim Response FIP PDU (see 7.11.8.14) shall have a length equal to the minimum of the FCoE sizes supported by the two involved VN2VN ENode MACs.

If no N_Port_ID P2P Claim Responses are received within ANNOUNCE_WAIT, then the VN2VN ENode MAC shall continue sending a N_Port_ID P2P Claim Notification every ANNOUNCE_WAIT until a N_Port_ID Claim Response is received. An N_Port_ID P2P Claim Notification may be sent upon receiving an N_Port_ID P2P Claim Notification without waiting for the expiration of the ANNOUNCE_WAIT timer.

If more than one N_Port_ID P2P Claim Responses are received from different VN2VN ENode MACs within ANNOUNCE_WAIT, then the VN2VN ENode MAC shall cease the point-to-point operations.

If one N_Port_ID P2P Claim Response is received within ANNOUNCE_WAIT, then the VN2VN ENode MAC shall add the responding VN2VN_Port to its VN2VN Neighbor Set. Once a VN2VN_Port is part of the VN2VN Neighbor Set the VN2VN ENode MAC may instantiate a VN_Port to VN_Port Virtual Link with the VN2VN neighbor (see 7.11.4.3). Upon receiving the N_Port_ID P2P Claim Response, the VN2VN MAC shall transmit a multicast N_Port_ID P2P Beacon (see 7.11.8.15) to All-PT2PT-ENode-MACs and shall continue to transmit multicast N_Port_ID P2P Beacons periodically every BEACON_PERIOD.

If a N_Port_ID P2P Claim Notification claiming its Locally Unique N_Port_ID is received, then the VN2VN ENode MAC shall check the N_Port_Name carried in the Vx_Port Identification descriptor of the received N_Port_ID P2P Claim Notification. If the received N_Port_Name is greater than the N_Port_Name of its VN2VN_Port, then the VN2VN ENode MAC shall select a new random Locally Unique N_Port_ID and shall reply with a unicast N_Port_ID P2P Claim Response indicating the new Locally Unique N_Port_ID. If the received N_Port_Name is lower than the N_Port_Name of its VN2VN_Port, then the VN2VN ENode MAC shall keep its selected Locally Unique N_Port_ID and shall reply with a unicast N_Port_ID P2P Claim Response indicating the originally selected Locally Unique N_Port_ID.

The VN2VN Neighbor Set is updated upon receiving an N_Port_ID P2P Claim Response (see 7.11.8.14).

If a N_Port_ID P2P Claim Notification claiming a different Locally Unique N_Port_ID is received, then the VN2VN ENode MAC shall reply with a unicast N_Port_ID P2P Claim Response.

A VN2VN ENode MAC shall cease point-to-point operations if after transmitting the first N_Port_ID P2P Beacon the VN2VN ENode MAC receives from a VN2VN_Port different than the one present in its Neighbor Set a:

- a) N_Port_ID P2P Claim Notification;
- b) N_Port_ID P2P Claim Response; or
- c) N_Port_ID P2P Beacon.

A VN2VN ENode MAC operating in point-to-point mode shall cease the point-to-point operations, at any time, upon receiving a:

- a) N_Port_ID Probe Request;
- b) N_Port_ID Claim Notification;
- c) N_Port_ID Beacon; or
- d) FIP Discovery Advertisement.

7.11.6.4 Persistence of Locally Unique N_Port_IDs

VN2VN ENode MACs that are equipped with persistent storage may record the Locally Unique N_Port_ID they have selected for themselves. The recorded Locally Unique N_Port_ID may be also administratively configured. On booting, VN2VN ENode MACs with a recorded Locally Unique N_Port_ID should use that Locally Unique N_Port_ID as their first tentative Locally Unique N_Port_ID. This increases the stability of N_Port_IDs (e.g., if some VN2VN ENode MACs are powered off, then if they are powered on they resume using the previous Locally Unique N_Port_IDs, instead of picking different N_Port_IDs and potentially having to resolve conflicts).

Depending on local configuration, a VN2VN ENode MAC may stop the processing of the Locally Unique N_Port_ID selection protocol and report the situation in a vendor specific manner if it detects that its recorded Locally Unique N_Port_ID is already in use on the network.

7.11.7 FIP frames

7.11.7.1 FIP frame format

A FIP frame is an Ethernet frame (see IEEE 802.3-2012) containing a FIP PDU. FIP frames shall be formatted in accordance with IEEE 802.3-2012 and the MAC Client Data field within the Ethernet frame shall contain a FIP PDU (see Table 25). The use of an 802.1Q tag header is optional and additional IEEE 802.1 defined tags may be present in a FIP frame. See annex B for examples of FIP frames.

The format of a FIP PDU is specified in Table 25.

Table 25 – FIP PDU format

Bit Word	3	3	2	2	2	2	2	2	2	2	2	1	1	1	1	1	1	1	1	1	0	9	8	7	6	5	4	3	2	1	0
0	Type = FIP_TYPE														Version				Reserved												
1	(MSB)																														
n	Encapsulated FIP operation (n words)																								(LSB)						

The Type field in the Ethernet header shall be set to FIP_TYPE (see Table 53). The Type field is not part of the FIP PDU.

The Version field shall be set to FIP_FRAME_VER (see Table 53).

The Encapsulated FIP operation field shall be as specified in 7.11.7.2.

A received FIP frame containing no encapsulated FIP operation shall be discarded.

7.11.7.2 Encapsulated FIP operation

The format of the encapsulated FIP operation is specified in Table 26.

Table 26 – Encapsulated FIP operation format

Bit Word	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	FIP Protocol Code														Reserved							FIP Subcode										
1	FIP Descriptor List Length														F	S	Reserved							C	D	R	A	S	F			
	P														P										P							
2	FIP Descriptor List (m words)																															
n-1	FIP_Pad (n-m-2 words)																															

The FIP Protocol Code and FIP Subcode field values and operations are specified in Table 27.

Table 27 – FIP Protocol Code and FIP Subcode field values

FIP Protocol Code	FIP Subcode	FIP Operation	Reference
0001h	01h	Discovery Solicitation	7.11.8.2
	02h	Discovery Advertisement	7.11.8.3
0002h	01h	Virtual Link Instantiation request	7.11.8.4
	02h	Virtual Link Instantiation reply	7.11.8.4
0003h	01h	FIP Keep Alive	7.11.8.5
	02h	FIP Clear Virtual Links	7.11.8.6
0004h	01h	FIP VLAN Request	7.11.8.7
	02h	FIP VLAN Notification	7.11.8.8
	03h	FIP VN2VN VLAN Notification	7.11.8.9
0005h	01h	N_Port_ID Probe Request	7.11.8.11
	02h	N_Port_ID Probe Reply	7.11.8.12
	03h	N_Port_ID Claim Notification	7.11.8.13
	04h	N_Port_ID Claim Response	7.11.8.14
	05h	N_Port_ID Beacon	7.11.8.15
FFF8h to FFFEh	00h to FFh	Vendor Specific	7.11.8.10
All others	All others	Reserved	

The FIP Descriptor List Length field shall be set to the length in words of all FIP descriptor(s) that follow. A receiver shall ignore any data beyond the length specified in this field.

The Fabric Provided (FP) bit and Server Provided (SP) bit settings are dependent on the FIP operation and shall be set as specified in Table 28.

Table 28 – Fabric Provided bit and Server Provided bit setting

Bit	FIP Protocol	FIP Operation (see Table 51)	Setting
Fabric Provided	Discovery	Discovery Solicitation	Shall be set to one.
		Discovery Advertisement	Shall be set to one.
	Virtual Link Instantiation	FIP FLOGI Request	Shall be set to one.
		FIP NPIV FDISC Request	Shall be set to one.
		FIP FLOGI LS_ACC	Shall be set to one.
		FIP NPIV FDISC LS_ACC	Shall be set to one.
	All others	All others	Reserved
Server Provided	Discovery	Discovery Solicitation	Shall be set to zero.
		Discovery Advertisement	Shall be set to zero.
	Virtual Link Instantiation	FIP FLOGI Request	Shall be set to zero.
		FIP NPIV FDISC Request	Shall be set to zero.
		FIP FLOGI LS_ACC	Shall be set to zero.
		FIP NPIV FDISC LS_ACC	Shall be set to zero.
	All others	All others	Reserved

The Controlling FCF (C) bit indicates if the originator of a FIP frame is a Controlling FCF. The Controlling FCF bit shall be set to one in Discovery Solicitations and Discovery Advertisements originated by a VA_Port/VE_Port capable FCF-MAC (i.e., Discovery Solicitations and Advertisements addressed to FDFs and FCFs and originated by a Controlling FCF). The Controlling FCF bit shall be set to zero in Discovery Advertisements originated by a VF_Port capable FCF-MAC (i.e., Discovery Advertisements addressed to ENodes and originated by a Controlling FCF or by an FCF). The Controlling FCF bit shall be set to zero by FDFs. The Controlling FCF bit is reserved for all other FIP operations.

The FDF (D) bit indicates if the originator of the FIP frame is an FDF. The FDF bit shall be set to one in Discovery Solicitations and Discovery Advertisements originated by a VA_Port capable FDF-MAC (i.e., Discovery Solicitations and Advertisements addressed to FDFs and FCFs and originated by an FDF). The FDF bit shall be set to zero in Discovery Advertisements originated by a VF_Port capable FDF-MAC (i.e., Discovery Advertisements addressed to ENodes and originated by an FDF). The FDF bit shall be set to zero by FCFs. The FDF bit is reserved for all other FIP operations.

If a FIP frame is received by a Controlling FCF or an FDF with the Controlling FCF bit set to one and the FDF bit set to one, then the FIP frame is invalid, shall be ignored, and its reception should be reported in a vendor specific manner.

The REC/P2P (RP) bit is meaningful in FIP N_Port_ID Probe Requests, FIP N_Port_ID Claim Notifications, FIP N_Port_ID Claim Responses, and FIP N_Port_ID Beacons. The REC/P2P bit is reserved for all other FIP operations. Usage of the REC/P2P bit is specified in 7.11.6.2.1 and 7.11.8. The REC/P2P bit is a REC bit in FIP N_Port_ID Probe Requests. The REC/P2P bit is a P2P bit in FIP N_Port_ID Claim Notifications, FIP N_Port_ID Claim Responses, and FIP N_Port_ID Beacons.

The Available for Login (A) bit shall be set to one in a Discovery Advertisement if the originating FCF or FDF is available to process FIP FLOGI, FIP NPIV FDISC, or FIP ELP Requests (see 7.11.3). The Available for Login bit shall be set to zero in a Discovery Advertisement if the originating FCF or FDF is not available to process FIP FLOGI, FIP NPIV FDISC, or FIP ELP Requests. The Available for Login bit is reserved for all other FIP operations.

The Solicited (S) bit shall be set to one in solicited unicast Discovery Advertisements (i.e., Discovery Advertisements transmitted in response to a Discovery Solicitation). The Solicited bit shall be set to zero in unsolicited multicast Discovery Advertisements (i.e., Discovery Advertisements not transmitted in response to a Discovery Solicitation). The Solicited bit is reserved for all other FIP operations.

The FCF (F) bit shall be set to one in a Discovery Solicitation, Discovery Advertisement, or FIP VLAN Request if the originating device is an FCF or an FDF. The FCF bit shall be set to zero in a Discovery Solicitation, Discovery Advertisement, or in a FIP VLAN Request if the originating device is not an FCF nor an FDF. The FCF bit is reserved for all other FIP operations.

The FIP Descriptor List field shall contain one or more FIP descriptors (see 7.11.7.3).

The FIP_Pad field shall be used in solicited unicast Discovery Advertisements to extend the FIP PDU (see Table 25) to have a length that matches the Max_FCoE_Size field value in the Max FCoE Size descriptor in the Discovery Solicitation that the Discovery Advertisement is responding to (see 7.11.8.3). The FIP_Pad field shall be used in N_Port_ID Claim Responses to extend the FIP PDU to have a length that matches the minimum of the FCoE sizes supported by the two involved VN2VN ENode MACs. The FIP_Pad field shall be of zero length (i.e., not present) for all other FIP operations.

Received FIP frames shall be checked for correct formatting before any FIP descriptor processing occurs. A malformed FIP frame shall be discarded and should be reported in a vendor specific manner. The minimum checks for correct formatting are:

- a) the FIP Descriptor List Length value matches the sum of the descriptors' lengths in the FIP Descriptor List;
- b) the FIP Protocol Code field and FIP Subcode field are valid (see Table 27);
- c) the critical descriptors (see 7.11.7.3.1) required by the FIP Protocol Code and FIP Subcode are present;
- d) no critical descriptors other than the ones required by the FIP Protocol Code and FIP Subcode are present; and
- e) descriptors use valid values for FKA_ADV_PERIOD (see Table 53) and VLAN IDs (see 7.11.7.3.15).

7.11.7.3 FIP descriptors

7.11.7.3.1 FIP descriptor overview

FIP descriptors are specified using a TLV format (i.e., Type, Length, Value). The length field value shall be specified as the number of words in the FIP descriptor including the TLV format. FIP descriptor type values are split into two ranges, critical and non-critical, as specified in Table 29.

Table 29 – FIP descriptor type value ranges

Range	Value	Description
Critical	0 to 127	An FCoE Controller that receives a FIP frame with an unknown critical descriptor shall discard the FIP frame.
Non-critical	128 to 255	An FCoE Controller that receives a FIP frame with one or more unknown non-critical descriptors shall ignore the unknown descriptors and continue to process the FIP frame.

A descriptor with an invalid length value shall be considered invalid. If that descriptor is critical, then the entire FIP operation shall be discarded. If that descriptor is not critical, then the descriptor shall be ignored.

The FIP descriptor types are specified in Table 30. See Table 51 for how FIP descriptors are used.

Table 30 – FIP descriptor types

Range	Type	FIP Descriptor	Reference
Critical	0	Reserved	
	1	Priority	7.11.7.3.2
	2	MAC address	7.11.7.3.3
	3	FC-MAP	7.11.7.3.4
	4	Name_Identifier	7.11.7.3.5
	5	Fabric	7.11.7.3.6
	6	Max FCoE Size	7.11.7.3.7
	7	FLOGI ^a	7.11.7.3.8
	8	NPIV FDISC ^a	7.11.7.3.9
	9	LOGO ^a	7.11.7.3.10
	10	ELP ^a	7.11.7.3.11
	11	Vx_Port Identification	7.11.7.3.12
	12	FKA_ADV_Period	7.11.7.3.13
	13	Vendor_ID	7.11.7.3.14
	14	VLAN	7.11.7.3.15
	15	VN2VN Attributes	7.11.7.3.17
Non-critical	16 to 127	Reserved	
	128	Clear Virtual Links Reason Code	7.11.7.3.18
	129 to 240	Reserved	
	241 to 254	Vendor Specific	7.11.7.3.16
	255	Reserved	
a The FC CRC, SOF, and EOF shall not be included in the FIP descriptor.			

7.11.7.3.2 FIP Priority descriptor

The FIP Priority descriptor is used in FIP operations as specified in Table 51. An ENode may use the value provided in the Priority descriptor of received Discovery Advertisements to select the FCF-MAC or FDF-MAC to which to perform FIP FLOGI. The default value for the Priority field is DEFAULT_FIP_PRIORITY (see Table 53). The highest priority value is 0 and the lowest priority value is 255 (i.e., lower numerical values indicate higher priorities).

The FIP Priority descriptor format shall be as specified in Table 31.

Table 31 – FIP Priority descriptor format

Bit Word	3 1	3 0	2 9	2 8	2 7	2 6	2 5	2 4	2 3	2 2	2 1	1 0	1 9	1 8	1 7	1 6	1 5	1 4	1 3	1 2	1 1	1 0	9	8	7	6	5	4	3	2	1	0
0	Type = 01h								Length = 01h								Reserved								Priority							

Priority: the Priority value associated with the sending FCF-MAC or FDF-MAC.

7.11.7.3.3 FIP MAC address descriptor

The FIP MAC address descriptor is used in FIP operations as specified in Table 51.

The FIP MAC address descriptor format shall be as specified in Table 32.

Table 32 – FIP MAC address descriptor format

Bit Word	3 1	3 0	2 9	2 8	2 7	2 6	2 5	2 4	2 3	2 2	2 1	1 0	1 9	1 8	1 7	1 6	1 5	1 4	1 3	1 2	1 1	1 0	9	8	7	6	5	4	3	2	1	0
0	Type = 02h								Length = 02h								(MSB)															
1	MAC address																(LSB)															

MAC address: the MAC address contained in the descriptor. Valid MAC addresses are unicast addresses.

7.11.7.3.4 FIP FC-MAP descriptor

The FIP FC-MAP descriptor is used in FIP operations as specified in Table 51.

The FIP FC-MAP descriptor format shall be as specified in Table 33.

Table 33 – FIP FC-MAP descriptor format

Bit Word	3 1	3 0	2 9	2 8	2 7	2 6	2 5	2 4	2 3	2 2	2 1	1 0	1 9	1 8	1 7	1 6	1 5	1 4	1 3	1 2	1 1	1 0	9	8	7	6	5	4	3	2	1	0
0	Type = 03h								Length = 02h								Reserved															
1	Reserved								(MSB)								FC-MAP								(LSB)							

FC-MAP: the value to be used as the most significant 24 bits in FPMAs (see 7.9).

7.11.7.3.5 FIP Name_Identifier descriptor

The FIP Name_Identifier descriptor is used in FIP operations as specified in Table 51.

The FIP Name_Identifier descriptor format shall be as specified in Table 34.

Table 34 – FIP Name_Identifier descriptor format

Bit Word	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	Type = 04h								Length = 03h								Reserved															
1	(MSB)																															
2	Name_Identifier																(LSB)															

Name_Identifier: the Name_Identifier (see FC-FS-4) contained in the descriptor.

7.11.7.3.6 FIP Fabric descriptor

The FIP Fabric descriptor is used in FIP operations as specified in Table 51.

The FIP Fabric descriptor format shall be as specified in Table 35.

Table 35 – FIP Fabric descriptor format

Bit Word	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	Type = 05h								Length = 04h								Reserved				VF_ID											
1	Reserved								(MSB)								FC-MAP								(LSB)							
2	(MSB)																															
3	Fabric_Name																(LSB)															

VF_ID: the VF_ID (see see FC-FS-4) associated with the Fabric, if any.

FC-MAP: the value to be used as the most significant 24 bits in FPMAs (see 7.9).

Fabric_Name: the Fabric_Name (see FC-FS-4) identifying the Fabric.

7.11.7.3.7 FIP Max FCoE Size descriptor

The FIP Max FCoE Size descriptor is used in FIP operations as specified in Table 51.

The FIP Max FCoE Size descriptor format shall be as specified in Table 36.

Table 36 – FIP Max FCoE Size descriptor format

Bit Word	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	Type = 06h								Length = 01h								Max_FCoE_Size															

Max_FCoE_Size: the size in bytes that the FIP PDU (see Table 25) in a solicited unicast Discovery Advertisement is requested to be extended to.

7.11.7.3.8 FIP FLOGI descriptor

The FIP FLOGI descriptor is used in FIP operations as specified in Table 51.

The FIP FLOGI descriptor format shall be as specified in Table 37.

Table 37 – FIP FLOGI descriptor format

Bit Word	3	3	2	2	2	2	2	2	2	2	2	1	1	1	1	1	1	1	1	1	0	9	8	7	6	5	4	3	2	1	0	
0	Type = 07h								Length								Reserved															
1	(MSB)								FLOGI request or FLOGI LS_ACC/LS_RJT																							
n																	(LSB)															

Length: shall be set to 36 for a FLOGI Request and FLOGI LS_ACC, or to 9 for a FLOGI LS_RJT.

FLOGI request or FLOGI LS_ACC/LS_RJT: an encapsulated FLOGI request, FLOGI LS_ACC, or FLOGI LS_RJT shall be a complete Fibre Channel frame content (see FC-FS-4) with a Fibre Channel Frame_Header and an ELS payload, but without the CRC field. In a FLOGI request or FLOGI LS_ACC, the Payload bit shall be set to zero (see FC-LS-3).

7.11.7.3.9 FIP NPIV FDISC descriptor

The FIP NPIV FDISC descriptor is used in FIP operations as specified in Table 51.

The FIP NPIV FDISC descriptor format shall be as specified in Table 38

Table 38 – FIP NPIV FDISC descriptor format

Bit Word	3	3	2	2	2	2	2	2	2	2	2	2	1	1	1	1	1	1	1	1	1	0	9	8	7	6	5	4	3	2	1	0
0	Type = 08h								Length								Reserved															
1	(MSB)								NPIV FDISC request or NPIV FDISC LS_ACC/LS_RJT																							
n																	(LSB)															

Length: shall be set to 36 for an FDISC request and FDISC LS_ACC, or to 9 for an FDISC LS_RJT.

NPIV FDISC request or NPIV FDISC LS_ACC/LS_RJT: an encapsulated NPIV FDISC request, NPIV FDISC LS_ACC, or NPIV FDISC LS_RJT shall be a complete Fibre Channel frame content (see FC-FS-4) with a Fibre Channel Frame_Header and an ELS payload, but without the CRC field. In an NPIV FDISC request or NPIV FDISC LS_ACC, the Payload bit shall be set to zero (see FC-LS-3).

7.11.7.3.10 FIP LOGO descriptor

The FIP LOGO descriptor is used in FIP operations as specified in Table 51.

The FIP LOGO descriptor format shall be as specified in Table 39.

Table 39 – FIP LOGO descriptor format

[illegible]

Length: shall be set to 11 for a LOGO request, 8 for a LOGO LS_ACC, or to 9 for a LOGO LS_RJT.

LOGO request or LOGO LS_ACC/LS_RJT: an encapsulated LOGO request, LOGO LS_ACC, or LOGO LS_RJT shall be a complete Fibre Channel frame content (see FC-FS-4) with a Fibre Channel Frame_Header and an ELS payload, but without the CRC field (see FC-LS-3).

7.11.7.3.11 FIP ELP descriptor

The FIP ELP descriptor is used in FIP operations as specified in Table 51.

The FIP ELP descriptor format shall be as specified in Table 40.

Table 40 – FIP ELP descriptor format

Bit Word	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	Type = 0Ah								Length								Reserved															
1	(MSB)																															
n	ELP request or ELP SW_ACC/SW_RJT																(LSB)															

Length: shall be set to 33 for an ELP request and ELP SW_ACC, or to 9 for an ELP SW_RJT.

ELP request or ELP SW_ACC/SW_RJT: an encapsulated ELP request, ELP SW_ACC, or ELP SW_RJT shall be a complete Fibre Channel frame content (see FC-FS-4) with a Fibre Channel Frame_Header and an SW_ILS payload, but without the CRC field (see FC-SW-6).

7.11.7.3.12 FIP Vx_Port Identification descriptor

The FIP Vx Port Identification descriptor is used in FIP operations as specified in Table 51.

The FIPVx Port Identification descriptor format shall be as specified in Table 41.

Table 41 – FIP Vx_Port Identification descriptor format

Bit Word	3 1	3 0	2 9	2 8	2 7	2 6	2 5	2 4	2 3	2 2	2 1	2 0	1 9	1 8	1 7	1 6	1 5	1 4	1 3	1 2	1 1	1 0	9	8	7	6	5	4	3	2	1	0																
0	Type = 0Bh								Length = 05h								(MSB)																															
1	MAC address																(LSB)																															
2	Reserved								(MSB)								Address Identifier								(LSB)																							
3	(MSB)																																															
4																	Port_Name																(LSB)															

MAC address: the MAC address associated with the referred VN_Port, VE_Port, or VA_Port. Valid MAC addresses are unicast addresses.

Address Identifier: the address identifier associated with the referred VN_Port or the value FFFFDh for a VE_Port or the value FFFF9h for a VA_Port.

Port_Name: the N_Port_Name of the referred VN_Port or the E_Port_Name of the referred VE_Port or the A_Port_Name of the referred VA_Port.

7.11.7.3.13 FIP FKA_ADV_Period descriptor

The FIP FKA_ADV_Period descriptor is used in FIP operations as specified in Table 51.

The FIP FKA_ADV_Period descriptor format shall be as specified in Table 42.

Table 42 – FIP FKA_ADV_Period descriptor format

Bit Word	3	3	2	2	2	2	2	2	2	2	2	1	1	1	1	1	1	1	1	1	0	9	8	7	6	5	4	3	2	1	0
0	Type = 0Ch								Length = 02h								Reserved														D
1	FKA_ADV_PERIOD																														

Disable Virtual Link Maintenance (D): the value of the Disable Virtual Link Maintenance bit shall be administratively configurable on FCFs and FDFs. The Disable Virtual Link Maintenance bit shall be set to zero unless administratively set to one. VE_Port capable FCF-MACs, VA_Port/VE_Port capable FCF-MACs, and VA_Port capable FDF-MACs shall ignore the value of the Disable Virtual Link Maintenance bit provided in received Discovery Advertisements. ENode MACs shall process the value of the Disable Virtual Link Maintenance bit provided in received Discovery Advertisements.

If the Disable Virtual Link Maintenance bit is set to zero, then the receiving ENode MAC shall verify periodic reception of Discovery Advertisements and shall transmit periodic ENode FIP Keep Alive and VN_Port FIP Keep Alive frames as specified in 7.11.5.2. Upon transmitting Discovery Advertisements with the Disable Virtual Link Maintenance bit set to zero, the VF_Port capable FCF-MAC or VF_Port capable FDF-MAC shall verify periodic reception of ENode FIP Keep Alive and VN_Port FIP Keep Alive frames as specified in 7.11.5.2.

If the Disable Virtual Link Maintenance bit is set to one, then the receiving ENode MAC may verify periodic reception of Discovery Advertisements and should not transmit periodic ENode FIP Keep Alive and VN_Port FIP Keep Alive frames as specified in 7.11.5.2. Upon transmitting Discovery Advertisements with the Disable Virtual Link Maintenance bit set to one, the VF_Port capable FCF-MAC or VF_Port capable FDF-MAC shall not verify periodic reception of ENode FIP Keep Alive and VN_Port FIP Keep Alive frames as specified in 7.11.5.2 and should discard possible received ENode FIP Keep Alive and VN_Port FIP Keep Alive frames.

FKA_ADV_PERIOD: the value of the advertised FKA_ADV_PERIOD (see Table 53). See Table 53 for the range of valid values for FKA_ADV_PERIOD.

7.11.7.3.14 FIP Vendor_ID descriptor

The FIP Vendor_ID descriptor is used in FIP operations as specified in Table 51.

The FIP Vendor_ID descriptor format shall be as specified in Table 43.

Table 43 – FIP Vendor_ID descriptor format

Bit Word	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	Type = 0Dh								Length = 03h								Reserved															
1	(MSB)																															
2	Vendor_ID																(LSB)															

Vendor_ID: the vendor's T10 Vendor_ID value (see FC-LS-3).

7.11.7.3.15 FIP VLAN descriptor

The FIP VLAN descriptor is used in in FIP operations as specified in Table 51.

The FIP VLAN descriptor format shall be as specified in Table 44.

Table 44 – FIP VLAN descriptor format

Bit Word	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	Type = 0Eh								Length = 01h								Reserved				FCoE VID											

FCoE VID: the VLAN ID of a VLAN where FCoE services may be available. The range of valid values for VLAN IDs is 001h to FFEh.

7.11.7.3.16 FIP Vendor Specific descriptors

FIP Vendor Specific descriptors are non-critical and may be used in any FIP frame. An FC-BB_E device shall not require the use of any FIP Vendor Specific descriptor in order to operate in accordance with this standard.

The FIP Vendor Specific descriptor format is specified in Table 45.

Table 45 – FIP Vendor Specific descriptor format

Bit Word	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	Type								Length								Reserved															
1	(MSB)								Vendor_ID																(LSB)							
2																																
3	(MSB)								Vendor Specific information																(LSB)							
n																																

Type: the FIP Vendor Specific descriptors are identified by a type value in the range 241 to 254, inclusive.

Length: shall be set to the length in words of the descriptor.

Vendor_ID: the vendor's T10 Vendor_ID value (see FC-LS-3).

Vendor Specific Information: defined by the vendor.

7.11.7.3.17 FIP VN2VN Attributes descriptor

The FIP VN2VN Attributes descriptor format shall be as specified in Table 46.

Table 46 – FIP VN2VN Attributes descriptor format

Bit Word	3 1	3 0	2 9	2 8	2 7	2 6	2 5	2 4	2 3	2 2	2 1	2 0	1 9	1 8	1 7	1 6	1 5	1 4	1 3	1 2	1 1	1 0		9	8	7	6	5	4	3	2	1	0	
0	Type = 0Fh								Length = 29h								Reserved																L	
1 to 8	MSB																																FC-4 Types	LSB
9 to 40	MSB																																FC-4 Features	LSB

Login Avoidance (L): this bit indicates if a FIP FLOGI with the originating VN2VN_Port should be avoided (see 7.11.8.14).

FC-4 Types: the FC-4 Types object, as specified in FC-GS-7.

FC-4 Features: the FC-4 Features object, as specified in FC-GS-7.

7.11.7.3.18 FIP Clear Virtual Links Reason Code descriptor

The FIP Clear Virtual Links Reason Code descriptor format shall be as specified in Table 47.

Table 47 – FIP Clear Virtual Links Reason Code descriptor format

Bit Word	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	Type = 80h								Length = 02h								Reserved															
1	Reserved								Reason Code								Reason Code Explanation				Vendor Specific											

Reason Code: shall be set to one of the Clear Virtual Links Reason Codes specified in Table 48.

Table 48 – FIP Clear Virtual Links Reason Codes

Encoded value (bits 23 to 16)	Description
00h	Timeout
01h	VE_Port, VF_Port, or VA_Port changed state
02h to FFh	Reserved

Reason Code Explanation: shall be set to one of the Clear Virtual Links Reason Code Explanations specified in Table 49.

Table 49 – FIP Clear Virtual Links Reason Code Explanations

Encoded value (bits 15 to 8)	Description
00h	No additional explanation
01h	ENode FIP Keep Alive timeout
02h	VN_Port FIP Keep Alive timeout
03h	Configuration change
04h	Fabric offline
05h	FIP Keep Alive received and no Virtual Link is instantiated
06h	Implicit Logout
05h to FFh	Reserved

The Reason Code field and Reason Code Explanation field may be used to indicate a particular event has occurred. FIP Clear Virtual Links Reason Code descriptor examples are specified in Table 50.

Table 50 – FIP Clear Virtual Links Reason Code descriptor examples

Reason Code	Reason Code Explanation	Description
00h	01h	2.5 * FKA_ADV_PERIOD has passed since the last ENode FIP Keep Alive frame was received by the FCF or FDF.
00h	02h	2.5 * FKA_VN_PERIOD has passed since the last VN_Port FIP Keep Alive frame was received by the FCF or FDF.
01h	03h	A configuration change has been performed on the FCF or FDF that requires the Virtual Link to be de-instantiated.
01h	04h	The Fibre Channel functionality within the FCF or FDF has been disabled or is no longer accessible.

Vendor Specific: defined by the vendor.

7.11.8 FIP operations

7.11.8.1 FIP operations overview

Table 51 specifies the FIP descriptors required in each FIP operation and the recommended order in which they should be encapsulated by a transmitting FCoE Controller. In certain cases, as indicated, strict ordering is required. A receiving FCoE Controller shall process unknown descriptors according to the criticality of the FIP descriptor (see 7.11.7.3.1). Unless otherwise specified (e.g., for a FIP FLOGI Request), a receiving FCoE Controller shall be able to process the FIP descriptors in any order.

NOTE 37 – The ability to process FIP descriptors in any order is to provide flexibility for future protocol extensions.

A FIP operation shall contain the expected critical descriptors (see Table 51) and may contain additional non-critical descriptors. If some critical descriptors are missing or unexpected, then the FIP operation shall be discarded and it should be reported in a vendor specific manner.

Table 51 – FIP operation descriptors and order

FIP Operation	FIP Protocol Code/Subcode ^a	Originator	Expected Descriptors and Order
Discovery Solicitation (see 7.11.8.2.1)	0001h/01h	ENode	1) MAC address 2) Name_Identifier 3) Max FCoE Size
Discovery Solicitation (see 7.11.8.2.2)	0001h/01h	FCF	1) MAC address 2) FC-MAP 3) Name_Identifier 4) Max FCoE Size
Discovery Solicitation (see 7.11.8.2.3)	0001h/01h	FDF	1) MAC address 2) FC-MAP 3) Name_Identifier 4) Max FCoE Size
Discovery Advertisement (see 7.11.8.3)	0001h/02h	FCF or FDF	1) Priority 2) MAC address 3) Name_Identifier 4) Fabric 5) FKA_ADV_Period
FIP FLOGI Request ^b (see 7.11.8.4.2)	0002h/01h	ENode	1) FLOGI 2) MAC address
FIP FLOGI LS_ACC ^b (see 7.11.8.4.2)	0002h/02h	FCF, FDF, or VN2VN ENode	1) FLOGI 2) MAC address
FIP FLOGI LS_RJT ^b (see 7.11.8.4.2)	0002h/02h	FCF, FDF, or VN2VN ENode	1) FLOGI
FIP NPIV FDISC Request ^b (see 7.11.8.4.2)	0002h/01h	ENode	1) NPIV FDISC 2) MAC address
FIP NPIV FDISC LS_ACC ^b (see 7.11.8.4.2)	0002h/02h	FCF or FDF	1) NPIV FDISC 2) MAC address
FIP NPIV FDISC LS_RJT ^b (see 7.11.8.4.2)	0002h/02h	FCF or FDF	1) NPIV FDISC
FIP Fabric LOGO Request ^b (see 7.11.8.4.3)	0002h/01h	ENode or FCF ^c	1) LOGO 2) MAC address
FIP Fabric LOGO LS_ACC ^b (see 7.11.8.4.3)	0002h/02h	FCF, FDF, or ENode ^c	1) LOGO 2) MAC address

FIP Operation	FIP Protocol Code/Subcode ^a	Originator	Expected Descriptors and Order
FIP Fabric LOGO LS_RJT ^b (see 7.11.8.4.3)	0002h/02h	FCF, FDF, or ENode ^c	1) LOGO
FIP ELP Request ^b (see 7.11.8.4.4)	0002h/01h	FCF or FDF	1) ELP 2) MAC address
FIP ELP SW_ACC ^b (see 7.11.8.4.4)	0002h/02h	FCF or FDF	1) ELP 2) MAC address
FIP ELP SW_RJT ^b (see 7.11.8.4.4)	0002h/02h	FCF or FDF	1) ELP
FIP Keep Alive (see 7.11.8.5)	0003h/01h	ENode	1) MAC address 2) Vx_Port Identification ^d
FIP Clear Virtual Links (see 7.11.8.6)	0003h/02h	FCF or FDF	1) MAC address 2) Name_Identifier 3) Vx_Port Identification(s) ^e 4) Clear Virtual Links Reason Codes ^g
FIP VLAN Request (see 7.11.8.7)	0004h/01h	ENode, FCF, or FDF	1) MAC address 2) Name_Identifier ^f
FIP VLAN Notification (see 7.11.8.8)	0004h/02h	FCF or FDF	1) MAC address 2) VLAN(s)
FIP VN2VN VLAN Notification (see 7.11.8.9)	0004h/03h	VN2VN ENode	1) VN2VN MAC address 2) VLAN(s)
FIP Vendor Specific (see 7.11.8.10)	FFF8h to FFFEh / 00h to FFh	ENode, FCF, or FDF	1) Vendor_ID 2) Descriptor(s)
N_Port_ID Probe Request (see 7.11.8.11)	0005h / 01h	VN2VN ENode	1) MAC address 2) Name_Identifier 3) Vx_Port Identification
N_Port_ID Probe Reply (see 7.11.8.12)	0005h / 02h	VN2VN ENode	1) MAC address 2) Name_Identifier 3) Vx_Port Identification
N_Port_ID Claim Notification (see 7.11.8.13)	0005h / 03h	VN2VN ENode	1) MAC address 2) Name_Identifier 3) Vx_Port Identification 4) VN2VN Attributes 5) Max FCoE Size
N_Port_ID Claim Response (see 7.11.8.14)	0005h / 04h	VN2VN ENode	1) MAC address 2) Name_Identifier 3) Vx_Port Identification 4) VN2VN Attributes 5) Max FCoE Size

FIP Operation	FIP Protocol Code/Subcode ^a	Originator	Expected Descriptors and Order
N_Port_ID Beacon (see 7.11.8.15)	0005h / 05h	VN2VN ENode	1) MAC address 2) Name_Identifier 3) Vx_Port Identification
a) Virtual Link instantiation requests are differentiated by the contained descriptors. b) Strict ordering of the FIP descriptors is required in transmission. In this way the encapsulated ELS or SW_ILS results at an offset in the FIP PDU equal to the offset it would have if it was encapsulated in an FCoE PDU. A receiving FCoE Controller is not required to be able to process these FIP operations in any order other than that specified here. c) FCFs and FDFs are allowed to generate FIP Fabric LOGO, however a FIP Clear Virtual Links frame is the recommended method for an FCF or FDF to de-instantiate a Virtual Link, except for the specific cases where LOGO is required (see FC-SP-2). d) The Vx_Port Identification descriptor is present only in VN_Port FIP Keep Alive frames, it is not present in ENode FIP Keep Alive frames. e) A FIP Clear Virtual Links frame intended to de-instantiate VN_Port to VF_Port Virtual Links contains zero or more Vx_Port Identification descriptors. A FIP Clear Virtual Links frame intended to de-instantiate a VE_Port to VE_Port Virtual Link contains one Vx_Port Identification descriptor. f) The Name_Identifier descriptor is optional in FIP VLAN Request frames (i.e., the Name_Identifier descriptor may be not present). g) The FIP Clear Virtual Links Reason Code descriptor is optional in FIP Clear Virtual Links frames (i.e., the FIP Clear Virtual Links Reason Code descriptor may not be present).			

7.11.8.2 FIP Discovery Solicitation

7.11.8.2.1 ENode FIP Discovery Solicitation

As specified in Table 51, a Discovery Solicitation operation originated by the FCoE Controller of an ENode MAC contains a MAC address descriptor (see 7.11.7.3.3), a Name_Identifier descriptor (see 7.11.7.3.5), and a Max FCoE Size descriptor (see 7.11.7.3.7).

A Discovery Solicitation frame may be unicast (i.e., addressed to a specific FCF-MAC or FDF-MAC) or multicast (i.e., addressed to the All-FCF-MACs group address).

The MAC address field in the MAC address descriptor shall be set to the MAC address to use for subsequent solicited Discovery Advertisements from VF_Port capable FCF-MACs or VF_Port capable FDF-MACs.

The Name_Identifier field in the Name_Identifier descriptor shall be set to the Node_Name of the ENode or to zero.

NOTE 38 – The Name_Identifier field may be set to zero if the Node_Name is ambiguous or not yet available when the Discovery Solicitation is transmitted.

The Max_FCoE_Size field in the Max FCoE Size descriptor shall be set to the maximum FCoE PDU size the ENode MAC intends to use for FCoE traffic. The Max_FCoE_Size value shall be specified as the number of octets starting with and including Version field, up to and including the Reserved field following the EOF field (see Table 22).

7.11.8.2.2 FCF FIP Discovery Solicitation

As specified in Table 51, a Discovery Solicitation operation originated by the FCoE Controller of a VE_Port capable FCF-MAC or a VA_Port/VE_Port capable FCF-MAC contains a MAC address descriptor (see 7.11.7.3.3), an FC-MAP descriptor (see 7.11.7.3.4), a Name_Identifier descriptor (see 7.11.7.3.5), and a Max FCoE Size descriptor (see 7.11.7.3.7).

A Discovery Solicitation frame may be unicast (i.e., addressed to a specific FCF-MAC) or multicast (i.e., addressed to the All-FCF-MACs group address).

The MAC address field in the MAC address descriptor shall be set to the MAC address to use for subsequent solicited Discovery Advertisements from VE_Port capable FCF-MACs.

The FC-MAP field in the FC-MAP descriptor shall be set to the FC-MAP value the FCF-MAC is using. If the FC-MAP value is not administratively configured, then the FC-MAP value shall be set to DEFAULT_FC-MAP (see Table 53).

The Name_Identifier field in the Name_Identifier descriptor shall be set to the Switch_Name of the FCF.

The Max_FCoE_Size field in the Max FCoE Size descriptor shall be set to the maximum FCoE frame size the VE_Port capable FCF-MAC intends to use for FCoE traffic. The Max_FCoE_Size value shall be specified as the number of octets starting with and including the Version field, up to and including the Reserved field following the EOF field (see Table 22).

7.11.8.2.3 FDF FIP Discovery Solicitation

As specified in Table 51, a Discovery Solicitation operation originated by the FCoE Controller of a VA_Port capable FDF-MAC contains a MAC address descriptor (see 7.11.7.3.3), an FC-MAP descriptor (see 7.11.7.3.4), a Name_Identifier descriptor (see 7.11.7.3.5), and a Max FCoE Size descriptor (see 7.11.7.3.7).

A Discovery Solicitation frame may be unicast (i.e., addressed to a specific FDF-MAC or FCF-MAC) or multicast (i.e., addressed to the All-FCF-MACs group address).

The MAC address field in the MAC address descriptor shall be set to the MAC address to use for subsequent solicited Discovery Advertisements from VA_Port capable FDF-MACs or VA_Port/VE_Port capable FCF-MACs.

The FC-MAP field in the FC-MAP descriptor shall be set to the FC-MAP value the FDF-MAC is using. If the FC-MAP value is not administratively configured, then the FC-MAP value shall be set to DEFAULT_FC-MAP (see Table 53).

The Name_Identifier field in the Name_Identifier descriptor shall be set to the Switch_Name of the FDF.

The Max_FCoE_Size field in the Max FCoE Size descriptor shall be set to the maximum FCoE frame size the VA_Port capable FDF-MAC intends to use for FCoE traffic. The Max_FCoE_Size value shall be specified as the number of octets starting with and including the Version field, up to and including the Reserved field following the EOF field (see Table 22).

7.11.8.3 FIP Discovery Advertisements

As specified in Table 51, a Discovery Advertisement operation contains a Priority descriptor (see 7.11.7.3.2), a MAC address descriptor (see 7.11.7.3.3), a Name_Identifier descriptor (see 7.11.7.3.5), a Fabric descriptor (see 7.11.7.3.6), and a FKA_ADV_Period descriptor (see 7.11.7.3.13).

If a Discovery Advertisement frame is solicited, then it shall be unicast (i.e., addressed to a specific ENode MAC, FCF-MAC, or FDF-MAC address). If a Discovery Advertisement frame is unsolicited, then it shall be multicast (i.e., addressed to the All-ENode-MACs or to the All-FCF-MACs group addresses).

The Priority field in the Priority descriptor shall be set to the value the originating FCF-MAC or FDF-MAC is using. If the priority value is not administratively configured, then the priority value shall be set to DEFAULT_FIP_PRIORITY (see Table 53).

The MAC address field in the MAC address descriptor shall be set to the originating FCF-MAC or FDF-MAC address.

The Name_Identifier field in the Name_Identifier descriptor shall be set to the Switch_Name of the originating FCF or FDF.

Discovery Advertisements shall only contain a single Fabric descriptor. All Discovery Advertisements from an FCF or FDF in a VLAN shall contain the same single Fabric descriptor. The VF_ID field in the Fabric descriptor shall be set to the VF_ID identifying the advertised FC Fabric. If a VF_ID is not defined for the advertised FC Fabric, then the VF_ID field shall be set to zero. The FC-MAP field in the Fabric descriptor shall be set to the FC-MAP value the FCF or FDF is using. If the FC-MAP value is not administratively configured, then the FC-MAP value shall be set to DEFAULT_FC-MAP (see Table 53). The Fabric_Name field in the Fabric descriptor shall be set to the Fabric_Name for the originating FCF or FDF.

The FKA_ADV_PERIOD field in the FKA_ADV_Period descriptor shall be set to the FKA_ADV_PERIOD value the FCF or FDF is advertising (see Table 53).

The FIP_Pad field shall be used to extend the FIP PDU (see Table 25) to have a length that matches the Max_FCoE_Size field value in the Max FCoE Size descriptor in the Discovery Solicitation to which the Discovery Advertisement is responding. The FIP_Pad field value shall be set to zero and not checked in reception. For an unsolicited Discovery Advertisement, the FIP_Pad field shall be of zero length (i.e., not present).

7.11.8.4 FIP Virtual Link Instantiation requests and replies

7.11.8.4.1 FIP Virtual Link Instantiation requests and replies overview

FIP Virtual Link Instantiation requests and replies encapsulate an ELS or an SW_ILS. The encapsulated ELS or SW_ILS shall be a single-frame Sequence. FIP Virtual Link Instantiation requests and replies are used to perform:

- a) Fabric login between ENode MACs and VF_Port capable FCF-MAC or VF_Port capable FDF-MACs, and between VN2VN ENode MACs (see 7.11.8.4.2);
- b) Fabric logout between ENode MACs and VF_Port capable FCF-MAC or VF_Port capable FDF-MACs, and between VN2VN ENode MACs (see 7.11.8.4.3); and
- c) Exchange Link Parameters between VE_Port capable FCF-MACs, VA_Port/VE_Port capable FCF-MACs, and VA_Port capable FDF-MACs (see 7.11.8.4.4).

7.11.8.4.2 Fabric login

As specified in Table 51:

- a) a FIP FLOGI Request operation contains a FLOGI descriptor (see 7.11.7.3.8) and a MAC address descriptor (see 7.11.7.3.3);
- b) a FIP FLOGI LS_ACC operation contains a FLOGI descriptor (see 7.11.7.3.8) and a MAC address descriptor (see 7.11.7.3.3);
- c) a FIP FLOGI LS_RJT operation contains a FLOGI descriptor (see 7.11.7.3.8);
- d) a FIP NPIV FDISC Request operation contains a NPIV FDISC descriptor (see 7.11.7.3.9) and a MAC address descriptor (see 7.11.7.3.3);
- e) a FIP NPIV FDISC LS_ACC operation contains a NPIV FDISC descriptor (see 7.11.7.3.9) and a MAC address descriptor (see 7.11.7.3.3); and
- f) a FIP NPIV FDISC LS_RJT operation contains a NPIV FDISC descriptor (see 7.11.7.3.9).

The FLOGI or NPIV FDISC descriptor shall be the first descriptor in the operation.

NOTE 39 – In this way, the encapsulated Fibre Channel ELS results at an offset in the FIP PDU equal to the offset it would have if it was encapsulated in a FCoE PDU.

The FLOGI or NPIV FDISC descriptor shall contain:

- a) a complete Fibre Channel frame content with a Fibre Channel Frame_Header (see FC-FS-4) and a FLOGI request payload or an NPIV FDISC request payload (see FC-LS-3), but without the CRC field (see FC-FS-4) for FIP FLOGI Request or FIP NPIV FDISC Request operations. The Payload bit in the FLOGI request payload or NPIV FDISC request payload shall be set to zero (see FC-LS-3);
- b) a complete Fibre Channel frame content with a Fibre Channel Frame_Header (see FC-FS-4) and a FLOGI LS_ACC payload or an NPIV FDISC LS_ACC payload (see FC-LS-3), but without the CRC field (see FC-FS-4) for FIP FLOGI LS_ACC or FIP NPIV FDISC LS_ACC operations. The Payload bit in the FLOGI LS_ACC payload shall be set to zero (see FC-LS-3); or
- c) a complete Fibre Channel frame content with a Fibre Channel Frame_Header (see FC-FS-4) and a FLOGI LS_RJT payload or an NPIV FDISC LS_RJT payload (see FC-LS-3), but without the CRC field (see FC-FS-4) for FIP FLOGI LS_RJT or FIP NPIV FDISC LS_RJT operations.

If instantiating VN_Port to VF_Port Virtual Links, then the MAC address field in the MAC address descriptor of a FIP FLOGI Request operation or a FIP NPIV FDISC Request operation shall contain all zeroes.

The MAC address field in the MAC address descriptor of a FIP FLOGI LS_ACC operation or a FIP NPIV FDISC LS_ACC operation shall contain the MAC address that the FCF or FDF returned for use as VN_Port MAC address. The Fabric Provided bit and Server Provided bit shall be set as specified in Table 28.

If instantiating VN_Port to VN_Port Virtual Links, then the MAC address field in the MAC address descriptor of a FIP FLOGI Request operation or of a FIP FLOGI LS_ACC operation shall contain the originating VN2VN_Port MAC address. The Fabric Provided bit and Server Provided bit shall be set as specified in Table 28.

A FIP FLOGI or FIP NPIV FDISC operation shall be processed respectively as the FLOGI or FDISC ELS with S_ID = 000000h specified in FC-LS-3, ignoring the buffer-to-buffer flow control parameters.

If no response to a FIP FLOGI Request or FIP NPIV FDISC Request is received within the ELS timeout value, then the requester:

- a) shall implicitly terminate the Exchange before the request is retried;
- b) shall not reuse the Exchange resources until 2 times R_A_TOV has elapsed since the transmission of the request;
- c) shall use a different Exchange if the request is retried within 2 times R_A_TOV since the transmission of the request; and
- d) shall not use the ABTS protocol (see FC-FS-4).

If performed between an ENode and an FCF or FDF, then a successful FIP FLOGI operation instantiates a VF_Port, a VN_Port, and a Virtual Link between them. Subsequent FIP NPIV FDISC operations from the same ENode MAC address as the FIP FLOGI operation associate additional VN_Ports to the same VF_Port that was instantiated by the FIP FLOGI operation. A subsequent FIP FLOGI operation from the same ENode MAC address is equivalent to an implicit logout of all the VN_Ports followed by an FLOGI, as specified in FC-LS-3.

NOTE 40 – A duplicate MAC address may be a cause for an implicit logout.

If performed between two VN2VN ENodes, then a successful FIP FLOGI operation instantiates a VN_Port to VN_Port Virtual Link between the two involved VN2VN_Ports.

FCFs and FDFs shall reject FIP FLOGI Requests and FIP NPIV FDISC Requests having:

- a) both the Server Provided bit and the Fabric Provided bit set to zero;

- b) the Fabric Provided bit set to one, the Server Provided bit set to zero, and the MAC address field in the MAC address descriptor not set to zero; or
- c) the Server Provided bit set to one.

Rejections of FIP FLOGI Requests and FIP NPIV FDISC Requests are performed with the LS_RJT Reason Codes and Reason Code Explanations specified in Table 52.

Table 52 – FIP Fabric login rejections

Error Condition	Reason Code (see FC-LS-3)	Reason Code Explanation (see FC-LS-3)
The MAC addressing mode in the FIP FLOGI/FDISC Request is not supported.	FIP Error (i.e., 20h)	MAC addressing mode not supported (i.e., 60h).
The MAC address proposed in the MAC address descriptor of a FIP FLOGI/FDISC Request is incorrect for the requested addressing mode.	FIP Error (i.e., 20h)	Proposed MAC address incorrectly formed (i.e., 61h).

7.11.8.4.3 Fabric logout

As specified in Table 51:

- a) a FIP Fabric LOGO Request operation contains a LOGO descriptor (see 7.11.7.3.10) and a MAC address descriptor (see 7.11.7.3.3);
- b) a FIP Fabric LOGO LS_ACC operation contains a LOGO descriptor (see 7.11.7.3.10) and a MAC address descriptor (see 7.11.7.3.3); and
- c) a FIP Fabric LOGO LS_RJT operation contains a LOGO descriptor (see 7.11.7.3.10).

The LOGO descriptor shall be the first descriptor in the operation.

NOTE 41 – In this way, the encapsulated Fibre Channel ELS results at an offset in the FIP PDU equal to the offset it would have if it was encapsulated in a FCoE PDU.

The LOGO descriptor shall contain:

- a) a complete Fibre Channel frame content with a Fibre Channel Frame_Header (see FC-FS-4) and a Fabric LOGO request payload (see FC-LS-3), but without the CRC field (see FC-FS-4) for FIP Fabric LOGO Request operations;
- b) a complete Fibre Channel frame content with a Fibre Channel Frame_Header (see FC-FS-4) and a Fabric LOGO LS_ACC payload (see FC-LS-3), but without the CRC field (see FC-FS-4) for FIP Fabric LOGO LS_ACC operations; or
- c) a complete Fibre Channel frame content with a Fibre Channel Frame_Header (see FC-FS-4) and a Fabric LOGO LS_RJT payload (see FC-LS-3), but without the CRC field (see FC-FS-4) for FIP Fabric LOGO LS_RJT operations.

If de-instantiating a VN_Port to VF_Port Virtual Link, then the MAC address field in the MAC address descriptor of a FIP Fabric LOGO Request and a FIP Fabric LOGO LS_ACC operation shall be set to the MAC address assigned to the VN_Port that is being logged out.

If de-instantiating a VN_Port to VN_Port Virtual Link, then the MAC address field in the MAC address descriptor of a FIP Fabric LOGO Request and a FIP Fabric LOGO LS_ACC operation shall be set to the originating VN2VN_Port MAC address.

A FIP Fabric LOGO operation shall be processed as the Fabric LOGO ELS specified in FC-LS-3. If no response to a FIP Fabric LOGO Request is received within the ELS timeout value specified in FC-LS-3, then the Exchange resources may be reused and the ABTS protocol (see FC-FS-3) shall not be used.

7.11.8.4.4 Exchange Link Parameters

As specified in Table 51:

- a) a FIP ELP Request operation contains a ELP descriptor (see 7.11.7.3.11) and a MAC address descriptor (see 7.11.7.3.3);
- b) a FIP ELP SW_ACC operation contains a ELP descriptor (see 7.11.7.3.11) and a MAC address descriptor (see 7.11.7.3.3); and
- c) a FIP ELP SW_RJT operation contains a ELP descriptor (see 7.11.7.3.11).

The ELP descriptor shall be the first descriptor in the operation.

NOTE 42 – In this way the encapsulated Fibre Channel SW_ILS results at an offset in the FIP PDU equal to the offset it would have if it was encapsulated in a FCoE PDU.

The ELP descriptor shall contain:

- a) a complete Fibre Channel frame content with a Fibre Channel Frame_Header (see FC-FS-4) and a ELP request payload specifying R_RDY Flow Control (see FC-SW-6), but without the CRC field (see FC-FS-4) for FIP ELP Request operations;
- b) a complete Fibre Channel frame content with a Fibre Channel Frame_Header (see FC-FS-4) and a ELP SW_ACC payload (see FC-SW-6), but without the CRC field (see FC-FS-4) for FIP ELP SW_ACC operations; or
- c) a complete Fibre Channel frame content with a Fibre Channel Frame_Header (see FC-FS-4) and a ELP SW_RJT payload (see FC-SW-6), but without the CRC field (see FC-FS-4) for FIP ELP SW_RJT operations.

The MAC address field in the MAC address descriptor of a FIP ELP Request and a FIP ELP SW_ACC operation shall be set to the MAC address of the destination FCF-MAC or FDF-MAC.

A FIP ELP operation shall be processed as the ELP SW_ILS specified in FC-SW-6, ignoring the buffer-to-buffer flow control parameters.

7.11.8.5 FIP Keep Alive

As shown in Table 51, a FIP Keep Alive operation contains a MAC address descriptor (see 7.11.7.3.3) and zero or one Vx_Port Identification descriptor (see 7.11.7.3.12).

ENode FIP Keep Alive operations (see 7.11.5.1) contains only a MAC address descriptor. VN_Port FIP Keep Alive operations (see 7.11.5.1) contain a MAC address descriptor and a Vx_Port Identification descriptor.

ENode FIP Keep Alive frames shall have the originating ENode MAC address as the source address. The MAC address field in the MAC address descriptor shall be set to the originating ENode MAC address.

VN_Port FIP Keep Alive frames shall have the VN_Port MAC address as the source address. The MAC address field in the MAC address descriptor shall be set to the originating ENode MAC address. In the Vx_Port Identification descriptor, the MAC address field shall be set to the VN_Port MAC address, the Address Identifier field shall be set to the VN_Port N_Port_ID, and the Port_Name field shall be set to the VN_Port N_Port_Name.

7.11.8.6 FIP Clear Virtual Links

7.11.8.6.1 De-instantiating VN_Port to VF_Port Virtual Links

The FCoE Controller of a VF_Port capable FCF-MAC or VF_Port capable FDF-MAC may de-instantiate one or more VN_Port to VF_Port Virtual Links by transmitting a FIP Clear Virtual Links frame to an ENode MAC. As specified in Table 51, this FIP Clear Virtual Links frame shall contain one MAC address descriptor (see 7.11.7.3.3), one Name_Identifier descriptor (see 7.11.7.3.5), zero or more Vx_Port Identification descriptors (see 7.11.7.3.12), and optionally a FIP Clear Virtual Links Reason Code descriptor (see 7.11.7.3.18).

The MAC address field in the MAC address descriptor shall be set to the FCF-MAC or FDF-MAC address of the originating FCF-MAC or FDF-MAC. The Name_Identifier field in the Name_Identifier descriptor shall be set to the Switch_Name of the originating FCF or FDF. The list of Vx_Port Identification descriptors, if present, shall contain one descriptor for each VN_Port whose Virtual Link is to be de-instantiated. For each Vx_Port Identification descriptor, the MAC address field shall be set to the VN_Port MAC address, the Address Identifier field shall be set to the VN_Port N_Port_ID, and the Port_Name field shall be set to the VN_Port N_Port_Name. The FCoE Controller of a receiving ENode MAC shall ignore a Vx_Port Identification descriptor that does not match any of its instantiated VN_Ports. The FIP Clear Virtual Links Reason Code descriptor shall contain a Reason Code (see Table 48) and a Reason Code Explanation (see Table 49). The optional FIP Clear Virtual Links Reason Code descriptor is informational only (i.e., its presence in a received FIP frame shall not change the processing of the FIP Clear Virtual Links operation).

7.11.8.6.2 De-instantiating VE_Port to VE_Port Virtual Links

The FCoE Controller for a VE_Port capable FCF-MAC may de-instantiate a VE_Port to VE_Port Virtual Link by transmitting a FIP Clear Virtual Links frame to a VE_Port capable FCF-MAC. As specified in Table 51, this FIP Clear Virtual Links frame shall contain one MAC address descriptor (see 7.11.7.3.3), one Name_Identifier descriptor (see 7.11.7.3.5), one Vx_Port Identification descriptor (see 7.11.7.3.12), and optionally a FIP Clear Virtual Links Reason Code descriptor (see 7.11.7.3.18).

The MAC address field in the MAC address descriptor shall be set to the FCF-MAC address of the originating FCF-MAC. The Name_Identifier field in the Name_Identifier descriptor shall be set to the Switch_Name of the originating FCF. In the Vx_Port Identification descriptor, the MAC address field shall be set to the remote FCF-MAC address, the Address Identifier field shall be set to FFFFDh, and the Port_Name field shall be set to the remote E_Port_Name. The FIP Clear Virtual Links Reason Code descriptor shall contain a Reason Code (see Table 48) and a Reason Code Explanation (see Table 49). The optional FIP Clear Virtual Links Reason Code descriptor is informational only (i.e., its presence in a received FIP frame shall not change the processing of the FIP Clear Virtual Links operation).

7.11.8.6.3 De-instantiating VA_Port to VA_Port Virtual Links

The FCoE Controller for a VA_Port/VE_Port capable FCF-MAC may de-instantiate a VA_Port to VA_Port Virtual Link by transmitting a FIP Clear Virtual Links frame to a VA_Port capable FDF-MAC. The FCoE Controller for a VA_Port capable FDF-MAC may de-instantiate a VA_Port to VA_Port Virtual Link by transmitting a FIP Clear Virtual Links frame to a VA_Port capable FDF-MAC or a VA_Port/VE_Port capable FCF-MAC. As specified in Table 51, this FIP Clear Virtual Links frame shall contain one MAC address descriptor (see 7.11.7.3.3), one Name_Identifier descriptor (see 7.11.7.3.5), one Vx_Port Identification descriptor (see 7.11.7.3.12), and optionally a FIP Clear Virtual Links Reason Code descriptor (see 7.11.7.3.18).

The MAC address field in the MAC address descriptor shall be set to the FCF-MAC or FDF-MAC address of the originating FCF-MAC or FDF-MAC. The Name_Identifier field in the Name_Identifier descriptor shall be set to the Switch_Name of the originating FCF or FDF. In the Vx_Port Identification descriptor, the MAC address field shall be set to the remote FCF-MAC or FDF-MAC address, the Address Identifier field shall be set to FFFF9h, and the Port_Name field shall be set to the remote A_Port_Name. The FIP Clear Virtual Links Reason Code descriptor shall contain a Reason Code (see Table 48) and a Reason Code Explanation (see Table 49). The optional FIP Clear Virtual Links Reason Code descriptor is informational only (i.e., its presence in a received FIP frame shall not change the processing of the FIP Clear Virtual Links operation).

7.11.8.7 FIP VLAN Request

As specified in Table 51, a FIP VLAN Request operation contains a MAC address descriptor and optionally a Name_Identifier descriptor (see 7.11.7.3.3). A FIP VLAN Request frame may be generated by an ENode MAC, an FCF-MAC, an FDF-MAC, or a VN2VN ENode MAC.

If generated by an ENode MAC or a VN2VN ENode MAC, then the FIP VLAN Request frame shall have the FCF bit set to zero and the MAC address field in the MAC address descriptor shall be set to the originating ENode MAC address. If a Name_Identifier descriptor is present, then the Name_Identifier field shall be set to the Node_Name of the originating ENode.

If generated by an FCF-MAC or FDF-MAC, then the FIP VLAN Request frame shall have the FCF bit set to one and the MAC address field in the MAC address descriptor shall be set to the originating FCF-MAC or FDF-MAC address. If a Name_Identifier descriptor is present, then the Name_Identifier field shall be set to the Switch_Name of the originating FCF or FDF.

7.11.8.8 FIP VLAN Notification

As specified in Table 51, a FIP VLAN Notification operation contains a MAC address descriptor (see 7.11.7.3.3) and one or more VLAN descriptors (see 7.11.7.3.15). A FIP VLAN Notification frame is generated by an FCF-MAC or a FDF-MAC.

The MAC address field in the MAC address descriptor shall be set to the originating FCF-MAC or FDF-MAC address. The FCoE VID field of each of the FIP VLAN descriptors shall be set to a VID over which the FCF-MAC or FDF-MAC supports FC-BB_E operations.

7.11.8.9 FIP VN2VN VLAN Notification

As specified in Table 51, a FIP VN2VN VLAN Notification operation contains a MAC address descriptor (see 7.11.7.3.3) and one or more VLAN descriptors (see 7.11.7.3.15). A FIP VLAN Notification frame is generated by a VN2VN ENode MAC.

The MAC address field in the MAC address descriptor shall be set to the originating VN2VN ENode MAC address. The FCoE VID field of each of the FIP VLAN descriptors shall be set to a VID over which the Locally Unique N_Port_ID protocol may be used.

7.11.8.10 FIP Vendor Specific frames

FIP Vendor Specific frames may be transmitted by ENodes, FCFs, and FDFs. As specified in Table 51, a FIP Vendor Specific frame shall include a Vendor_ID descriptor (see 7.11.7.3.14) as the first descriptor, followed by one or more additional descriptors. An unknown received FIP Vendor Specific frame shall be discarded. An FC-BB_E device shall not require the use of any FIP Vendor Specific frame in order to operate in accordance with this standard.

7.11.8.11 N_Port_ID Probe Request

As shown in Table 51, an N_Port_ID Probe Request operation contains a MAC address descriptor, a Name_Identifier descriptor, and a Vx_Port Identification descriptor. The REC/P2P bit (see 7.11.7.2) in a N_Port_ID Probe Request frame shall be set to one if the probed tentative Locally Unique N_Port_ID is a recorded one (see 7.11.6.4), otherwise shall be set to zero.

The MAC address field in the MAC address descriptor shall be set to the ENode MAC address of the originating VN2VN ENode MAC. The Name_Identifier field in the Name_Identifier descriptor shall be set to the Node_Name of the originating ENode. In the Vx_Port Identification descriptor, the Port_Name field shall be set to the N_Port_Name of the VN2VN_Port whose tentative Locally Unique N_Port_ID is being probed, the Address Identifier field shall be set to the tentative Locally Unique N_Port_ID, and the MAC address field shall be set to the tentative VN_Port MAC address.

An N_Port_ID Probe Request is multicast (i.e., addressed to the All-VN2VN-ENode-MACs multicast address) and uses the ENode MAC address as source address.

7.11.8.12 N_Port_ID Probe Reply

As shown in Table 51, an N_Port_ID Probe Reply operation contains a MAC address descriptor, a Name_Identifier descriptor, and a Vx_Port Identification descriptor. The REC/P2P bit (see 7.11.7.2) is not used by N_Port_ID Probe Reply frames and shall be set to zero.

The MAC address field in the MAC address descriptor shall be set to the ENode MAC address of the originating VN2VN ENode MAC. The Name_Identifier field in the Name_Identifier descriptor shall be set to the Node_Name of the originating ENode. In the Vx_Port Identification descriptor, the Port_Name field shall be set to the N_Port_Name of the originating VN2VN_Port, the Address Identifier field shall be set to the Locally Unique N_Port_ID of the originating VN2VN_Port, and the MAC address field shall be set to the VN_Port MAC address of the originating VN2VN_Port.

An N_Port_ID Probe Reply is unicast (i.e., addressed to the ENode MAC address provided in the MAC address descriptor of the multicast N_Port_ID Probe Request it is responding to) and uses the ENode MAC address as source address.

7.11.8.13 N_Port_ID Claim Notification

As shown in Table 51, an N_Port_ID Claim Notification operation contains a MAC address descriptor, a Name_Identifier descriptor, a Vx_Port Identification descriptor, a VN2VN Attributes descriptor, and a Max FCoE Size descriptor.

The MAC address field in the MAC address descriptor shall be set to the ENode MAC address of the originating VN2VN ENode MAC. The Name_Identifier field in the Name_Identifier descriptor shall be set to the Node_Name of the originating ENode. In the Vx_Port Identification descriptor, the Port_Name field shall be set to the N_Port_Name of the originating VN2VN_Port, the Address Identifier field shall be set to the claimed Locally Unique N_Port_ID, and the MAC address field shall be set to the associated VN_Port MAC address. The FC-4 Types and FC-4 Features fields of the VN2VN Attributes descriptor shall be set to the FC-4 Type and Features supported by the originating VN2VN_Port. The Login Avoidance bit in the VN2VN Attributes descriptor shall be set to zero. The Max_FCoE_Size field in the Max FCoE Size descriptor shall be set to the maximum FCoE PDU size the ENode MAC intends to use for FCoE traffic.

An N_Port_ID Claim Notification is multicast and uses the ENode MAC address as source address. If the REC/P2P bit is set to zero, then an N_Port_ID Claim Notification is addressed to the All-VN2VN-ENode-MACs multicast address. If the REC/P2P bit is set to one, then an N_Port_ID Claim Notification is addressed to the All-PT2PT-ENode-MACs multicast address and is referred to as an N_Port_ID P2P Claim Notification.

7.11.8.14 N_Port_ID Claim Response

As shown in Table 51, an N_Port_ID Claim Response operation contains a MAC address descriptor, a Name_Identifier descriptor, a Vx_Port Identification descriptor, a VN2VN Attributes descriptor, and a Max FCoE Size descriptor.

The MAC address field in the MAC address descriptor shall be set to the ENode MAC address of the originating VN2VN ENode MAC. The Name_Identifier field in the Name_Identifier descriptor shall be set to the Node_Name of the originating ENode. In the Vx_Port Identification descriptor, the Port_Name field shall be set to the N_Port_Name of the originating VN2VN_Port, the Address Identifier field shall be set to the Locally Unique N_Port_ID of the originating VN2VN_Port, and the MAC address field shall be set to the VN_Port MAC address of the originating VN2VN_Port. The FC-4 Types and FC-4 Features fields of the VN2VN Attributes descriptor shall be set to the FC-4 Type and Features supported by the originating VN2VN_Port. The Login Avoidance bit in the VN2VN Attributes descriptor is set to one to indicate to the destination VN2VN_Port that a FIP FLOGI should not be performed with the originating VN2VN_Port or set to zero to indicate to the destination VN2VN_Port that a FIP FLOGI may be performed with the originating VN2VN_Port. The Max_FCoE_Size field in the Max FCoE Size descriptor shall be set to the maximum FCoE PDU size the ENode MAC intends to use for FCoE traffic. The FIP_Pad field shall be used to

extend the FIP PDU to have a length that matches the minimum of the FCoE sizes supported by the two involved VN2VN ENode MACs.

An N_Port_ID Claim Response is unicast (i.e., addressed to the ENode MAC address provided in the MAC address descriptor of the multicast N_Port_ID Claim Notification it is responding to) and uses the ENode MAC address as source address. If the REC/P2P bit is set to zero, then an N_Port_ID Claim Response replies to a multicast N_Port_ID Claim Notification, and the REC/P2P bit is set to zero in the response. If the REC/P2P bit is set to one, then an N_Port_ID Claim Response replies to a multicast N_Port_ID P2P Claim Notification and is referred to as an N_Port_ID P2P Claim Response, and the REC/P2P bit is set to one in the response.

7.11.8.15 N_Port_ID Beacon

As shown in Table 51, a N_Port_ID Beacon operation contains a MAC address descriptor, a Name_Identifier descriptor, and a Vx_Port Identification descriptor.

The MAC address field in the MAC address descriptor shall be set to the ENode MAC address of the originating VN2VN ENode MAC. The Name_Identifier field in the Name_Identifier descriptor shall be set to the Node_Name of the originating VN2VN_Port. In the Vx_Port Identification descriptor, the Port Name field shall be set to the N_Port_Name of the originating VN2VN_Port, the Address Identifier field shall be set to the Locally Unique N_Port_ID of the originating VN2VN_Port, and the MAC address field shall be set to the VN_Port MAC address of the originating VN2VN_Port.

A N_Port_ID Beacon is multicast and uses the VN_Port MAC address as source address. If the REC/P2P bit is set to zero, then an N_Port_ID Beacon is addressed to the All-VN2VN-ENode-MACs multicast address. If the REC/P2P bit is set to one, then an N_Port_ID Beacon is addressed to the All-PT2PT-ENode-MACs multicast address and is referred to as an N_Port_ID P2P Beacon.

7.12 Timers and constants

FC-BB_E timers and constants are specified in Table 53.

Table 53 – FC-BB_E timers and constants

Timer/Constant	Value	Description	Reference
FIP_TYPE	8914h	The value specified in the Ethernet Type field for a FIP PDU.	7.11.7.1
FCoE_TYPE	8906h	The value specified in the Ethernet Type field for an FCoE PDU.	7.10
FIP_FRAME_VER	0001b	The value specified in the Version field for a FIP PDU.	7.11.7.1
FCoE_FRAME_VER	0000b	The value specified in the Version field for an FCoE PDU.	7.10
All-FCoE-MACs	01-10-18-01-00-00	The group address for all FCoE devices.	7.11.1
All-ENode-MACs	01-10-18-01-00-01	The group address for all ENodes.	7.11.1
All-FCF-MACs	01-10-18-01-00-02	The group address for all FCFs and FDFs.	7.11.1

Timer/Constant	Value	Description	Reference
All-VN2VN-ENode-MACs	01-10-18-01-00-04	The group address for all VN2VN ENodes.	7.11.1
All-PT2PT-ENode-MACs	01-10-18-01-00-05	The group address for all PT2PT ENodes.	7.11.1
DEFAULT_FIP_PRIORITY	128	The default value specified in the FIP Priority descriptor.	7.11.7.3.2
DEFAULT_FC-MAP	0EFC00h	The default value for the FC-MAP field in a FIP FC-MAP descriptor.	7.11.7.3.4
VN2VN-FC-MAP	0EFD00h	The value used to construct MAC addresses for the Locally Unique N_Port_ID protocol.	7.4
ADV_TOV	2 sec	The interval within which solicited Discovery Advertisements are transmitted, if the FCF or FDF chooses so, upon reception of a Discovery Solicitation.	7.11.3
FKA_ADV_PERIOD	-	The interval in milliseconds between periodic Discovery Advertisements and ENodes FIP Keep Alive frames. The default value is 8 000. Valid values are in the range 250 to 90 000.	7.11.3, 7.11.5
FKA_VN_PERIOD	900 sec	The interval between periodic VN_Port FIP Keep Alive frames.	7.11.5
PROBE_WAIT	100 ms	The interval used to probe a Locally Unique N_Port_ID.	7.11.6.2
ANNOUNCE_WAIT	400 ms	The interval used to claim a Locally Unique N_Port_ID.	7.11.6.2, 7.11.6.3
RATE_LIMIT_INTERVAL	10 000 ms	The interval between new VN2VN ENode Locally Unique N_Port_ID probes after detecting greater than 10 Locally Unique N_Port_ID conflicts.	7.11.6.2.1
BEACON_PERIOD	8 000 ms	The interval between N_Port_ID Beacons and N_Port_ID P2P Beacons.	7.11.5.4, 7.11.6.2.2, 7.11.6.3.1
a) This value has been chosen as appropriate to keep the forwarding tables of intermediate Lossless Ethernet bridges updated.			

7.13 FC-BB_E Link Error Status Block (LESB) definition

FC-FS-4 specifies a Link Error Status Block (LESB) to monitor link error statistics that are useful for problem determination in Fibre Channel topologies. The LESB may be obtained from an FC-BB_E VN_Port using the Read Link Error Status Block (RLS) ELS request (see FC-LS-3).

An FC-BB_E VN_Port or VF_Port that supports the LESB with the FC-BB_E format (see Table 22) shall provide this LESB format in response to an RLS ELS request. If an FC-BB_E VN_Port or VF_Port that is the designated FC_Port of an RLS request sequence does not support any of the fields specified in Table 54, then the recipient of the RLS request sequence shall reply to the RLS request with an LS_RJT specifying a reason code of "Unable to perform command request" (i.e., 09h) and a reason code explanation of "Request not supported" (i.e., 2Ch) (see FC-LS-3). A subset of the counters in the LESB may be supported and any unsupported counters shall be set to zero.

Table 54 specifies the format for the FC-BB_E LESB that shall be used in response to an RLS request by an FC-BB_E VN_Port or VF_Port.

Table 54 – FC-BB_E Link Error Status Block format

Bit Word	3	3	2	2	2	2	2	2	2	2	2	1	1	1	1	1	1	1	1	1	0	9	8	7	6	5	4	3	2	1	0
0	Link Failure Count																														
1	Virtual Link Failure Count																														
2	Missing FIP Keep Alive or Discovery Advertisement Count																														
3	Symbol Error During Carrier Count																														
4	Errored Block Count																														
5	Frame Check Sequence Error Count																														

The Link Failure Count field indicates the number of link failures detected through detection of physical link transitions (i.e., the number of times that the aMediaAvailable attribute (see IEEE 802.3-2012 subclause 30.5.1.1.4) changes from the enumeration "available" to any other enumeration.

The Virtual Link Failure Count field indicates the number of Virtual Link failures detected by the Virtual Link maintenance protocol.

The Missing FIP Keep Alive or Discovery Advertisement field indicates the number of missing Virtual Link maintenance protocol frames. A missing Virtual Link maintenance protocol frame is detected after 1.5 times FKA_ADV_PERIOD since the reception of the last Virtual Link maintenance protocol frame. For an ENode, the Missing FIP Keep Alive or Discovery Advertisement field indicates the number of missing Discovery Advertisements. For a VF_Port, the Missing FIP Keep Alive or Discovery Advertisement field indicates the number of missing FIP Keep Alive frames from an ENode.

The Symbol Error During Carrier Count field indicates the number of reception errors at the PHY layer that occur during frame reception. The detection procedure is dependant on media and link speed (see IEEE 802.3-2012 subclause 30.3.2.1.5).

The Errored Block Count field indicates the cumulative count of the events counted by the eight-bit errored blocks counter (see IEEE 802.3-2012 subclause 45.2.3.14.4).

The Frame Check Sequence Error Count field indicates the number of Ethernet frames received that are an integral number of octets in length and do not pass the FCS check (see IEEE 802.3-2012 subclause 30.3.1.1.6).

7.14 Link incidents definition

The Link Incident reporting procedure (see FC-LS-3) defines link incidents and corresponding incident codes that are based on the Fibre Channel physical layer. A definition of FC-BB_E Link Incidents and their respective Incident Code values (see Table 55) are based on the IEEE 802.3 physical layer and the FC-BB_E Virtual Link maintenance protocol.

Bit-error rate thresholding (see FC-FS-4) defines error intervals as a time period during which Fibre Channel invalid Transmission Words are recognized. For FC-BB_E devices an error interval is a time period during which one or more error blocks (see IEEE 802.3-2012 subclause 45.2.3.14.4) are recognized.

Table 55 specifies the Link Incidents for FC-BB_E that shall be used in a RLIR ELS request by an FC-BB_E VN_Port, VF_Port, or VE_Port.

Table 55 – FC-BB_E Link Incidents

Value	Meaning
00h	Reserved
01h	Implicit incident: A condition, caused by an event known to have occurred within the incident port, has been recognized by the incident port. The condition affects the attached link in such a way that it may cause a link incident to be recognized by the connected port.
02h	Bit-error-rate threshold exceeded: The incident port has detected that the Error Interval Count equals the Error Threshold (see FC-FS-4) where the Error Interval Count is based on errored blocks (see IEEE 802.3-2012 subclause 45.2.3.14.4).
03h	Link Failure - Loss-of-Signal: The aLoseMediaCounter has been incremented for entering an aMediaAvailable state indicating anything other than a remote fault (see IEEE 802.3-2012 subclause 30.5.1.1.4 and IEEE 802.3-2012 subclause 30.5.1.1.5).
04h	Link Failure - Remote fault: The aLoseMediaCounter has been incremented for an aMediaAvailable state being entered indicating a remote fault (see IEEE 802.3-2012 subclause 30.5.1.1.4 and IEEE 802.3-2012 subclause 30.5.1.1.5).
05h	Link Failure - Virtual Link failure: The incident port has detected a Virtual Link failure using the Virtual Link maintenance protocol.
06h to FFh	Reserved

7.15 Distributed FCF operations

A Distributed FCF operates as a Distributed Switch (see FC-SW-6), with the only difference that FIP discovery is performed before instantiating Virtual Links. This enables Controlling FCFs and FDFs to instantiate VA_Port to VA_Port Virtual Links only with the discovered FDFs that are directly reachable and part of the FDF_Set (i.e., VA_Port to VA_Port Virtual Links are not established with FDFs that are not part of the FDF_Set, see 7.11.4.4).

Annex A (informative)

FC-BB_GFPT Interoperability Guidelines

A.1 GFPT-specific interoperability guidelines

To ensure interoperability, it is recommended that GFPT-specific implementations for FC-BB_GFPT devices should include the option of:

- a) using no optional Payload FCSs (see ITU-T Rec. G.7041/Y.1303);
- b) using a null Extension Header (see ITU-T Rec. G.7041/Y.1303);
- c) not propagating Client Signal Fail via Client Management Frames (see ITU-T Rec. G.7041/Y.1303); and
- d) having the number of superblocks per GFPT frame not exceed twice the minimum number of superblocks per GFPT frame as recommended in ITU-T Rec. G.7041/Y.1303.

Annex B (informative)

FCoE and FIP Frame Examples

B.1 Overview

An example format of an FCoE frame is specified in Table B.1.

Table B.1 – FCoE frame format example

Bit Word	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0				
0	(MSB)																Destination Address																			
1																	(LSB)				(MSB)															
2	Source Address																(LSB)																			
3	IEEE 802.1Q Tag																																			
4	Type=FCoE_TYPE																Version				Reserved															
5	Reserved																																			
6	Reserved																																			
7	Reserved																								SOF											
8	(MSB)																Encapsulated FC Frame (n words)																			
n+7																																	(LSB)			
n+8	EOF								Reserved																											
n+9	FCS																																			

NOTE 43 – Additional IEEE 802.1 defined tags may be present in an FCoE frame.

An example format of a FIP frame is specified in Table B.2.

Table B.2 – FIP frame format example

Bit Word	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	(MSB)																Destination Address															
1	(LSB)																(MSB)															
2	Source Address																(LSB)															
3	IEEE 802.1Q Tag																															
4	Type=FIP_TYPE																Version				Reserved											
5	(MSB)																															
n+4	Encapsulated FIP operation (n words)																(LSB)															
n+5	FCS																															

NOTE 44 – Additional IEEE 802.1 defined tags may be present in a FIP frame.

Annex C (informative)

Increasing FC-BB_E Robustness Using Access Control Lists

C.1 Overview

In Fibre Channel Fabrics, Fibre Channel switches are generally considered trusted devices. Fibre Channel end devices log into the switch to which they are attached before they may communicate with other end devices that are attached to the Fabric. Given that Fibre Channel links are point-to-point, the Fibre Channel switch has complete control over the traffic an end device injects into the Fabric or is received from the Fabric. As a result, the switch may enforce zoning configurations, ensure end devices are using their assigned addresses, and prevent various types of anomalous behaviors, both erroneous and malicious.

FCoE provides increased flexibility, but with this flexibility new challenges arise in assuring highly robust Fabrics. Specifically, if Ethernet bridges exist between an ENode and an FCF, the point-to-point assurance between the ENode and FCF is lost. Thus the FCF does not have the complete control that a Fibre Channel switch has.

Equivalent robustness between FCoE and Fibre Channel may be achieved by ensuring that all FCoE traffic to and from an ENode passes through an FCF and that if multiple ENodes access an FCF through a single physical FCF port, those ENodes use their assigned MAC addresses. Doing so, in effect, creates the equivalent of a point-to-point link between an ENode and FCF.

Note that the above are necessary, but not sufficient, conditions to ensure equivalent robustness. See annex D for a complete discussion on achieving equivalent robustness.

A possible method of achieving this robustness is to ensure every ENode is physically connected to an FCF with no intervening Ethernet bridges, but in many deployments this is not practical.

Ethernet bridges commonly provide a feature called Access Control Lists (ACLs). Properly configured ACLs may emulate a point-to-point link by providing the traffic enforcement previously discussed. Furthermore, the FIP protocol has been designed to enable Ethernet bridges to efficiently monitor FIP frames passing through them. This data facilitates the automatic configuration of these ACLs. In addition, the automatic configuration is possible independent of any other ACLs that may be in use in the network for other applications.

This annex discusses the ACLs, the required Access Control Entries (ACEs) within the ACL to provide FCoE with equivalent robustness as Fibre Channel, and the process of generating these ACEs automatically by examining FIP frames.

The particular set of ACEs to be used depend on the location of the port within the network and the traffic that is administratively configured to pass through it. Figure C.1 illustrates a network along with the potential different ACEs that are applicable.

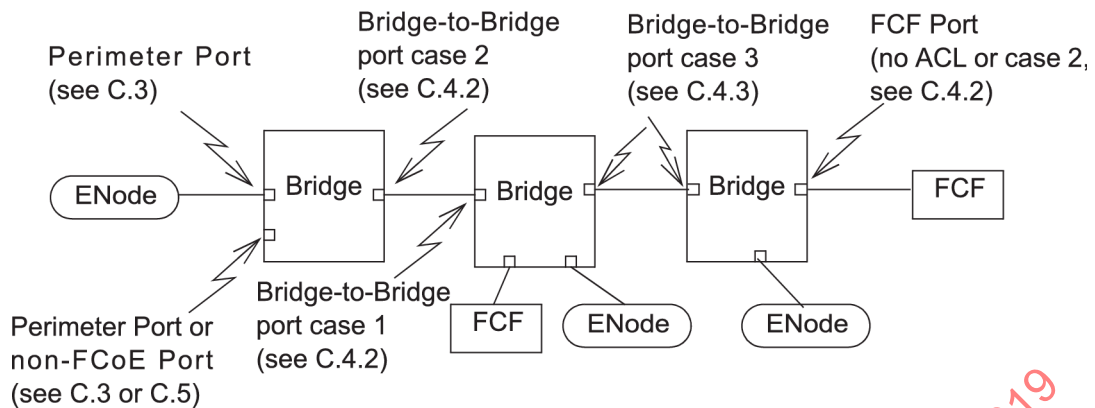


Figure C.1 – Bridge port to ACE cross reference

C.2 Access Control Lists

C.2.1 ACL overview

The implementation of ACLs is not standardized and specifics vary among Ethernet bridges. However, certain features are available from a wide variety of Ethernet bridges.

In general, an Access Control List consists of an ordered list of rules that determine whether a given frame should be forwarded (i.e., “permit”) or discarded (i.e., “deny”). Each rule is specified by matching bits within the received frame to a specified pattern. The pattern may require that the bits are set to one, to zero, or to don’t care. If a frame matches multiple patterns within the ACL, then the first matching ACE determines whether the frame is permitted or denied. A default permit or deny may be specified in the last entry to cover the case in which no patterns match.

Most ACL implementations allow specification of ACLs per bridge port and operate on frames as they enter the bridge using the ACL specified for the ingress port (i.e., an ingress ACL). Some implementations may also apply ACLs to frames as they exit from the bridge (i.e., an egress ACL). For the purpose of this annex, all ACLs are assumed to be ingress ACLs.

It is recommended that ACL protection be applied at the edge of the network (i.e., at the ports that connect directly to the ENodes). The ACLs provided in this annex are intended to be applied to these ports. It is also possible to construct ACLs that provide a lower level of protection on bridge-to-bridge ports. This annex also provides suggested ACLs for this purpose.

In addition, ACL implementations vary in how deeply into a frame the patterns may be applied. For the purpose of this annex, it is only necessary to examine the Source and Destination MAC address fields, the VLAN tag, and the Ethernet Type fields. Most ACL implementations are capable of this.

Implementations vary as to whether bridge learning is subject to the ingress ACL. This annex assumes that bridge learning is subject to the ACL (i.e., if a frame is denied by the ACL, its source address is not learned). For implementations that learn source addresses of denied frames, a simple extension using Static Forwarding Entries (see IEEE 802.1D-2004) is discussed to provide equivalent functionality (see C.7).

C.2.2 ACL nomenclature

The exact method of specifying ACLs is implementation specific. A generalized nomenclature is used in this annex. ACLs consist of an ordered list of access control entries. In general, an access control entry (ACE) has the form of:

`[field = value],[field = value],...,permit || deny;`

The last ACE may contain only the keyword “permit” or “deny” to cover the case that no ACEs match.

The fields used in this annex are:

- a) DA: destination MAC address (48 bits);
- b) SA: source MAC address (48 bits);
- c) SApri: 24 most significant bits of the Source MAC address (24 bits);
- d) VLAN: value of the VLAN field within the VLAN tag (12 bits); and
- e) Type: value of the Ethernet Type field (16 bits).

The following constants, defined in Table 53, are used:

- a) FIP_TYPE; and
- b) FCoE_TYPE.

FC-MAP is the 24-bit FPMA address prefix being used on the network (see 7.9).

“{FCFs}” represents the set of FCF-MAC addresses that a given ENode is allowed to connect. For simplicity, a single ACE is illustrated using this set. In general, multiple ACEs may be required to represent all the members of the set.

C.3 Perimeter ACL construction

C.3.1 Perimeter ACL construction overview

The ACL described in this subclause should be used at the perimeter of the network. This includes bridge ports connected to ENodes and unconnected bridge ports. It may also be used on bridge to bridge ports to provide additional security in depth; however, doing so in certain deployments may exceed the ACEs capacity of the bridge. In addition, deploying this ACL on bridge-to-bridge ports may limit the network ability to autonomously respond to link failures. See C.4 for additional options to address these issues on bridge-to-bridge links.

The following are the requirements of the ACL and the subclauses that describe how these requirements are met using ACEs:

- a) enable transmission of FIP frames from ENodes to FCFs (see C.3.2);
- b) ensure that FIP frames from ENodes may only be addressed to FCFs (see C.3.2);
- c) ensure no end device uses an FCF-MAC address as its source (see C.3.3);
- d) prevent transmission of all FCoE frames from an ENode prior to its successful completion of FIP FLOGI (see C.3.4);
- e) after successful completion of FIP FLOGI, ensure that only the FCoE source addresses used by an ENode are the ones assigned by the FCF to that ENode (see C.3.5);
- f) after successful completion of FIP FLOGI, ensure that the assigned FCoE source address is only used for FCoE traffic (see C.3.5) and FIP traffic (i.e., VN_Port FIP Keep Alive frames); and
- g) after successful completion of each FIP FLOGI or FIP NPIV FDISC, ensure that FCoE frames may only be addressed to the accepting FCFs (see C.3.5).

These ACEs are constructed such that if they are inserted prior to any other non-FCoE and non-FIP related ACEs that may be in use, they do not conflict with those ACEs. In addition, these ACEs are constructed such that they do not inhibit non-FCoE and non-FIP traffic (i.e., traffic that does not contain the FCoE or FIP Ethernet Type value and does not utilize an FCoE source MAC address)

C.3.2 FIP frame transmission

An ENode is allowed to send FIP frames to FCFs, and only to FCFs. These frames may be addressed to a specific FCF, or to the All-FCF-MACS group address. ACEs that accomplish this are:

DA = All-FCF-MACs, Type = FIP_TYPE, permit;
 DA = {FCFs}, Type = FIP_TYPE, permit; (see note 45)
 Type = FIP_TYPE, deny;

NOTE 45 – This ACE also allows VN_Port FIP Keep Alive frames.

C.3.3 Prevention of the transmission of frames using an FCF-MAC address for the source address

An ENode is not allowed to transmit frames using an FCF source address. This is necessary to prevent address learning and FCF impersonation attacks. The ACE that prevents this is:

SA = {FCFs}, deny;

C.3.4 Prevention of frames using FCoE Type or FCoE source addresses prior to successful completion of FIP FLOGI

ENodes are not permitted to send any FCoE frames prior to the successful completion of FIP FLOGI. FCoE frames are identified by the Type field being equal to FCoE_TYPE. The ACE to accomplish this is:

Type = FCoE_TYPE, deny;

C.3.5 Enabling traffic after successful completion of FIP FLOGI or FIP NPV FDISC

After successful completion of FIP FLOGI, FCoE traffic between the ENode and the FCF that accepted the FLOGI using the assigned VN_Port MAC address is enabled. The following ACE accomplishes this:

SA = assigned VN_Port MAC address, DA = FCF-MAC address, Type = FCoE, permit;

For proper operation these ACEs are inserted anywhere prior to those in C.3.4 and it may be convenient to simply insert these at the top of the ACL.

C.3.6 Prevention of duplicate VN_Port MAC addresses

Duplicate VN_Port MAC addresses within a FC-BB_E network may lead to various catastrophic failures, including undetected corruption of data, denial of service, and undetected interception of data. Duplicate VN_Port MAC addresses may occur due to network configuration issues and malicious entities on the network. Duplicate VN_Port MAC addresses may be prevented with the use of FPMAs and the appropriate use of ACEs.

With FPMAs, it is possible to identify all VN_Port MAC addresses. The following ACE causes a bridge port to discard any frame with a source address equal to a VN_Port MAC address:

SAPre = FC-MAP, deny;

This entry should be in place in the ACL after the entries specified in C.3.5.